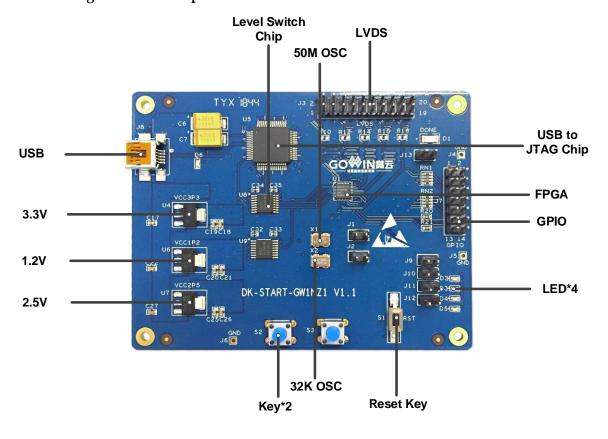


# DK-START-GW1NZ1 Development Board Quick Start User Guide

**Figure 1 PCB Components** 

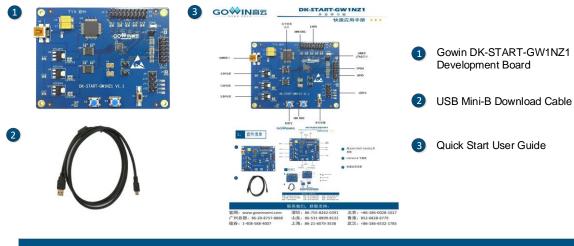


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### Kit List

Figure 2 Development Board Kit



#### Contact Us for Technical Support:

- Website: www.gowinsemi.com.en
- \_ Guangzhou Headquarters: 86-20-8757-8868

Silicon Valley: 1-408-588-4007

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### Introduction

Thank you very much for taking DK-START-GW1NZ1 as the development & learning platform. This user guide can help you install the required software, compile the Demo, and download it to the development board to test so as to be familiar with the development flow.

### **Install Software**

Install Gowin EDA software (Gowin YunYuan Software) to creat, compile and download FPGA Demo program. Download the EDA software, apply for a license, and obtain software user guide at GOWINSEMI website: <a href="https://www.gowinsemi.com/en/support/home/">https://www.gowinsemi.com/en/support/home/</a>. For details on the software installation method and usage, please refer to <a href="SUG100">SUG100</a>, Gowin YunYuan Software User Guide.

# **Development Board Power-on Test**

The test program has been downloaded into the internal FLASH before the delivery of DK-START-GW1NZ1 development board. And the development board can be checked whether to work when it is powered on.

Connect the download cable of the USB Mini-B to the USB interface, the internal FLASH is loaded succefully if the power indicator and DONE light are on. After loading is done, four green LED lights will be blinking, indicating that the development board can work.

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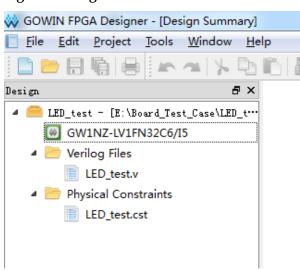
## **Compile Demo Program**

The LED test program is to demonstrate four LEDs blinking. Users can download the corresponding demo at Gowinsemi website:

<u>https://www.gowinsemi.com/en/support/database/</u>. Save the project in the directory with no Chinese characters. Open and compile this demo using Gowin YunYuan software.

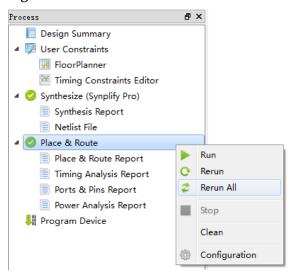
- Open the "LED\_test.gprj" project and the follows are displayed in the "Design" window:
  - GW1NZ-LV1FN32C6/I5: Gowin FPGA device part number;
  - LED test.v: Verilog code;
  - LED\_test.cst: Physical constraints file.

Figure 3 Design



Right click "Place & Route" in the "Process" window and select "Rerun All".

Figure 4 Select Rerun All



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3. After compilation, the following information will be displayed. The generated bitstream file is saved in: ..LED\_test\impl\pnr\LED\_test.fs.

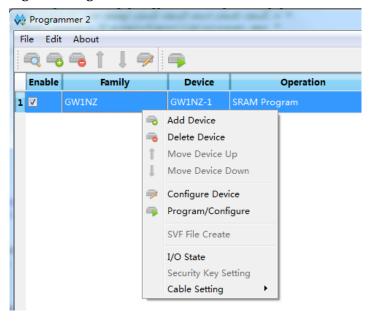
#### **Figure 5 Compiling Completed**

```
Output
          (TA0001) : Timing analysis completed.
Info
Info
          (FS0001) : Bitstream generation in progress.....
Info
          (FS0002) : Bitstream generation completed.
          (PW0001) : Power analysis completed.
Info
          (CM0008) : Generate 'LED_test.power.html' file completed.
Info
          (CM0008) : Generate 'LED_test.tr.html' file completed. (CM0008) : Generate 'LED_test.rpt.html' file completed.
Info
Info
          (CM0008) : Generate 'LED_test.rpt.txt' file completed.
Info
Info
          (CM0001) : Mon Aug 13 09:15:29 2018
         Error Warning Info
 Output
```

### Download and Run

 Connect the development board and PC using the download cable, turn on the power switch, and double-click the Program Device in the "Process" window. The "Programmer" window will pop up, right-click on the device list, and select "Configure Device", the "Device configuration" dialog box will pop up.

#### Figure 6 Programmer

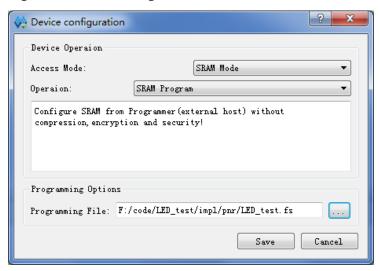


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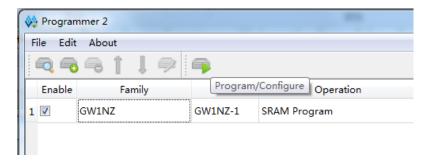
2. Set the download mode as shown below and specify the bitstream file path.

**Figure 7 Device Configuration** 



3. After configuration, click the "Program/Configure" to download the program. After finishing, the four LEDs of the development board will blink simultaneously.

Figure 8 Program/Configure



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# Support and Feedback

Gowin Semiconductor provides customers with comprehensive technical support. If you have any questions, comments, or suggestions, please feel free to contact us directly by the following ways.

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E-mail:<a href="mailto:support@gowinsemi.com">support@gowinsemi.com</a>

Tel: 00 86 0755 82620391

# **Revision History**

Date	Version	Description
02/26/2020	1.0E	Initial version published.

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