

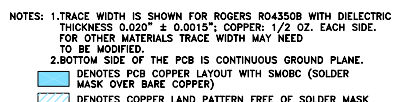
2 Way-0° 50Ω 5500 to 6500 MHz

CASE STYLE F·FV1206-1

Permanent damage may occur if any of these limits are exceeded

PORT 1-2 resistor external 100 OHMS

Suggested Layout,
Tolerance to be within $\pm .002$



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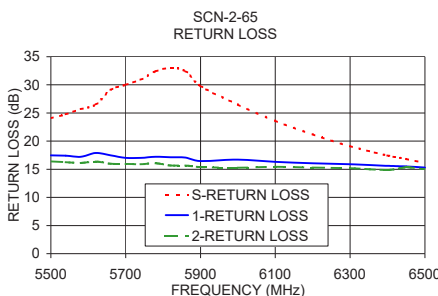
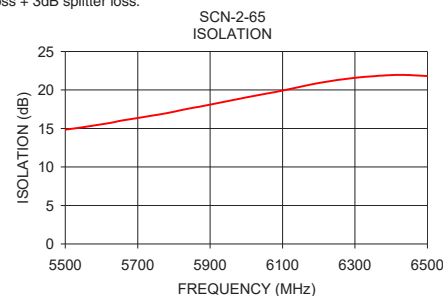
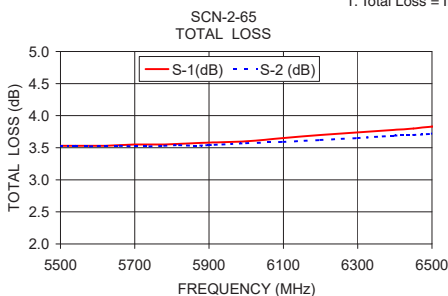
- isolation resistor, external 100 ohms
- low insertion loss, 0.5 dB typ.
- excellent amplitude unbalance, 0.1 dB typ.
- excellent phase unbalance, 2.0 deg. typ.
- high isolation, 17 dB typ.
- excellent power handling, 4W as splitter
- small size, 0.12"X0.06"X0.035"
- ESD non-sensitive
- temperature stable LTCC technology
- wrap around terminations for excellent solderability
- low cost
- protected by US patent 6,967,544

- WLAN
- ISM

FREQUENCY (MHz)	INSERTION LOSS (dB) ABOVE 3.0 dB		ISOLATION (dB)		PHASE UNBALANCE (Degrees)		AMPLITUDE UNBALANCE (dB)		RETURN LOSS (dB)	
	Typ.	Max.	Typ.	Min.	Typ.	Max.	Typ.	Max.	INPUT Typ.	OUTPUT Typ.
	5500-6500	0.8	1.1	17	11	3	5	0.1	0.4	18
5700-5900	0.5	1.0	17	11	2	4	0.1	0.3	22	16

Frequency (MHz)	Total Loss¹ (dB)		Amplitude Unbalance (dB)	Isolation (dB)	Phase Unbalance (deg.)	Return Loss (dB)		
	S-1	S-2				S	1	2
5500.00	3.53	3.52	0.00	14.85	1.24	24.06	17.48	16.39
5540.00	3.53	3.52	0.01	15.10	1.15	24.77	17.41	16.27
5580.00	3.53	3.52	0.01	15.40	1.13	25.71	17.22	16.12
5700.00	3.55	3.52	0.03	16.35	0.93	30.00	17.02	15.95
5740.00	3.55	3.52	0.03	16.68	0.93	30.92	17.03	15.90
5780.00	3.55	3.53	0.03	16.98	0.94	32.42	17.21	16.07
5860.00	3.57	3.53	0.03	17.74	0.83	32.43	17.07	15.68
5900.00	3.58	3.54	0.04	18.11	0.83	29.79	16.46	15.38
6000.00	3.60	3.57	0.04	19.05	0.81	26.48	16.70	15.27
6100.00	3.65	3.59	0.07	19.93	0.70	23.61	16.33	15.44
6200.00	3.70	3.62	0.07	20.91	0.69	21.21	16.06	15.30
6300.00	3.74	3.65	0.08	21.59	0.72	19.11	15.88	15.18
6400.00	3.78	3.69	0.09	21.94	0.67	17.43	15.62	14.88
6450.00	3.80	3.70	0.10	21.93	0.65	16.81	15.51	15.32
6500.00	3.83	3.72	0.11	21.81	0.68	16.15	15.29	15.03

1. Total Loss = Insertion Loss + 3dB splitter loss.



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