LTCC SURFACE MOUNT

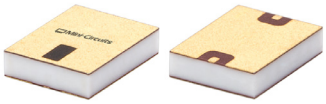
Low Pass Filter

LFCV-1852+

50Ω DC to 18.5 GHz

THE BIG DEAL

- Low Insertion Loss, 1.1 dB Typ.
- Pass Band Return Loss, 15 dB Typ.
- Stop Band Rejection, 40 dB Typ.
- 1210 Surface Mount Footprint
- Power Handling: 4.5 W

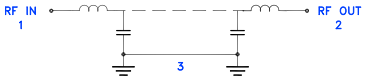


Generic photo used for illustration purposes only

APPLICATIONS

- 5G MIMO and Back Haul Radio Systems
- Test and Measurement Equipment
- Radar, EW, and ECM Defense Systems

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

Mini-Circuits' LFCV-1852+ is a miniature low temperature co-fired ceramic (LTCC) low pass filter with a DC to 18.5 GHz passband that supports a variety of applications. This model provides 1.1 dB typical insertion loss over a wide band due to its rugged monolithic construction. Housed in a 1210 ceramic form factor which is ideal for dense signal chain PCB layouts where it complements MMIC size and performance. The LTCC fabrication process assures minimal RF performance variation while delivering a product that is well suited for environmental extremes of high humidity and temperature.

KEY FEATURES

Features	Advantages
Ultra-wide Stopband	The LTCC lowpass filter provides a very good stopband rejection upto 50 GHz which is suitable for wide band applications.
LTCC Construction	The use of LTCC technology allows for repeatable performance in a rugged ceramic package, well suited for tough environments such as high humidity and temperature extremes. See Mini-Circuits Environmental Rating ENV06T10 for more information.
Small Size, 1210	1210 package allows for space to be saved in dense circuit board layouts, while also minimizing the effects of parasitics.
Rugged Power Handling, 4.5 W	Handles up to 4.5 Watts in a small 1210 package.

REV. OR
ECO-019931
LFCV-1852+
EDU4633
URJ
231213

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ELECTRICAL SPECIFICATIONS[1,2,3] AT +25°C

Parameter		F#	Frequency (GHz)	Min.	Typ.	Max.	Units
Passband	Insertion Loss	DC-F1	DC - 18.5	—	1.1	1.9	dB
	Freq. Cut-Off[4]	Fc	20.25	—	3	—	dB
	Return Loss	DC-F1	DC - 18.5	—	15	—	dB
Stopband	Rejection	F2-F3	23.7 - 26.5	20	38	—	dB
		F3-F4	26.5 - 32	30	40	—	
		F4-F5	32 - 50	—	34	—	

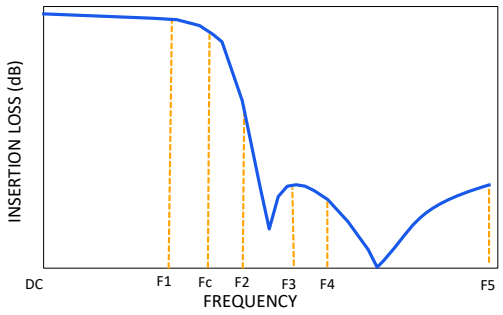
1. Tested on Evaluation Board P/N TB-LFCV-1852+
2. Bi-directional RF1 and RF2 ports can be interchanged.
3. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.
4. Typical variation ±5%.

ABSOLUTE MAXIMUM RATINGS[5]

Parameter	Ratings
Operating Temperature	-55°C to +125°C
Storage Temperature	-55°C to +125°C
Input Power[6]	4.5 W @+25°C

5. Permanent damage may occur if any of these limits are exceeded.
6. Power rating applies only to signals within the passband. Power rating above +25°C operating temperature decreases linearly to 1.6 W at +125°C.

TYPICAL FREQUENCY RESPONSE AT +25°C



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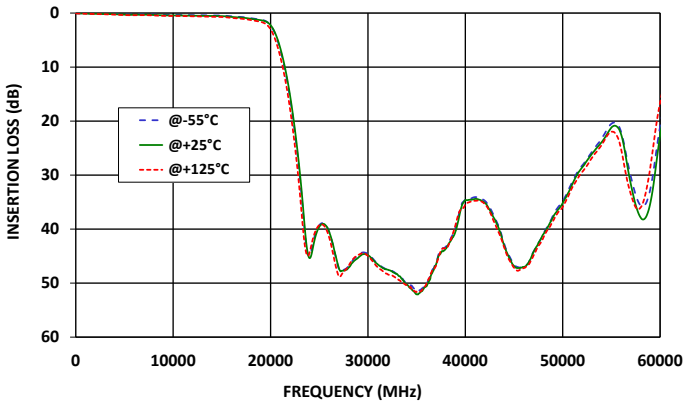
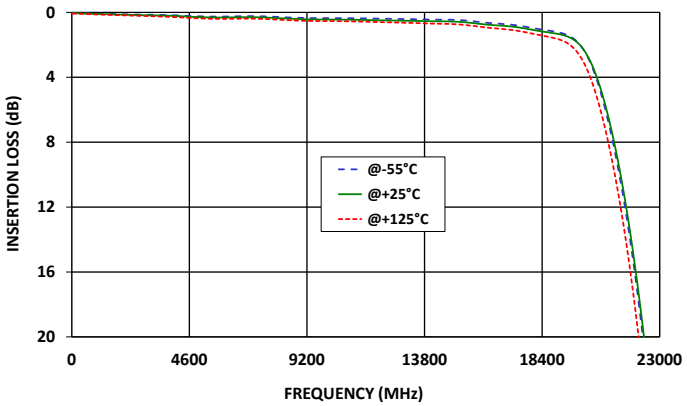
TYPICAL PERFORMANCE GRAPHS

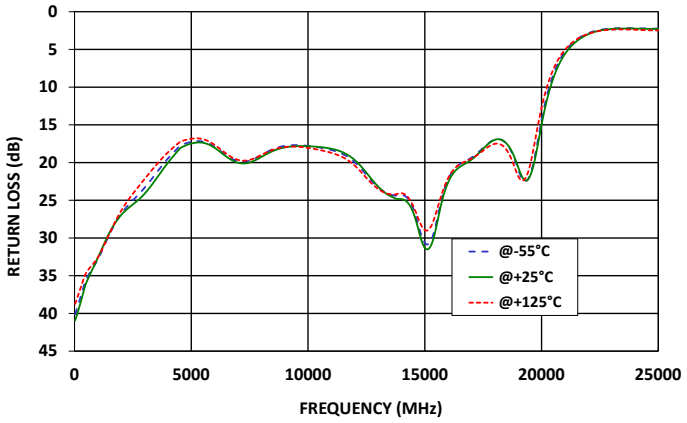
LFCV-1852+
INSERTION LOSS



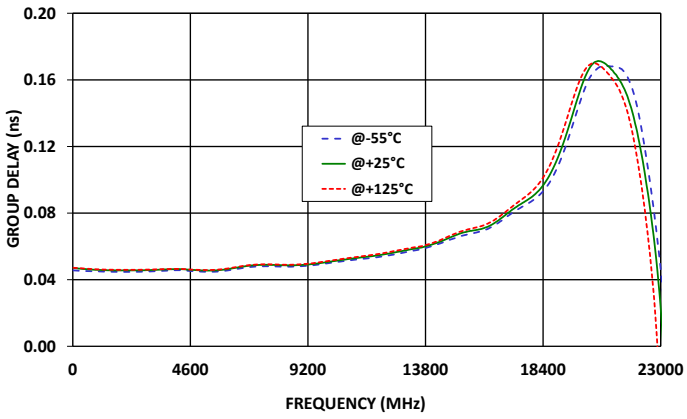
LFCV-1852+
INSERTION LOSS (Zoomed)



LFCV-1852+
RETURN LOSS



LFCV-1852+
GROUP DELAY



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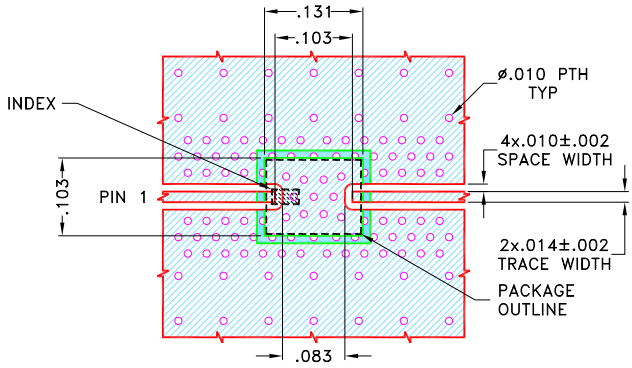
FUNCTIONAL DIAGRAM

Figure 1. LFCV-1852+ Functional Diagram

PAD DESCRIPTION

Function	Pad Number	Description
RF1[2]	1	Connects to RF Input Port
RF2[2]	2	Connects to RF Output Port
GROUND	3	Connects to Ground on PCB, (See drawing PL-679)

SUGGESTED PCB LAYOUT (PL-679)



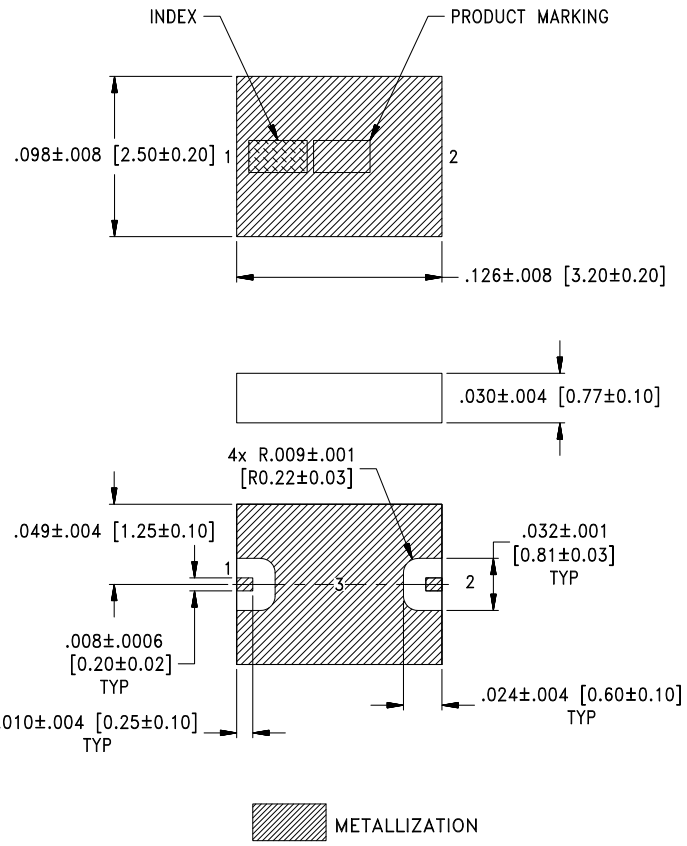
NOTES:
1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS (RO4835 Lo Pro) WITH DIELECTRIC THICKNESS .0073±.0007. COPPER: 1/2 Oz. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER PATTERN WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES PCB COPPER PATTERN FREE OF SOLDERMASK

Figure 2. Suggested PCB Layout PL-679

CASE STYLE DRAWING



Weight: .024 grams

Dimensions are in inches (mm). Tolerances: 2 Pl.±.010; 3 Pl. ±.005

PRODUCT MARKING*: XU

*Marking may contain other features or characters for internal lot control.

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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASH BOARD. CLICK HERE

Performance Data and Graphs	Data Graphs S-Parameter (S2P Files) Data Set (.zip file) De-embedded to device pads
Case Style	JV1210C-13 Lead Finish: Gold over Nickel Plating.
RoHS Status	Compliant
Tape and Reel	F74
Suggested Layout for PCB Design	PL-679
Evaluation Board	TB-LFCV-1852+
	Gerber File
Environmental Rating	ENV06T10

NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits' standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html

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LFCV-1852+