Ceramic

Balance Filter

50Ω 690 to 1570 MHz

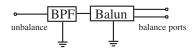
Features

- Small size (0.126"x0.098"x0.039")
- Temperature stable
- · Hermetically sealed
- LTCC construction

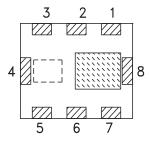
Applications

- ISM
- Cellular

Simplified Schematic



Top View



Pad Connections

Unbalanced Port	7
Balanced Port	3, 5
GND	2, 4, 8
GNC or DC Feed	6
NC	1

BLFCV-1570+



Generic photo used for illustration purposes only

CASE STYLE: JV1210C-4

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications



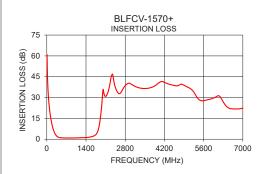
Electrical Specifications at 25°C

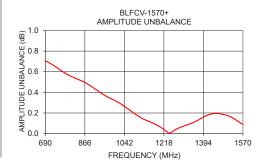
Parameter	F#	Frequency (MHz)	Min.	Тур.	Max.	Unit
Impedance Ratio			2:1			
Insertion Loss	F1-F2	690 - 1570	_	_	3.0	dB
A.L		2200-5050	25	_	_	dB
Attenuation		5050-6000	20	_	_	
Amplitude Unbalance		690 - 1570	_	_	1.5	dB
Phase Unbalance		690 - 1570	_	_	15	degree
Input VSWR		690 - 1570	_	1.78	_	:1

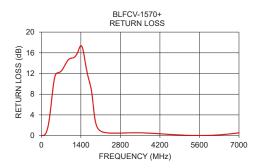
Maximum Ratings

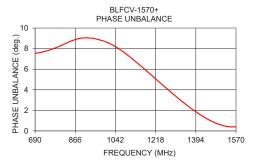
Operating Temperature	-55°C to +105°C
Storage Temperature	-55°C to +105°C
RF Power Input	1W @25°C

* Refer to product storage temperature after installation Suggestion for T&R unused product storage condition: +5 \sim +35 °C, Humidity 45~75%RH, 12 month Max







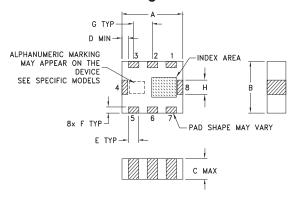


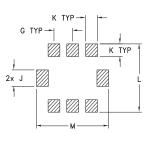


Typical Performance Data

Frequency (MHz)	Insertion Loss (dB)	Return Loss (dB)	Amplitude Unbalance (dB)	Phase Unbalance (Deg,)
10	60.98	0.04	2.72	31.03
50	33.22	0.05	2.49	9.47
100	21.27	0.14	2.27	5.68
690	0.91	12.38	0.71	7.54
1000	0.93	14.80	0.32	8.66
1570	1.67	13.49	0.09	0.41
2000	34.43	1.38	6.11	11.55
2500	34.77	0.51	3.43	27.47
3000	39.84	0.55	3.20	37.34
3500	36.52	0.57	1.36	30.47
4000	40.59	0.45	9.38	85.68
4500	38.92	0.27	0.20	158.12
5000	37.79	0.12	4.09	170.46
5500	27.79	0.05	0.06	65.38
6000	30.01	0.08	10.33	69.21
7000	22.48	0.58	4.00	84.49

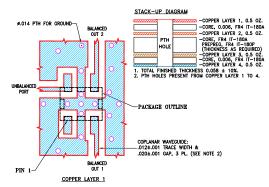
Outline Drawing





Suggested Layout,
Tolerance to be within .002

Demo Board MCL P/N: TB-1053+ Suggested PCB Layout (PL-632)



NOTES:

INDIGS.

1. PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.

2. TRACE WIDTH & CAP PARAMETERS ARE SHOWN FOR FR4 IT-180A WITH DIELECTRIC THICKNESS, 006*±0.007*; COPPER: 1/2 0Z. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.

3. LAYERS 2,3,4 OF THE PCB ARE CONTINUOUS GROUND PLANE.

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

Outline Dimensions (inch)

G	F	E	D	С	В	Α
.039	.012	.022	.004	.039	.098	.126
1.0	0.3	0.56	0.1	1.0	2.5	3.2
wt		M	L	K	J	Н
grams		0.15	.130	.024	.031	.028
0.030		3.81	3.30	0.6	0.8	0.7

Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



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