

RVT28UEFNWC10

LCD TFT Datasheet

Rev.1.0

2020-10-07

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally white	1
Size	2.83	Inch
Viewing Direction	6:00 (without image inversion)	O' Clock
Gray Scale Inversion Direction	12:00	O' Clock
LCM (W \times H \times D)	67.6 x 82.0 x 7.83	mm3
Active Area (W × H)	43.2 × 57.6	mm2
Dot Pitch (W × H)	0.18 × 0.18	mm2
Number of Dots	240 x (RGB) × 320	1
Driver IC	FT801	1
Backlight Type	4 LEDs	1
Surface Luminance	255	cd/m2
Interface Type	SPI	1
Color Depth	262k	1
Pixel Arrangement	RGB Vertical Stripe	1
Surface Treatment	Clear	
Input Voltage	2.8	V
With/Without TSP	Projected Capacitive Touch Panel	1
Weight	TBD	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.

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REVISION RECORD

REVNO.	REVDATE	CONTENTS	REMARKS
1.0	2020-10-07	Initial Release	

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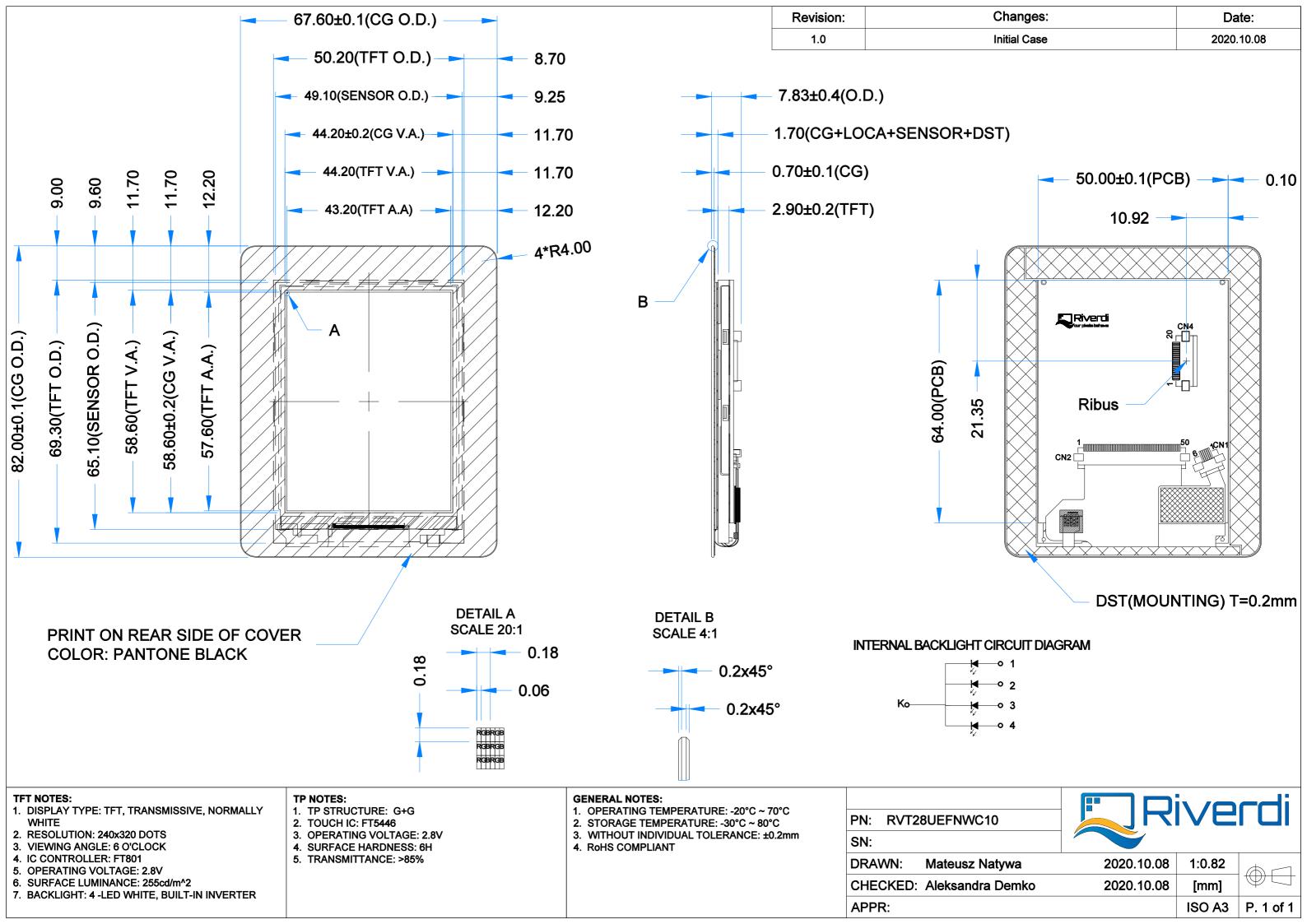
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1 MODULE CLASSIFICATION INFORMATION

RV	Т	28	Ш	Е	F	Ν	W		10
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	28 – 2.83"
4.	MODEL SERIAL NO.	U (A-Z)
5.	RESOLUTION	Е– 240х320 рх
6.	INTERFACE	F – TFT + Controller FT801
7.	FRAME	N – No Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – Capacitive Touch Panel
10.	VERSION	10 (00-99)





3 ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage for Logic	VDD	0	4	V
Supply Voltage for Backlight	BLVDD	-0.3	6	V
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

4 ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Power Supply Voltage for Logic	VDD	2.9	3.3	3.6	V
Power Supply Voltage for Backlight	BLVDD	3.3	-	6	V
Input Current	IDD	-	22	-	mA
Input Voltage ' H ' level	V _{IH}	0.7VDD	-	VDD	V
Input Voltage ' L ' level	VIL	GND	-	0.3VDD	V
Output Voltage ' H ' level	V _{oH}	0.8VDD	-	VDD	V
Output Voltage ' L ' level	V _{oL}	GND	-	0.2VDD	V

5 BACKLIGHT CHARACTERISTICS

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Power Supply Voltage for Backlight	V_{BL}	3.3	-	6	V
Current for LED backlight	lf	72	80	88	mA
Voltage for single LED	V_{f}	-	3.2	3.4	V
LED Life Time	-	30000	40000	-	Hrs

Note:

- **1.**The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C, 60%RH ± 5 %.
- **2.** The life time of LED will be reduced if LED is driven by high current, high ambient temperature and humidity conditions.
- **3.** Typical operating life time is an estimated data.
- **4.** Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded. Functional operation should be restricted to the conditions described under normal operating conditions.

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6 ELECTRO-OPTICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK	NOTE
Response Time		Tr+Tf		-	25	30	ms	Figure 1	4
Contrast Rat	tio	Cr	θ=0°	-	500	-		Figure 2	1
Luminance l	Jniformity	δ WHITE	Ø=0° Ta=25	80	90	-	%	Figure 2	3
Surface Lum	inance	Lv	Tu-25	-	255	-	cd/m ²	Figure 2	2
			Ø = 90°	-	70	-	deg	Figure 3	
		θ	Ø = 270°	-	57	-	deg	Figure 3	
Viewing Ang	gle Range	8	Ø = 0°	-	70	-	deg	Figure 3	6
			Ø = 180°	-	70	-	deg	Figure 3	
	Red	х		-	0.6368	-			
()		У		-	0.3329	-			
CIE (x, y) Chromatici	Green	Х		-	0.3397	-			5
ty		У	θ=0°	-	0.6138	-		Fig 2	5
	Blue	Х	Ø=0° Ta=25	-	0.1433	-	Figure 2		
White		У	14 23	-	0.0807	-			
		х		-	0.2886	-			
		У		-	0.3194	-			
NTSC	-	S	-	55	67	-		%	-

Note 1. Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1

Contrast Ratio = $\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information, see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information, see Figure 2.

 $\delta \, WHITE \, = \, \frac{Minimum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5)}{Maximum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5)}$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information, see Figure 3.

A:5 mm



Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 1. The definition of response time

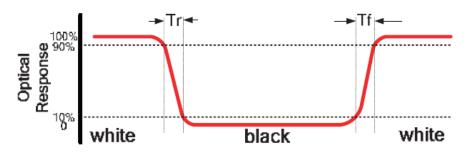


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

B: 5 mm H,V: Active Area Light spot size \varnothing =5mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-5

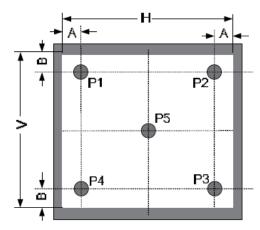
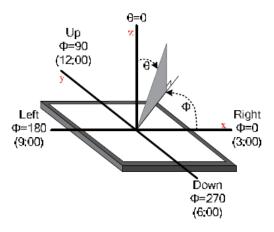


Figure 3. The definition of viewing angle



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7 INTERFACE DESCRIPTION

PIN NO.	SYMBOL	DESCRIPTION
1	VDD	Power Supply
2	GND	Ground
3	SPI_SCLK	SPI SCK Signal, Internally 47k Pull UP
4	MISO	SPI MISO Signal, Internally 47k Pull UP
5	MOSI	SPI MOSI Signal, Internally 47k Pull UP
6	CS	SPI Chip Select Signal, Internally 47k Pull UP
7	INT	Interrupt Signal, Active Low, Internally 47k Pull UP
8	PD	Power Down Signal, Active Low, Internally 47k Pull UP
9	MODE	Host Interface SPI (Pull Low), Internally 10k Pull DOWN
10	AUDIO_OUT	Audio Out Signal
11	NC	Not Connected
12	NC	Not Connected
13	NC	Not Connected
14	NC	Not Connected
15	NC	Not Connected
16	NC	Not Connected
17	BLVDD	Backlight Power Supply, Can Be Connected to VDD
18	BLVDD	Backlight Power Supply, Can Be Connected to VDD
19	BLGND	Backlight Ground, Internally connected to GND
20	BLGND	Backlight Ground, Internally connected to GND

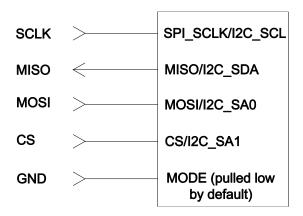


8 FT801 CONTROLLER SPECIFICATIONS

FT801 or EVE (Embedded Video Engine) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object oriented architecture approach that extends from display creation to the rendering of the graphics.

8.1 Serial host interface

Figure 4.SPI interface connection

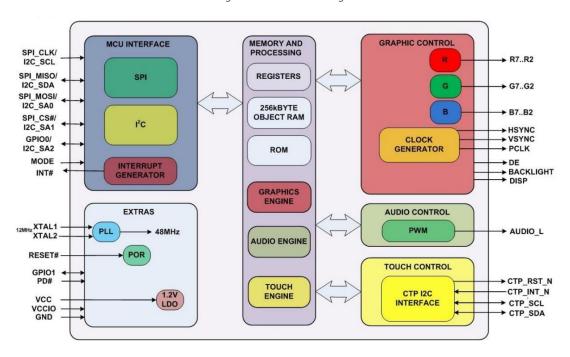


SPI Interface – the SPI slave interface operates up to 30MHz.

Only SPI mode 0 is supported. The SPI interface is selected by default (MODE pin is internally pulled low by 47k resistor).

8.2 Block Diagram

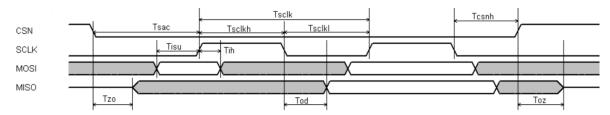
Figure 5. FT801 Block diagram





8.3 Host interface SPI mode 0

Figure 6. SPI timing diagram

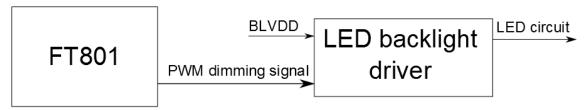


For more information about FT801 controller please go to official FT801 Datasheet. http://www.ftdichip.com/Support/Documents/DataSheets/ICs/DS_FT801.pdf

8.4 Backlight driver block diagram

Backlight enable signal is internally connected to FT801 Backlight control pin. This pin is controlled by two FT801's registers. One of them specifies the PWM output frequency, second one specifies the duty cycle. Refer to FT801 datasheet for more information.

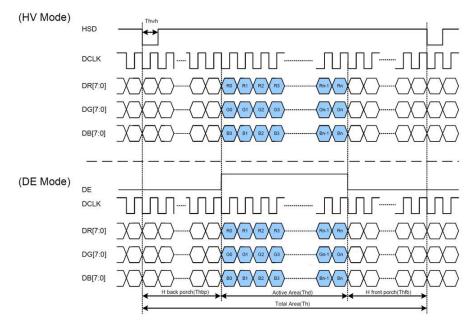
Figure 7. Backlight driver block diagram



9 LCD TIMING CHARACTERISTICS

9.1 Clock and data input time diagram

Figure 8. Clock and data input time diagram





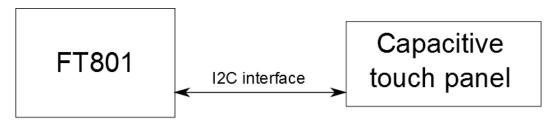
9.2 Parallel RGB input timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK Frequency	Fclk	-	6.35	-	MZH
VSD Period Time	Tv	324	326	320	Н
VSD Display Area	Tvd		320		Н
VSD Back Porch	Tvb	1	2	-	Н
VSD Front Porch	Tvfp	3	4	-	Н
HSD Period Time	Th	244	270	280	DCLK
HSD Display Area	Thd		240		DCLK
HSD Back Porch	Thbp	2	20	24	DCLK
HSD Front Porch	Thfp	2	10	16	DCLK

10 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

Capacitive Touch Panel is directly connected to FT801 module. Therefore communication with Capacitive touch panel is simplified to read registers of FT801.

Figure 9. Capacitive Touch Panel Connection



DESCRIPTION	SPECIFICATION	REMARK
Touch Panel Size	2.83 inch	
Outline Dimension (OD)	67.6mm x 82.0mm	Cover Lens Outline
Product Thickness	1.70mm	
Glass Thickness	0.70mm	
Ink View Area	44.2mm x 58.6mm	
Surface Hardness	≥6H	
Operating Voltage	DC 2.8~3.3V	
Interface	I ² C	
Controller	FT5446	



11 INSPECTION

Standard acceptance/rejection criteria for TFT module.

11.1 Inspection condition

Ambient conditions:

Temperature: 21±4°CHumidity: (50±10) %RH

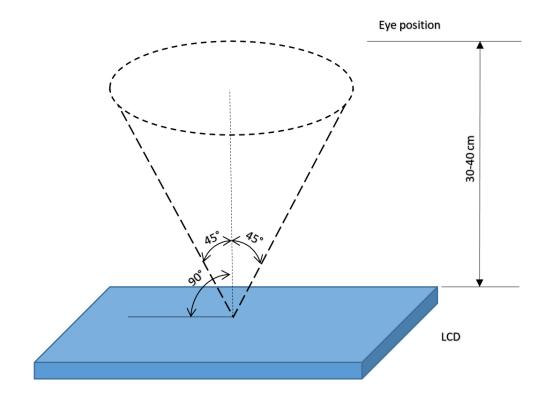
• Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance:

35±5cm between inspector bare eye and LCD.

Viewing Angle:

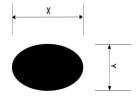
U/D: 45°/45°, L/R 45°/45°





11.2 Inspection standard

Criterion



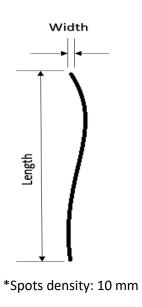
ח	_	(x +	<i>y</i>)	
υ	_		2	

*Spots density: 10 mm

Size < 5"			
Average Diameter	Qualified Qty		
D < 0.2 mm	Ignored		
0.2 mm < D < 0.3 mm	3		
0.3 mm < D < 0.5 mm	2		
0.5 mm < D	0		

Size >= 5"			
Average Diameter	Qualified Qty		
D<0.2 mm	Ignored		
0.2 mm < D < 0.3 mm	4		
0.3 mm < D < 0.5 mm	2		
0.5 mm < D	0		

LCD black spots, white spots, light leakage (line Type)



Size < 5"			
Length	Width	Qualified Qty	
-	W< 0.02	Ignored	
L < 3.0	0.02 < W < 0.05	2	
L < 2.5	0.05 < W < 0.08		
-	0.08 < W	0	

Size >= 5"			
Length	Width	Qualified	
		Qty	
-	W< 0.02	Ignored	
L < 3.0	0.02 < W < 0.05	4	
L < 2.5	0.05 < W < 0.08		
-	0.08 < W	0	





Item	Criterion		
Clear spots		Size < 5"	
	Average Diameter	3120 + 3	Qualified Qty
	D < 0.2 mm		Ignored
	0.2 mm < D < 0.3 mm		3
	0.3 mm < D < 0.5 mm		2
	0.5 mm < D		0
		Size >= 5"	
	Average Diameter		Qualified Qty
	D<0.2 mm		Ignored
	0.2 mm < D < 0.3 mm		4
	0.3 mm < D < 0.5 mm		2
	0.5 mm < D		0
	*Spots density: 10 mm		
Polarizer bubbles		C:-	
	Average Diameter	Size < 5"	Qualified Oty
	Average Diameter D < 0.2 mm		Qualified Qty Ignored
	0.2 mm < D < 0.5 mm		3
	0.5 mm < D < 1 mm		2
	1 mm < D		0
	Total Q'ty		3
	Total Q ty		3
		Size >= 5"	
	Average Diameter		Qualified Qty
	D<0.25 mm		Ignored
	0.25 mm < D < 0.5 mm		3
	0.5 mm < D		0
Electrical Dot		C: F"	
Defect	*****	Size < 5"	Qualified Oty
	item Black do defect		Qualified Qty
			2
	Bright dot defect		5
	Total Dot		3
	Size >= 5"		
	item		Qualified Qty
	Black do defect		5
	Bright dot defect		2
	Total Dot		5
			1





Item	Criterion			
Touch panel spot				
		Size < 5"		
	Average Diam	eter	Qualified Qty	
	D < 0.2 mm		Ignored	
	0.2 mm < D < 0	0.2 mm < D < 0.4 mm		
	0.4 mm < D < 0	0.4 mm < D < 0.5 mm		
	0.5 mm < D	0.5 mm < D		
		Size >= 5"		
	Average Diam	eter	Qualified Qty	
	D<0.25 mm		Ignored	
	0.25 mm < D <	: 0.5 mm	4	
		0.5 mm < D		
	0.5 mm < D		0	
Touch panel White Line Scratch		Size < 5"	0	
Touch panel White Line Scratch	2	Size < 5"		
•		Width	Qualified Qty	
•	Length	Width W< 0.02		
•	2	Width	Qualified Qty Ignored	
•	Length - L < 3.0	Width W< 0.02 0.02 < W < 0.05	Qualified Qty Ignored	
•	Length - L < 3.0	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08	Qualified Qty Ignored 2	
•	Length - L < 3.0	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08	Qualified Qty Ignored 2	
•	Length - L < 3.0	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08 0.08 < W	Qualified Qty Ignored 2	
•	Length - L < 3.0 L < 2.5 -	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08 0.08 < W Size >= 5"	Qualified Qty Ignored 2	
•	Length - L < 3.0 L < 2.5 -	Width W< 0.02 0.02 < W < 0.05 0.05 < W < 0.08 0.08 < W Size >= 5" Width	Qualified Qty Ignored 2 0	

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12 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	INSPECTION AFTER TEST
1	High Temperature Storage	80±2°C/96 hours	
2	Low Temperature Storage	-30±2°C/96 hours	
3	High Temperature Operating	70±2°C/96 hours	Inspection after 2~4 hours storage at
4	Low Temperature Operating	-20±2°C/96 hours	room temperature and humidity. The condensation is not accepted. The
5	Temperature Cycle	$-30\pm2^{\circ}$ C ~ 25~ 80 \pm 2°C × 10 cycles (30 min.) (5min.) (30min.)	sample shall be free from defects:
6	Damp Proof Test	60°C ±5°C × 90%RH/96 hours	1. Air bubble in the LCD
7	Vibration Test	Frequency 10Hz~55Hz Stroke: 1.5mm Sweep: 10Hz~150 Hz~10Hz 2 hours For each direction of X, Y, Z	 Seal leak Non-display Missing segments Glass crack
8	Shock Test	Half-sine, wave, 300m/s	
9	Packing Drop Test	Height: 80 cm 1 corner, concrete floor	
11	Electrostatic Discharge Test	C=150pF, R=330 Ω Air: ±8KV 150pF/330 Ω 30 times Contact: ±4KV,20 times	

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13 INFORMATION

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