LCD TFT Module Specification

RVT101HVLNWC00-B

IPS LVDS 10.1” LCD TFT Datasheet

Rev.1.0

2020-10-22

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally black/IPS	/
Size	10.1	Inch
Viewing Direction	Free	/
Outside Dimensions (W × H × D)	257.96 x 168.60 x 9.30	mm^3
Active Area (W × H)	216.96 × 135.60	mm^2
Pixel Pitch (W × H)	0.1695 × 0.1695	mm^2
Resolution	1280 (RGB) × 800	/
Brightness	850	cd/m^2
LCD Interface Type	LVDS	/
Color Depth	16.7M	/
Pixel Arrangement	RGB Vertical Stripe	/
LCD Driver	EK79202B	/
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2312	/
Touch Interface Type	USB /I2C/ Optional UART	/
Bonding Technology	Optical Bonding	/
Weight	TBD	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.

REVISION RECORD

REVNO.	REVDATE	CONTENTS	REMARKS
1.0	2020-10-22	Initial Release	

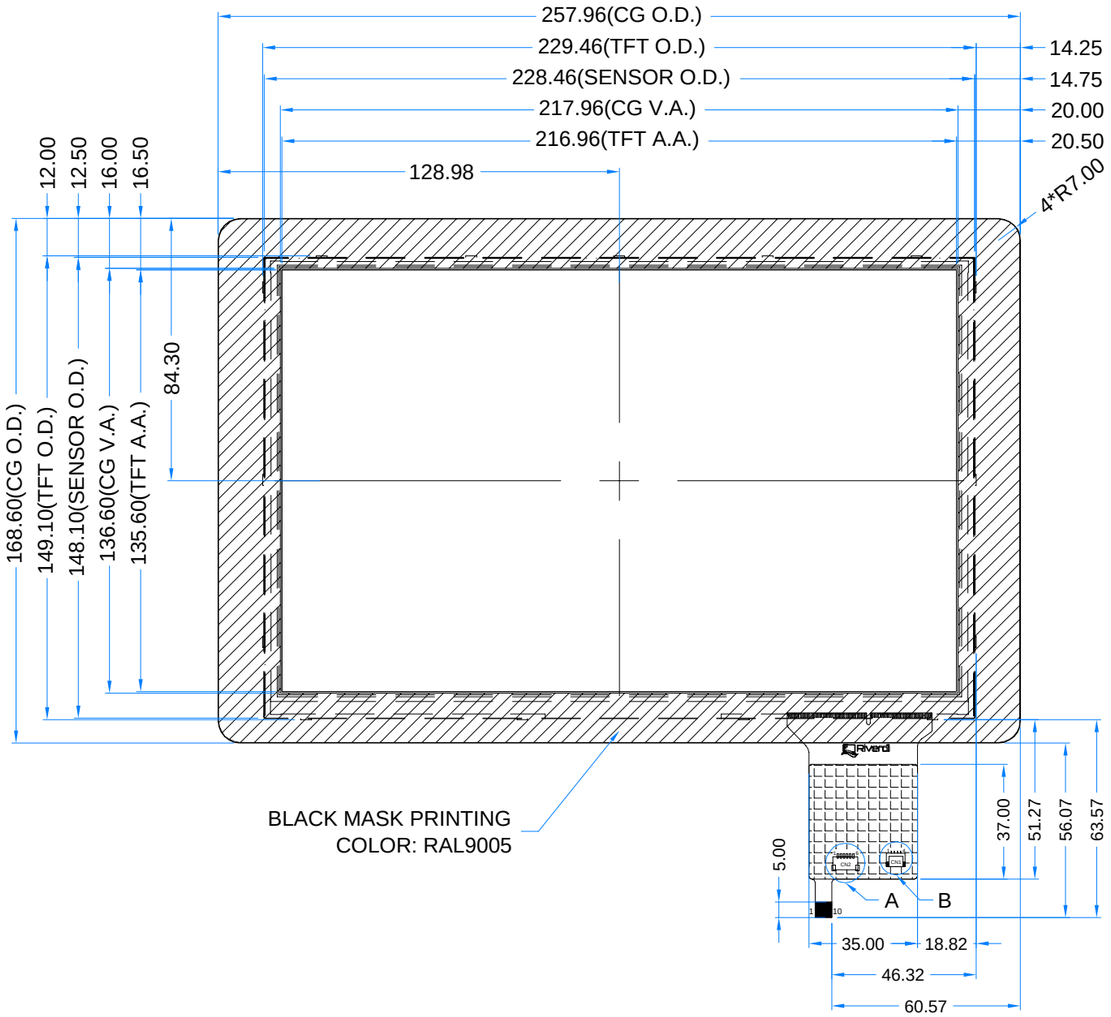
CONTENTS

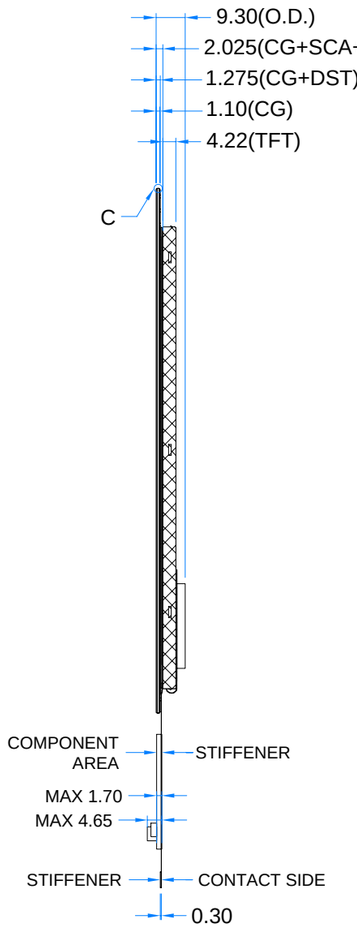
1 MODULE CLASSIFICATION INFORMATION

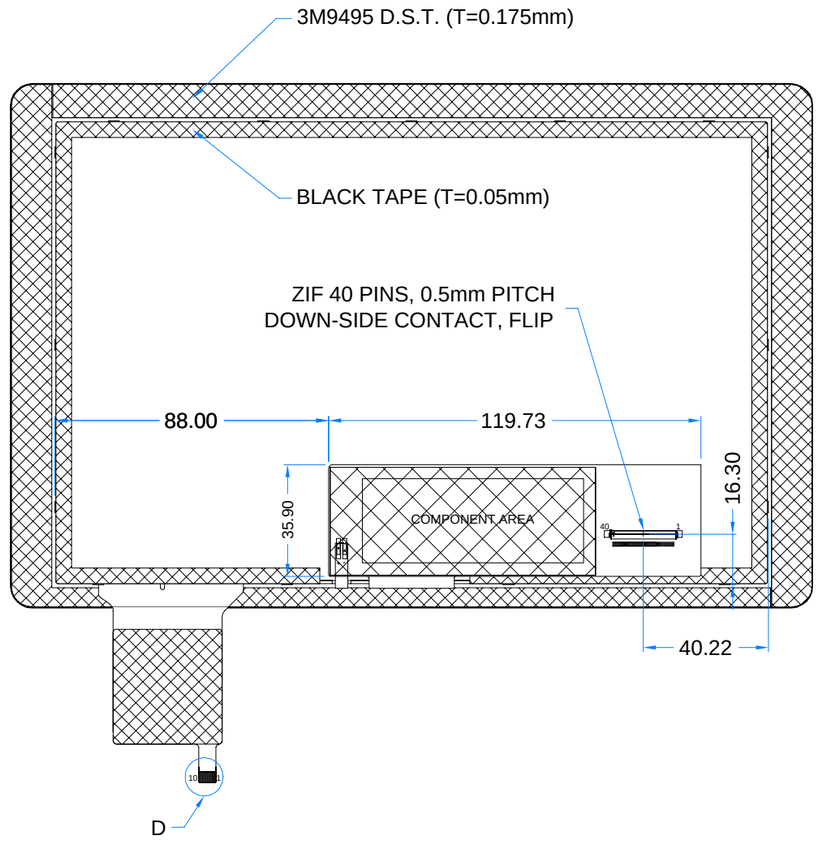
RV	T	101	H	V	L	N	W	C	00	B
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.	11.

1.	**BRAND**	**RV – Riverdi**
2.	**PRODUCT TYPE**	**T – TFT Standard**
3.	**DISPLAY SIZE**	**101 – 10.1”**
4.	**MODEL SERIAL NO.**	**H – High Brightness, IPS**
5.	**RESOLUTION**	**V – 1280 x 800 px**
6.	**INTERFACE**	**L – TFT LCD, LVDS**
7.	**FRAME**	**N – No Frame**
8.	**BACKLIGHT TYPE**	**W – LED White**
9.	**TOUCH PANEL**	**C – With Touch Panel**
10.	**VERSION**	**00 – (00-99)**
11.	**BONDING TECHNOLOGY**	**Optical Bonding**

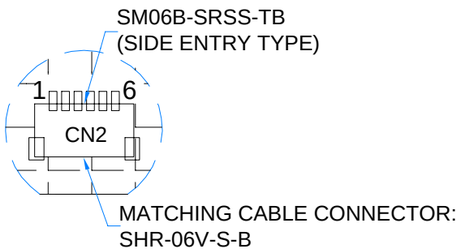
Revision:	Changes:	Date:
1.0	Initial Case	2020.09.08



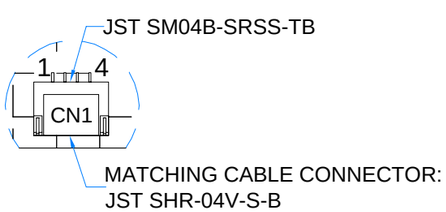




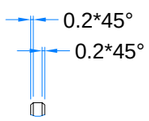
DETAIL A
SCALE 4:1



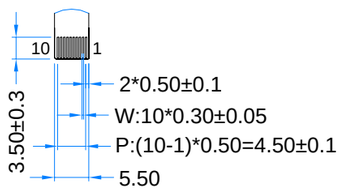
DETAIL B
SCALE 4:1

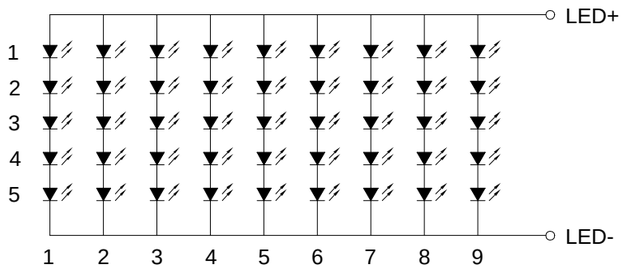


DETAIL C
SCALE 4:1



DETAIL D
SCALE 2:1





LED Diagram Circuit

PINOUT ON THE 2ND PAGE

TFT NOTES:
1. LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
2. RESOLUTION: 1280x800
3. VIEWING ANGLE: FREE
4. DRIVING VOLTAGE: 3.3V
5. BACKLIGHT: 45 LEDS, V_f=16.0V(TYP.), I_f=360mA
6. ZERO BAD PIXEL

TP NOTES:
1. TP STRUCTURE: G+G
2. CG THICKNESS: 1.10mm
3. SURFACE HARDNESS: 7H
4. DRIVER IC: ILI2132A
5. INTERFACE: USB; I2C; OPTIONAL UART
6. OPERATING VOLTAGE: 3.3V(CTP I2C); 5.0V(CTP USB);

GENERAL NOTES:
1. MODULE SURFACE LUMINANCE: 850 cd/m^2
2. OPTICAL BONDING
3. OPERATING TEMPERATURE: -20°C ~ 70°C
4. STORAGE TEMPERATURE: -30°C ~ 80°C
5. WITHOUT INDIVIDUAL TOLERANCE: ±0.3mm
6. RoHS COMPLIANT

PN: RVT101HVLNWC00-B			Riverdi
SN:			
DRAWN: M.Natywa	2020.09.08	1:1.97	
CHECKED: Carol Gao		[mm]	
APPR:		ISO A3	P. 1 of 1

3 ABSOLUTE MAXIMUM RATINGS

The following are maximum values which, if exceeded may cause operation or damage to the unit.

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage	VDD	-0.3	3.9	V
Operating Temperature	T_{OP}	-20	70	°C
Storage Temperature	T_{ST}	-30	80	°C

Note. The absolute maximum rating values are not allowed to be exceeded at any times. a module should be used with any of the absolute maximum ratings exceeds.

The characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

4 ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	VDD	2.6	3.3	3.6	V
Input Voltage ' H ' level	V_{IH}	0.7VDD	-	VDD	V
Input Voltage ' L ' level	V_{IL}	0	-	0.3VDD	V

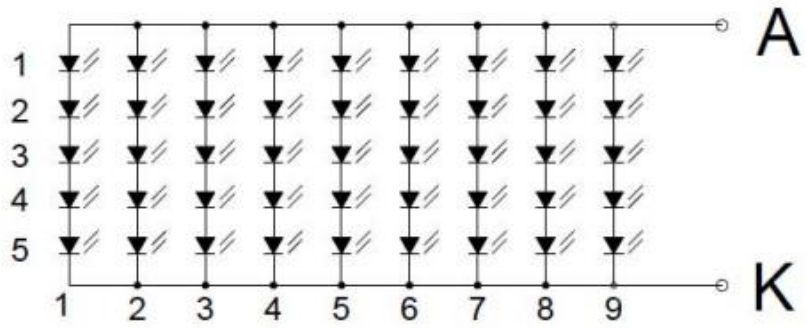
5 BACKLIGHT DRIVING CONDITIONS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Backlight Driving Voltage	VF	15.0	16.0	17.0	V
Backlight Driving Current	IF	315	360	405	mA
Backlight Power Consumption	W_{BL}	-	5760	-	mW
LED Life Time	-	-	50,000	-	Hrs

Note 1. Each LED: IF =40 mA, VF=3.2 ±0.2V.

Note 2. Optical performance should be evaluated at Ta=25 °C only.

Note 3. If LED is driven by high current. High ambient temperature &temperature & humidity condition. The life Time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



6 ELECTRO-OPTICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK	NOTE
Response Time		Tr+Tf	θ=0° Ø=0° Ta=25 °C	-	25	35	ms	FIG 1.	4
Contrast Ratio		Cr		800	1000	-	---	FIG 2.	1
Luminance Uniformity		δ WHITE		-	75	-	%	FIG 2.	3
Surface Luminance		Lv		-	850	-	cd/m²	FIG 2.	2
Viewing Angle Range		θ	Ø = 90°	75	85	-	deg	FIG 3.	6
			Ø = 270°	75	85	-	deg	FIG 3.	
			Ø = 0°	75	85	-	deg	FIG 3.	
			Ø = 180°	75	85	-	deg	FIG 3.	
CIE (x, y) Chromaticity	Red	x	θ=0° Ø=0° Ta=25 °C	0.22	0.26	0.30	FIG 2.		5
		y		0.20	0.24	0.28			
	Green	x		0.34	0.38	0.42			
		y		0.50	0.54	0.58			
	Blue	x		0.10	0.14	0.18			
		y		0.09	0.13	0.17			
	White	x		0.28	0.32	0.36			
		y		0.29	0.33	0.37			

Note 1. Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see Figure 2.

$$\delta\ \text{WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 1. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 3.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 1. The definition of response time

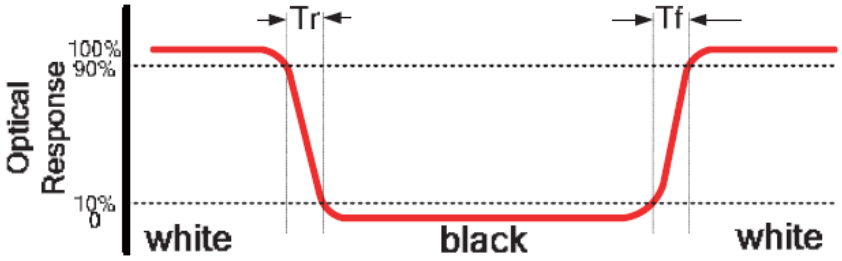


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

A : 5 mm
B : 5 mm
H,V : Active Area
Light spot size ∅=5mm, 500mm distance from the LCD surface to detector lens
measurement instrument is TOPCON's luminance meter BM-5

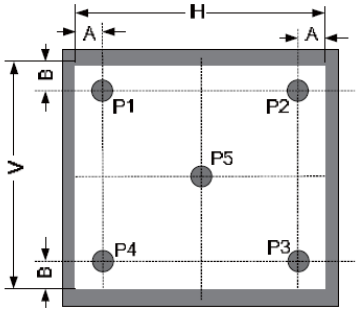
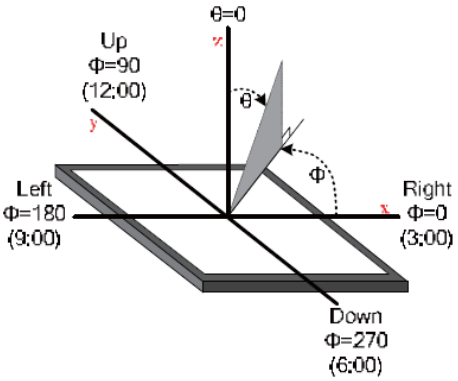
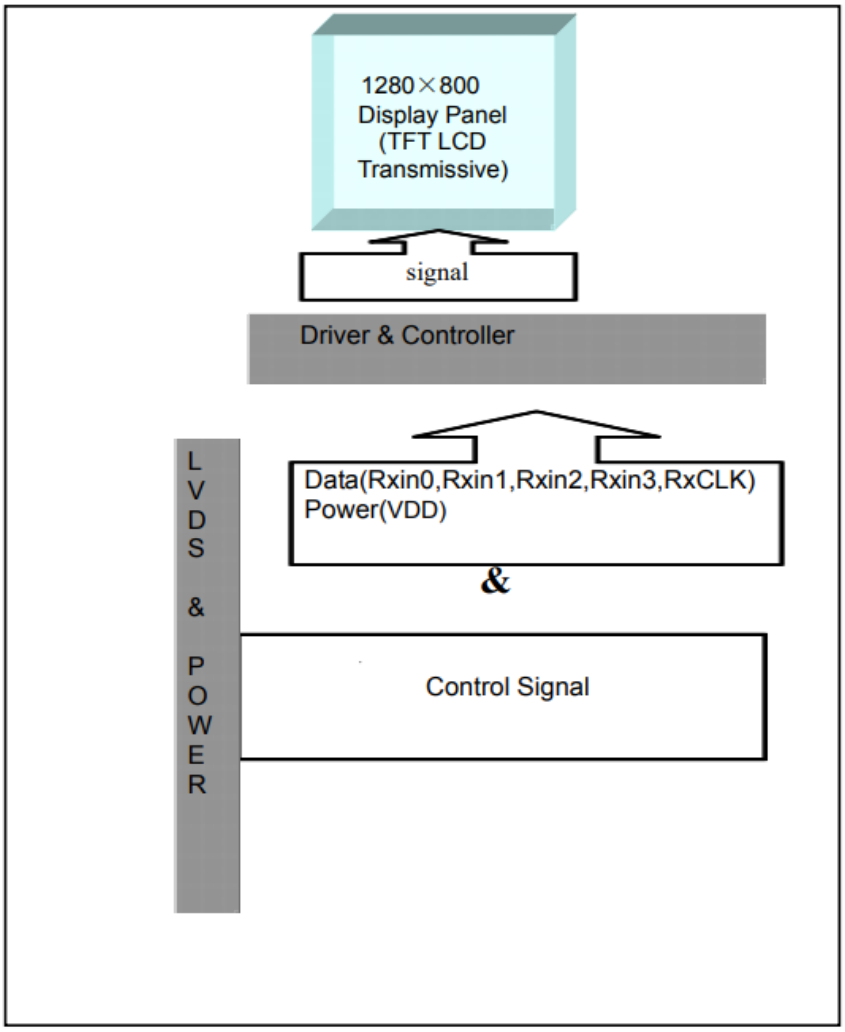


Figure 3.The definition of viewing angle



7 BLOCK DIAGRAM



8 INTERFACE DESCRIPTION

8.1 TFT assignment

PIN NO.	SYMBOL	I/O	DESCRIPTION
1	NC	-	No Connection
2	VDD	P	Power Supply, 3.3V
3	VDD	P	Power Supply, 3.3V
4	NC	-	No Connection
5	NC	-	No Connection
6	NC	-	No Connection
7	GND	P	Ground
8	Rxin0-	I	-LVDS Differential Data Input
9	Rxin0+	I	+LVDS Differential Data Input
10	GND	P	Ground
11	Rxin1-	I	-LVDS Differential Data Input
12	Rxin1+	I	+LVDS Differential Data Input
13	GND	P	Ground

14	Rxin2-	I	-LVDS Differential Data Input
15	Rxin2+	I	+LVDS Differential Data Input
16	GND	P	Ground
17	RxCLK-	I	-LVDS Differential Data Input
18	RxCLK+	I	+LVDS Differential Data Input
19	GND	P	Ground
20	Rxin3-	I	-LVDS Differential Data Input
21	Rxin3+	I	+LVDS Differential Data Input
22	GND	P	Ground
23	NC	-	No Connection
24	NC	-	No Connection
25	GND	P	Ground
26	NC	-	No Connection
27	NC	-	No Connection
28	NC	-	No Connection
29	NC	-	No Connection
30	GND	P	Ground
31	LED-	P	LED Cathode
32	LED-	P	LED Cathode
33	NC	-	No Connection
34	NC	-	No Connection
35	NC	-	No Connection
36	NC	-	No Connection
37	NC	-	No Connection
38	NC	-	No Connection
39	LED+	P	LED Anode
40	LED+	P	LED Anode

Note 1. I: input, O: output, P:Power

8.2 Touch panel assignment

PIN NO.	SYMBOL	DESCRIPTION
1	USB_GND	USB_Ground
2	USB_VDD	USB_Power for CTP, DC 5.0 V
3	USB_D–	USB _Data Signal –
4	USB_D+	USB _Data Signal +
5	I2C_GND	I2C _Power Ground
6	I2C_VDD	I2C _Power For CTP, DC 3.3 V
7	I2C_RST	I2C _Reset Pin
8	I2C_SCL	I2C _Clock Input
9	I2C_INT	I2C _Interrupt Signal from CTP
10	I2C_SDA/TXD	I2C _Data Signal

8.3 CON1 assignment

PIN NO.	SYMBOL	DESCRIPTION
1	USB_VDD	USB_Power for CTP, DC 5.0V
2	USB_D–	USB _Data Signal -
3	USB_D+	USB _Data Signal +
4	USB_GND	USB_ Ground

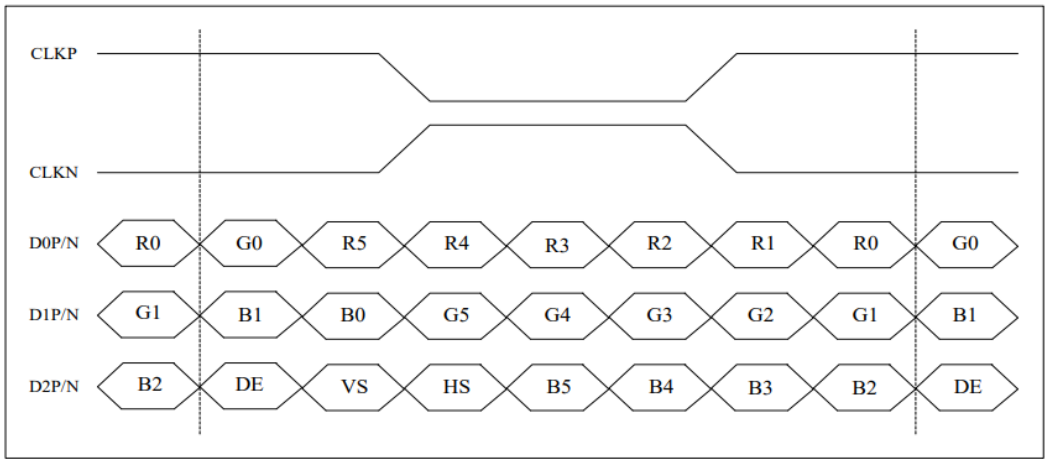
8.4 CON2 assignment

PIN NO.	SYMBOL	DESCRIPTION
1	I2C_GND	I2C _Power Ground
2	I2C_VDD	I2C _Power For CTP, DC 3.3 V
3	I2C_RST	I2C _Reset Pin
4	I2C_SCL	I2C _Clock Input
5	I2C_INT	I2C _Interrupt Signal from CTP
6	I2C_SDA/TXD	I2C _Data Signal

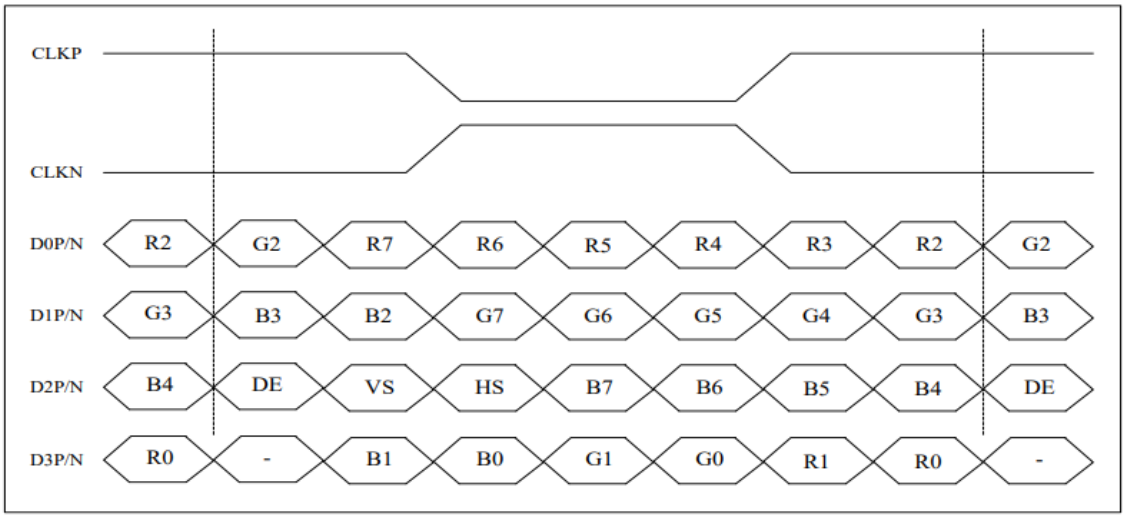
9 TIMING CHARACTERISTICS

9.1 LVDS interface characteristic

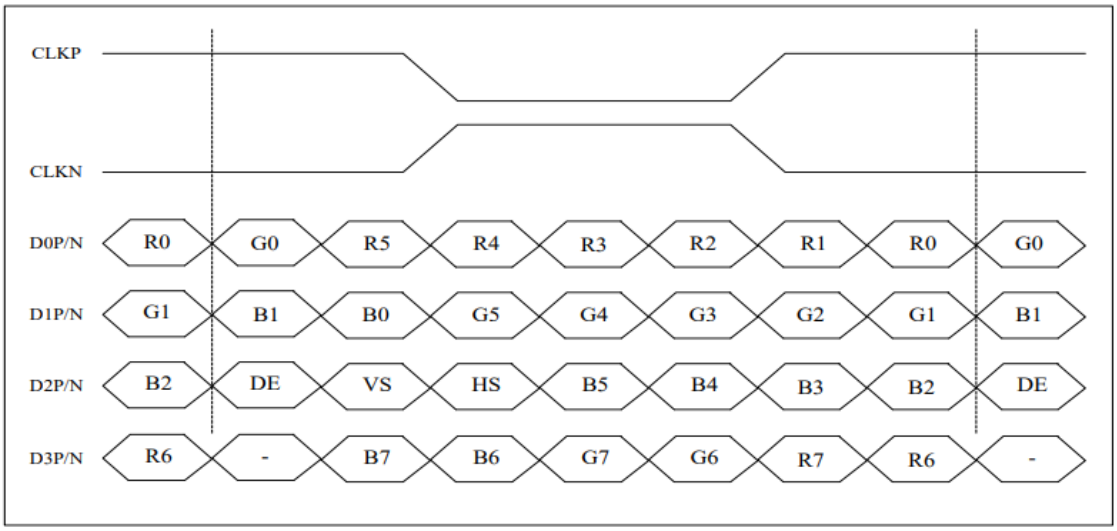
6-bit LVDS input (LVBIT=L, LVFMT=H OR L)



8-bit LVDS input (LVBIT=L, LVFMT=L)



8-bit LVDS input (LVBIT=L, LVFMT=H)



9.2 Timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Clock Frequency (Rate=60Hz(LVDS))	FDCLK	66.3	72.4	78.9	MHz
HSYNC Period Time	T_H	1380	1440	1500	DCLK
Horizontal Display area	T_{HD}		1280		DCLK
Hsync Pluse Width	T_{HPW}	1	-	40	Tc
Hsync Back Porch (with pluse width)	T_{HBP}	88	88	88	DCLK
Hsync Front Porch	T_{HFP}	12	72	132	DCLK
VSYNC Period Time	T_V	824	838	872	H
Vertical Display area	T_{VD}		800		H
Vsync Pluse Width	T_{VW}	1	-	20	H
Vsync Back Porch (with pluse width)	T_{VBP}	23	23	23	H
Vsync Front Porch	T_{VFP}	1	15	49	H

9.3 Reset timing characteristic

When RESETB of the reset pin equals to Low, it will be in the condition of reset. When it is in the condition of reset, it will make the device recover the initial set. However, in order to avoid the reset noise cause reset, there is a mechanism to judge about whether the reset is needed or not.

The closed interval of Low can be shown as the following.

(Test condition: VDDIO=2.3V~3.6V, VSS=0V, TA=-20 ~+85)

PARAMETER	SYMBOL	VALUE			UNIT
		MIN.	TYP.	MAX.	
Reset low pluse width	Trst	20	-	-	us

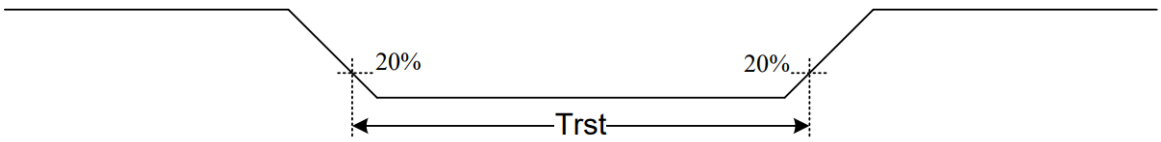
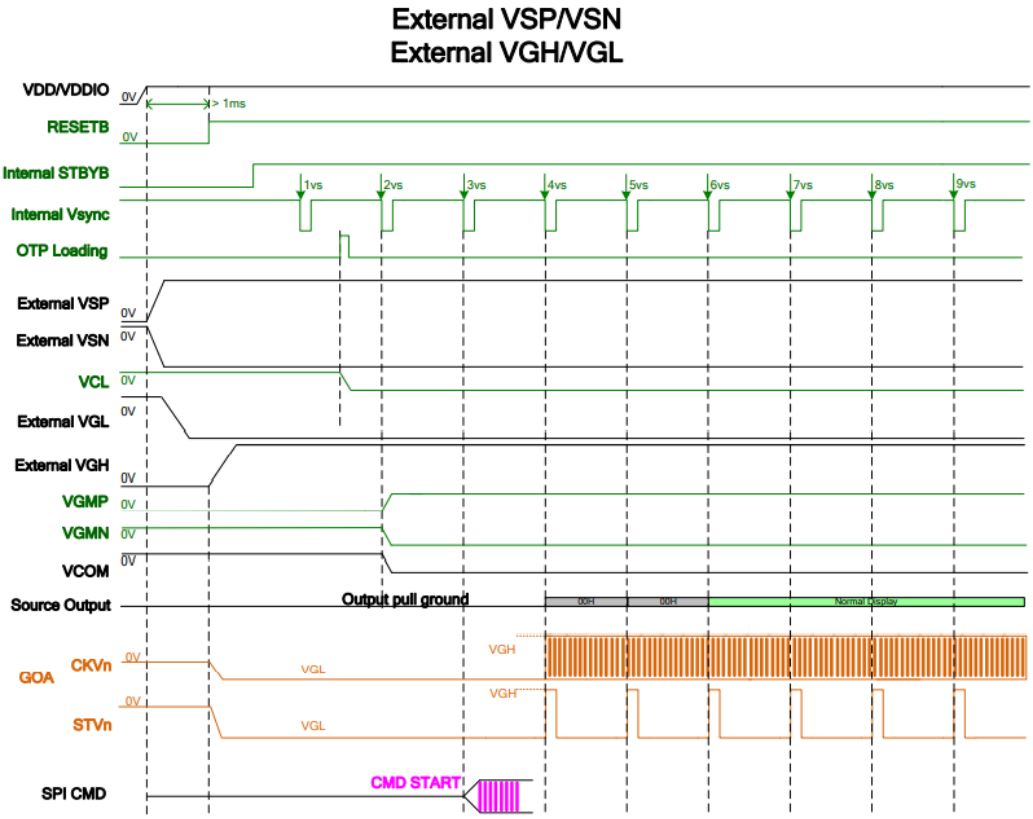


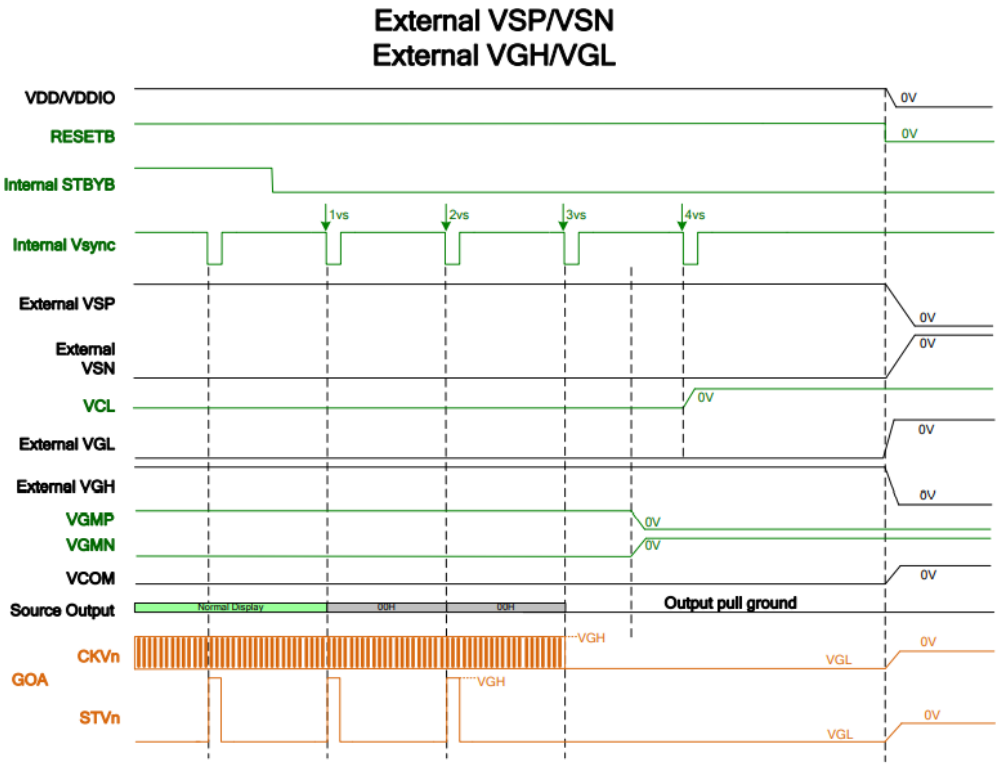
Figure 4 Reset Timing

9.4 Power ON/OFF sequence

9.4.1 Power on sequence with PMODE=H



9.4.2 Power off sequence with PMODE=H



10 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

10.1 Mechanical characteristics

DESCRIPTION	INL SPECIFICATION	REMARK
Touch Panel Size	10.1 inch	UxTouch
Outline Dimension of CTP	257.96 mm x 168.60 mm	UxTouch
Product Thickness	2.35 mm	UxTouch
Glass Thickness	1.1 mm	UxTouch
CTP View Area	217.96 mm x 136.60 mm	UxTouch
Sensor Active Area	228.46mm x 148.10 mm	UxTouch
Surface Hardness	7H	UxTouch

10.2 Electrical characteristics

DESCRIPTION		SPECIFICATION
Operating Voltage		DC 5.0 V (USB)
		DC 3.3 V (I^2C)
Power Consumption (IDD)	Active Mode	90 mA
	Sleep Mode	10 mA
Interface		USB / I^2C /Optional UART
Linearity		+/-1.5mm
Controller		ILI2312A
I2C address		0X82
Resolution		1280 x 800

11 INSPECTION

Standard acceptance/rejection criteria for TFT module.

11.1 Inspection condition

Ambient conditions:

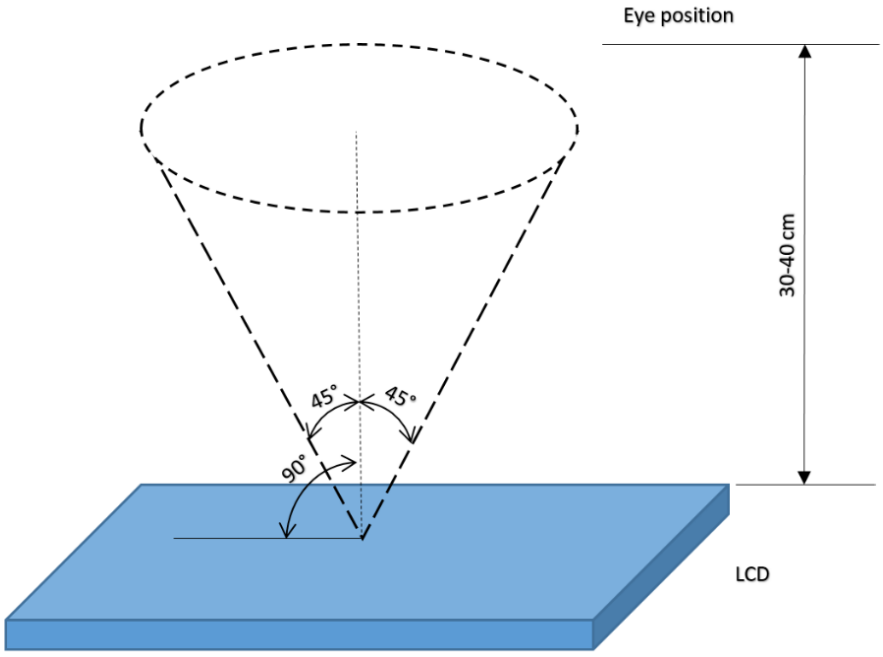
- Temperature: 25±°C
- Humidity: (60±10)%RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance:

35±5cm between inspector bare eye and LCD.

Viewing Angle:

U/D: 45°/45°, L/R 45°/45°



11.2 Inspection standard

The LCD TFT has zero bad pixel. Please refer the item “Bright/Dark dots ”.

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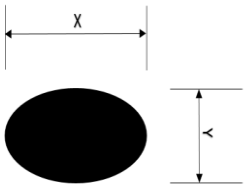

$$D = \frac{(x+y)}{2}$$
*Spots density: 10 mm
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<tr><td>D ≤ 0.2 mm</td><td>Ignored</td></tr>
<tr><td>0.2 mm < D ≤ 0.3 mm</td><td>N≤4</td></tr>
<tr><td>0.5mm < D</td><td>N=0</td></tr>
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<table>
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<tr><td>D ≤ 0.15 mm</td><td>Ignored</td></tr>
<tr><td>0.15 mm < D ≤ 0.30 mm</td><td>N≤3</td></tr>
<tr><td>0.3mm < D</td><td>Not allowed</td></tr>
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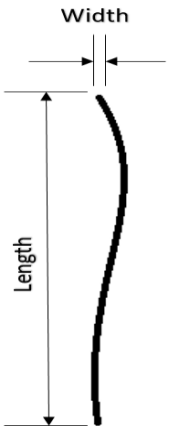
</td></tr>
<tr><td>LCD black spots, white spots, light leakage (line Type)</td><td>


*Spots density: 10 mm
<table>
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<tr><td>-</td><td>W ≤ 0.05</td><td>Ignored</td></tr>
<tr><td>L ≤ 5.0</td><td>0.05< W ≤ 0.1</td><td>N ≤ 3</td></tr>
<tr><td>5.0 < L</td><td>0.10< W or 5.0 < L</td><td>N = 0</td></tr>
</table>
<table>
<tr><th colspan="3">3.5” ≤ Size ≤ 5”</th></tr>
<tr><td>Length/mm</td><td>Width/mm</td><td>Qualified Qty</td></tr>
<tr><td>-</td><td>W≤ 0.03</td><td>Ignored</td></tr>
<tr><td>L ≤ 3.0</td><td>0.03< W ≤ 0.05</td><td>2</td></tr>
<tr><td>L ≤ 3.0</td><td>0.05 < W ≤ 0.1</td><td>1</td></tr>
<tr><td>3.0 < L</td><td>0.1< W</td><td>Not allowed</td></tr>
</table>
</td></tr>
<tr><td>Bright/Dark Dots</td><td>
<table>
<tr><th colspan="2">Size =10.1”</th></tr>
<tr><td>item</td><td>Qualified Qty</td></tr>
<tr><td>Bright Dots</td><td>0</td></tr>
<tr><td>Dark Dots</td><td>0</td></tr>
<tr><td>Cluster Bright Dots or Dark Dots</td><td>0</td></tr>
<tr><td>Total Bright and Dark Dots</td><td>0</td></tr>
</table>
</td></tr>
</table>

<table>
<tr><th>Item</th><th>Criterion</th></tr>
<tr><td>Clear spots</td><td>
<table>
<tr><th colspan="2">Size < 5”</th></tr>
<tr><td>Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td>D < 0.2 mm</td><td>Ignored</td></tr>
<tr><td>0.2 mm < D < 0.3 mm</td><td>3</td></tr>
<tr><td>0.3 mm < D < 0.5 mm</td><td>2</td></tr>
<tr><td>0.5 mm < D</td><td>0</td></tr>
</table>
<table>
<tr><th colspan="2">Size >= 5”</th></tr>
<tr><td>Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td>D<0.2 mm</td><td>Ignored</td></tr>
<tr><td>0.2 mm < D < 0.3 mm</td><td>4</td></tr>
<tr><td>0.3 mm < D < 0.5 mm</td><td>2</td></tr>
<tr><td>0.5 mm < D</td><td>0</td></tr>
</table>
*Spots density: 10 mm
</td></tr>
<tr><td>Touch panel spot</td><td>
<table>
<tr><th colspan="2">Size >= 5”</th></tr>
<tr><td>Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td>D<0.25 mm</td><td>Ignored</td></tr>
<tr><td>0.25 mm < D < 0.5 mm</td><td>4</td></tr>
<tr><td>0.5 mm < D</td><td>0</td></tr>
</table>
</td></tr>
<tr><td>Touch panel white line Scratch</td><td>
<table>
<tr><th colspan="3">Size >= 5”</th></tr>
<tr><td>Length</td><td>Width</td><td>Qualified Qty</td></tr>
<tr><td>-</td><td>W< 0.03</td><td>Ignored</td></tr>
<tr><td>L < 5.0</td><td>0.03 < W <0.05</td><td>2</td></tr>
<tr><td>-</td><td>0.05 < W</td><td>0</td></tr>
</table>
</td></tr>
</table>

<table>
<tr><th>Item</th><th>Criterion</th></tr>
<tr><td>Touch panel spot</td><td>
<table>
<tr><th colspan="2">Size < 5”</th></tr>
<tr><td>Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td>D < 0.2 mm</td><td>Ignored</td></tr>
<tr><td>0.2 mm < D < 0.4 mm</td><td>5</td></tr>
<tr><td>0.4 mm < D < 0.5 mm</td><td>2</td></tr>
<tr><td>0.5 mm < D</td><td>0</td></tr>
</table>
<table>
<tr><th colspan="2">Size >= 5”</th></tr>
<tr><td>Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td>D<0.25 mm</td><td>Ignored</td></tr>
<tr><td>0.25 mm < D < 0.5 mm</td><td>4</td></tr>
<tr><td>0.5 mm < D</td><td>0</td></tr>
</table>
</td></tr>
<tr><td>Touch panel white line Scratch</td><td>
<table>
<tr><th colspan="3">Size < 5”</th></tr>
<tr><td>Length</td><td>Width</td><td>Qualified Qty</td></tr>
<tr><td>-</td><td>W< 0.02</td><td>Ignored</td></tr>
<tr><td>L < 3.0</td><td>0.02 < W <0.05</td><td rowspan="2">2</td></tr>
<tr><td>L < 2.5</td><td>0.05 < W <0.08</td></tr>
<tr><td>-</td><td>0.08 < W</td><td>0</td></tr>
</table>
<table>
<tr><th colspan="3">Size >= 5”</th></tr>
<tr><td>Length</td><td>Width</td><td>Qualified Qty</td></tr>
<tr><td>-</td><td>W< 0.03</td><td>Ignored</td></tr>
<tr><td>L < 5.0</td><td>0.03 < W <0.05</td><td>2</td></tr>
<tr><td>-</td><td>0.05 < W</td><td>0</td></tr>
</table>
</td></tr>
</table>

12 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	REMARK
1	High Temperature Storage	80 °C/120hours	Note 1
2	Low Temperature Storage	-30 °C/120hours	Note 1
3	High Temperature Operating	70 °C /120hours	Note 1
4	Low Temperature Operating	-20 °C/120hours	Note 1
5	High Temperature and High Humidity	40 °C, 90 % RH/120 Hrs	Note 1
6	Thermal Cycling Test (No operation)	-20 ℃ for 30min, 70℃ for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test (No operation)	Frequency :10~55 HZ; Stroke :1.5mm; Sweep:10HZ~55HZ~10HZ; 2hours for each direction of X, Y, Z(6 hours for total)	
8	Package Drop Test	Height:60 cm, 1 corner, 3 edges, 6 surfaces	
10	ESD Test	± 2KV, Human body mode,100pF/1500Ω	

Note 1: Sample quantity for each test item is 5~10pcs.

Note 2: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

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