TFT MODULE SPECIFICATION

RVT35HHBNWC00

EVE4 IPS 3.5" LCD TFT DATASHEET

Rev.1.3
2021-07-30

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	3.5	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	93.96 x 76.44 x 12.25	mm
Active Area (W x H)	70.08 x 52.56	mm
Pixel Pitch (W x H)	0.219 x 0.219	mm
Resolution	320 (RGB) x 240	/
Brightness	800	cd/m^2
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
Driver IC of Board	BT817Q	/
Interface	SPI/QSPI	/
Host Connector	RiBUS, ZIF 20 pin, 0.5mm pitch, down-side contact	/
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Supply Voltage for Module	3.3	V
Supply Voltage for Backlight	5.0 (TYP.)	V
Weight	75	g

Note 1: RoHS3 compliant

Note 2: LCM weight tolerance: ± 5%.

1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2021-04-19	Initial Release	
1.1	2021-06-03	Updating the drawing	
1.2	2021-07-20	Updating the supply voltage for backlight	
1.3	2021-07-30	Updating the drawing (dimensions in inches, adding the speaker), New template	

2. CONTENTS

3. MODULE CLASSIFICATION INFORMATION

RV	T	35	H	H	B	N	W	C	00
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

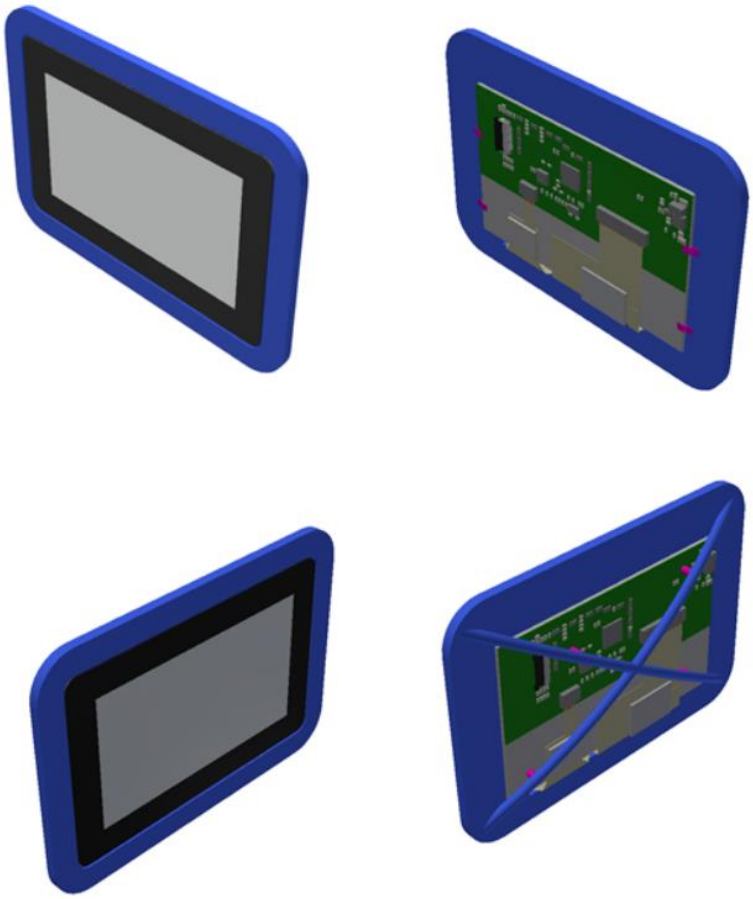
NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	35 – 3.5”
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	H – 320 x 240 px
6.	INTERFACE	B – SPI/QSPI
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel
10.	VERSION	00 – (00-99)

4. uxTouch ASSEMBLY

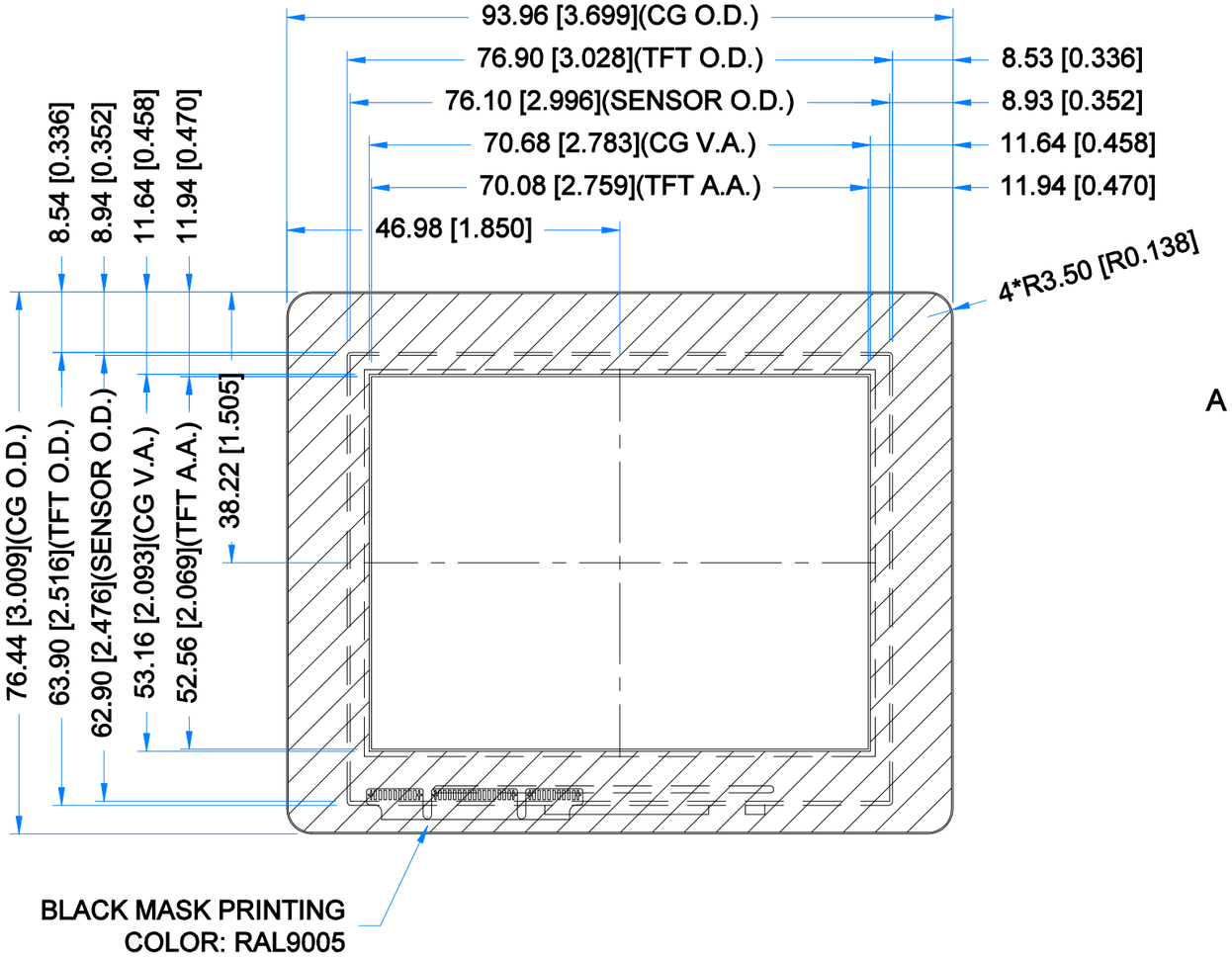
uxTouch are LCD TFT displays with specially designed projected capacitive touch panels. uxTouch display can be mounted without any holed in the housing. Our standard uxTouch displays include double-sided adhesive tape (DST) to stick TFT easily to the housing. Basic series include 4.3”, 5.0”,7.0” and 10.1” display sizes.

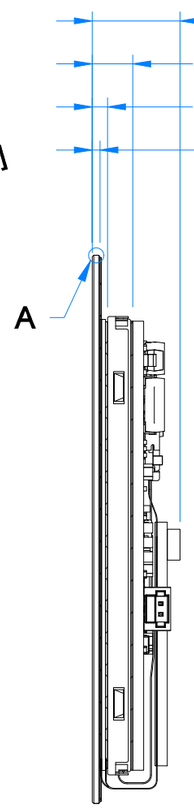
uxTouch models with double-side adhesive tape can be mounted by connecting the glass to the housing. Riverdi recommends using support brackets assembled to display’s back. An additional support will stiffen the whole structure and minimize the influence of external factors such as vibration. Figure 1 below show examples of using support elements.

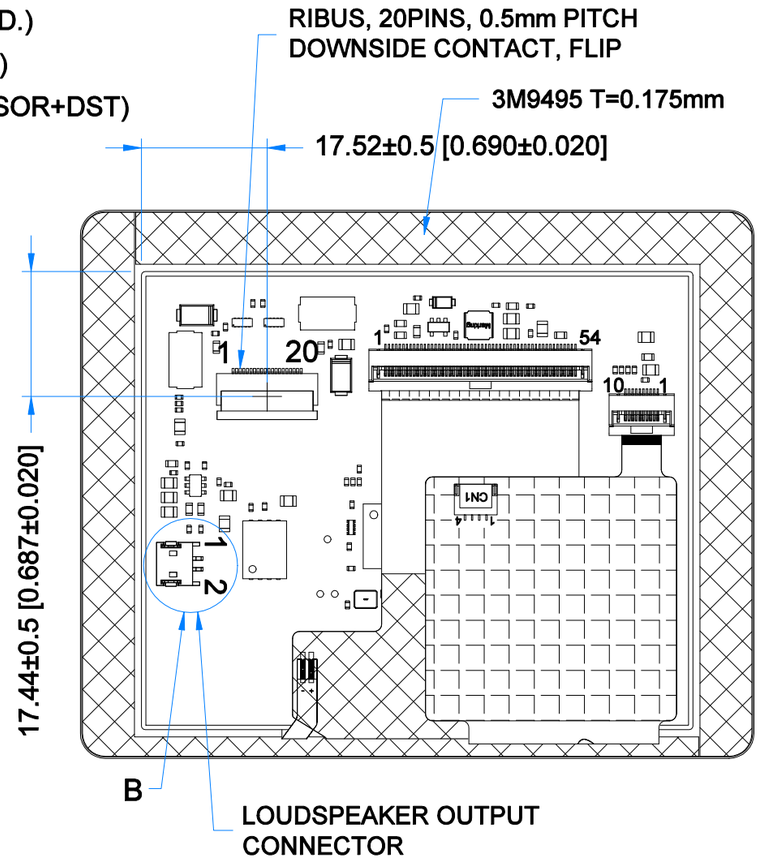
Figure 1. Example of using support brackets



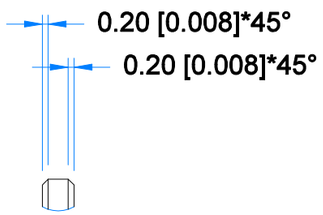
Revision:	Changes:	Date:
1.0	Initial Case	2020.12.15
1.1	PCB Update And Dimension Overhaul	2021.04.29
1.2	Adding Speaker PN And Matching Connector PN	2021.07.09



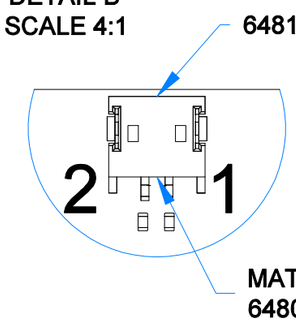


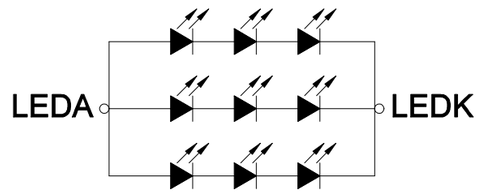


DETAIL A
SCALE 4:1



DETAIL B
SCALE 4:1





LED Diagram Circuit

TFT NOTES:
1. LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
2. RESOLUTION: 320x240
3. VIEWING ANGLE: FREE
4. SURFACE LUMINANCE: 800 cd/m^2
5. DRIVING IC ON THE BOARD: BT817Q
6. INTERFACE: SPI/QSPI
7. SUPPLY VOLTAGE FOR MODULE: 3.3V
8. SUPPLY VOLTAGE FOR BACKLIGHT:5.0V(TYP.), BUILT-IN LED INVERTER

TP NOTES:
1. TP STRUCTURE: G+G
2. CG THICKNESS: 1.10 mm
3. SURFACE HARDNESS: 7H
4. DRIVER IC: ILI2132A
5. INTERFACE: SPI/QSPI VIA RIBUS AND BT817Q

GENERAL NOTES:
1. OPERATING TEMPERATURE: -20°C ~ 70°C
2. STORAGE TEMPERATURE: -30°C ~ 80°C
3. WITHOUT INDIVIDUAL TOLERANCE:
   DIM ACCORDING DIRECTLY TO TFT: ±0.2mm
   DIM ACCORDING DIRECTLY TO TP: ±0.3mm
4. RoHS3 COMPLIANT

PN: RVT35HHBNWC00			
SN:			
DRAWN: M.Natywa	2021.04.29	1:0.86	
CHECKED: K.Brodacka	2021.07.09	[mm]	
APPR:		ISO A3	P. 1 of 1

Riverdi

6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	0	3.6	V	NOTE 1
Digital I/O signals (SPI/QSPI/GPIO) Voltage	VIN	-0.5	3.3		NOTE 1, 2
Supply voltage for Backlight	BLVDD	-0.3	5.5		NOTE 1
Operating Temperature	T_{OP}	-20	70	°C	
Storage Temperature	T_{ST}	-30	80	°C	
Storage Humidity (@ 25 ± 5°C)	H_{ST}	10	-	% RH	
Operating Ambient Humidity (@ 25 ± 5°C)	H_{OP}	10	-	% RH	

Note 1. Exceeding maximum values may cause improper operation or permanent damage to the unit.

Note 2. Digital I/O signals are to be connected to pins 3 ÷ 9, 11 and 12 pins at RiBUS connector (P1).

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	3.0	3.3	3.6	V	
Current drawn from VDD	I_{VDD}	TBD	180	TBD	mA	Note 1
Input Voltage “H” Level	V_{IH}	2.0	-	3.3	V	
Input Voltage “L” Level	V_{IL}	0	-	0.8	V	

Note 1. These values will be announced after the samples have been tested.

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Backlight	BLVDD	2.7	5.0	6.0	V	
Current drawn from BLVDD @5.0V	$I_{BLVDD=5.0V}$	TBD	150	TBD	mA	100% of backlight, NOTE 1
Current drawn from BLVDD @7.0V	$I_{BLVDD=7.0V}$	TBD	65	TBD		50% of backlight, NOTE 1
Current drawn from BLVDD @9.0V	$I_{BLVDD=9.0V}$	TBD	250	TBD		100% of backlight, NOTE 1
Current drawn from BLVDD @12.0V	$I_{BLVDD=12.0V}$	TBD	100	TBD		50% of backlight, NOTE 1
Lifetime	-	-	50,000	-	hours	NOTE 2

Note 1. Backlight intensity is driven by BT817Q controller by PWM wave from GPIO pin. Please refer to subchapter 11.4.

Note 2. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.

9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf	θ=0° ø=0° Ta=25 °C	-	30	-	ms	FIG 2.	4
Contrast Ratio	Cr		-	1000	-	---	FIG 3.	1
Luminance Uniformity	δ WHITE		-	75	-	%	FIG 3.	3
Surface Luminance	Lv		-	800	-	cd/m²	FIG 3.	2
Viewing Angle Range	θ	ø = 90°	-	80	-	deg	FIG 4.	6
		ø = 270°	-	80	-	deg	FIG 4.	
		ø = 0°	-	80	-	deg	FIG 4.	
		ø = 180°	-	80	-	deg	FIG 4.	
CIE (x, y) Chromaticity	Rx	θ=0° ø=0° Ta=25 °C	0.575	0.615	0.655	-	FIG 3.	5
	Ry		0.296	0.336	0.376	-		
	Gx		0.352	0.392	0.432	-		
	Gy		0.512	0.552	0.592	-		
	Bx		0.100	0.140	0.180	-		
	By		0.085	0.125	0.165	-		
	Wx		0.274	0.316	0.358	-		
	Wy		0.295	0.336	0.378	-		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 2. The definition of response time

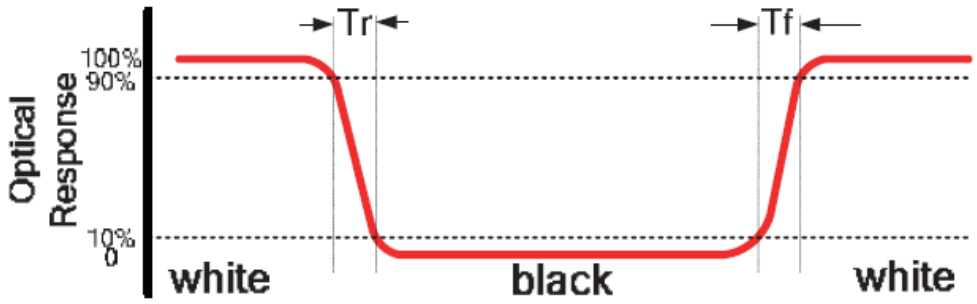
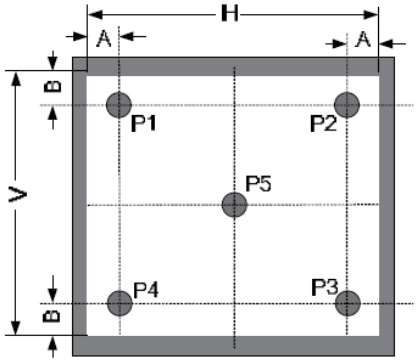


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



A: 5mm

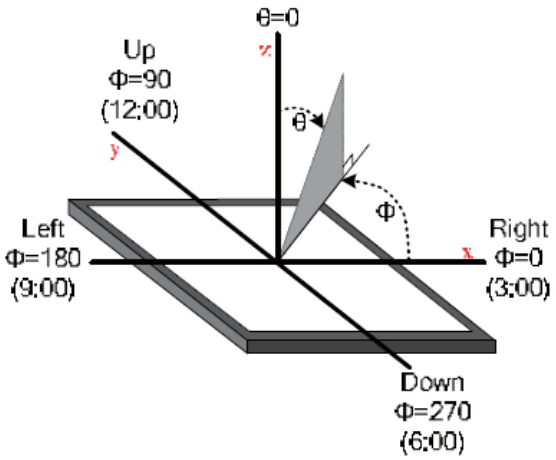
B: 5mm

H, V: Active Area

Light spot size ⌀=5mm, 500mm distance from the LCD surface to detector lens.

Measurement instrument is TOPCON'S luminance meter BM-5

Figure 4. The definition of viewing angle



10. INTERFACES DESCRIPTION

10.1 P1 connector - RiBUS description

PIN NO.	CONNECTOR	DESCRIPTION	NOTE
1	VDD	Supply voltage for module; TYP 3.3 V	
2	GND	Ground	
3	SPI_SCLK	SPI SCK signal	
4	MISO/ IO.1	SPI MISO signal / SPI Quad mode: SPI data line 1	
5	MOSI/ IO.0	SPI MOSI signal / SPI Quad mode: SPI data line 0	
6	CS	SPI chip select signal	
7	INT	Interrupt signal from device to the system, Active Low, Internally 47k Pull UP	
8	RST/PD	Reset / Power down signal, Active Low, Internally Pulled UP 47k	
9	GPIO.0	GPIO.0	
10	DISP_AUDIO	Display audio in/out	NOTE 1
11	GPIO.1/IO.2	SPI Single/Dual mode: General purpose IO0. QSPI mode: SPI data line 2	
12	GPIO.2/IO.3	SPI Single/Dual mode: General purpose IO1. QSPI mode: SPI data line 3	
13	NC	Not connected	
14	NC	Not connected	
15	NC	Not connected	
16	NC	Not connected	
17	BLVDD	Supply voltage for backlight	
18	BLVDD	Supply voltage for backlight	
19	BLGND	Backlight Ground, Internally connected to GND	
20	BLGND	Backlight Ground, Internally connected to GND	

Note 1. Requirements for audio external signal voltage will be announced after samples have been tested.

10.2 P2 connector description

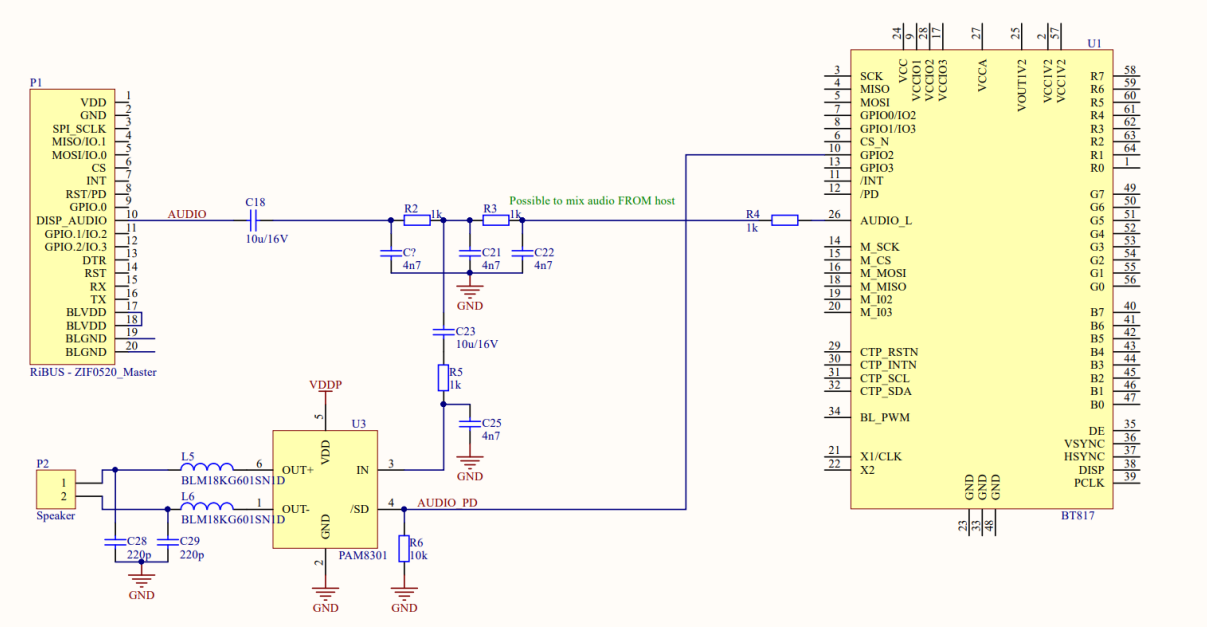
PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	SPEAKER +	Speaker coil “+” terminal	NOTE 1
2	SPEAKER -	Speaker coil “-” terminal	

The audio circuit allows for the following 3 things:

1. To play sounds from BT817Q on internal amplifier U3.
2. To play sounds from host on internal amplifier U3.
3. To play sounds from BT817Q on external amplifier.

Note 1. The loudspeaker assembly (loudspeaker + cables + plug compatible with P2 connector) will be sold separately. The documentation of the loudspeaker assembly will be released soon.

Figure 5. The simplified audio circuit design



Note 2. By standard, the EVE4 board is equipped with the separate 256Mb flash memory chip, which allows to show up to 410 full resolution (320 * 240 pixels, JPG) images. If you need to change the memory size, please contact us: contact@riverdi.com

11. BT817Q CONTROLLER SPECIFICATION

BT817Q or EVE4 (Embedded Video Engine 4) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object-oriented architecture approach that extends from display creation to the rendering of the graphics.

11.1 Serial host interface

Figure 6. SPI single/dual interface connection

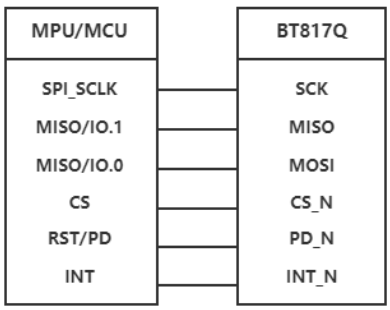
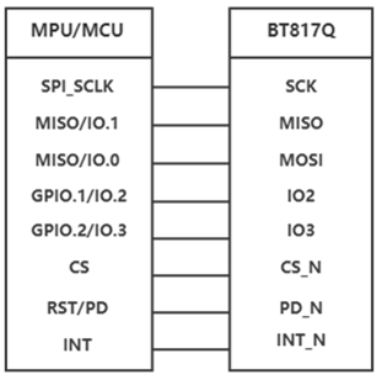


Figure 7. QSPI interface connection



SPI Interface – the SPI slave interface operates up to 30MHz (It depends on EVE4 system clock frequency and needs verification in Riverdi lab).

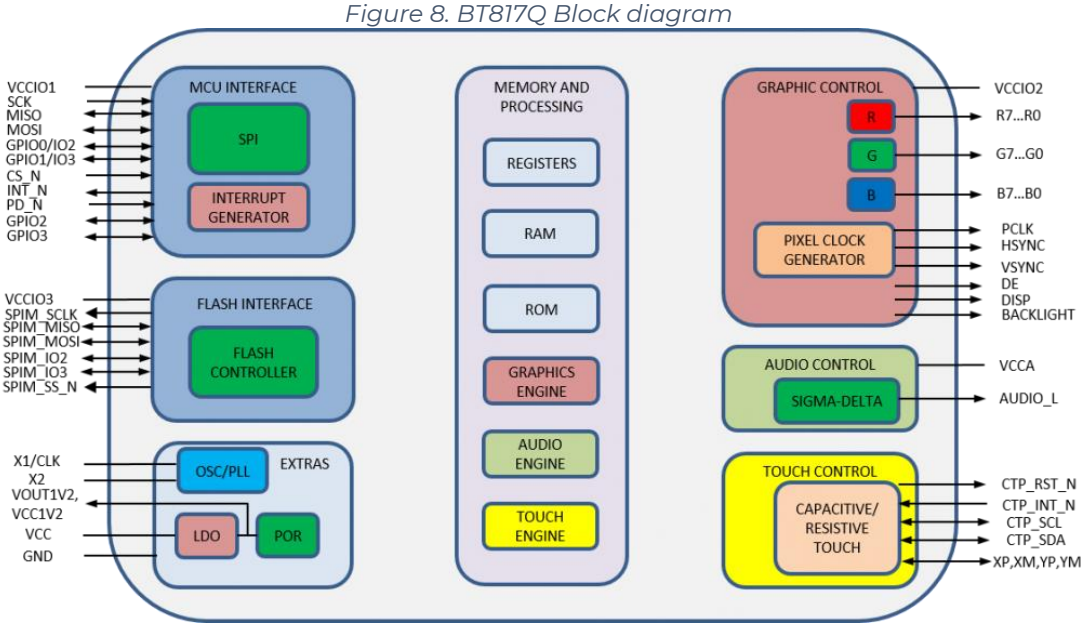
Only SPI mode 0 is supported. The SPI interface is selected by default.

QSPI Interface – the QSPI slave interface operates up to 30MHz (It depends on EVE 4 system clock frequency and will be verified in Riverdi lab). Only SPI mode 0 is supported. The QSPI can be configured as a SPI slave in SINGLE, DUAL or QUAD channel modes.

By default, the SPI slave operates in the SINGLE channel mode with MOSI as input from the master and MISO as output to the master. DUAL and QUAD channel modes can be configured through the SPI slave itself. To change the channel modes, write to register REG_SPI_WIDTH.

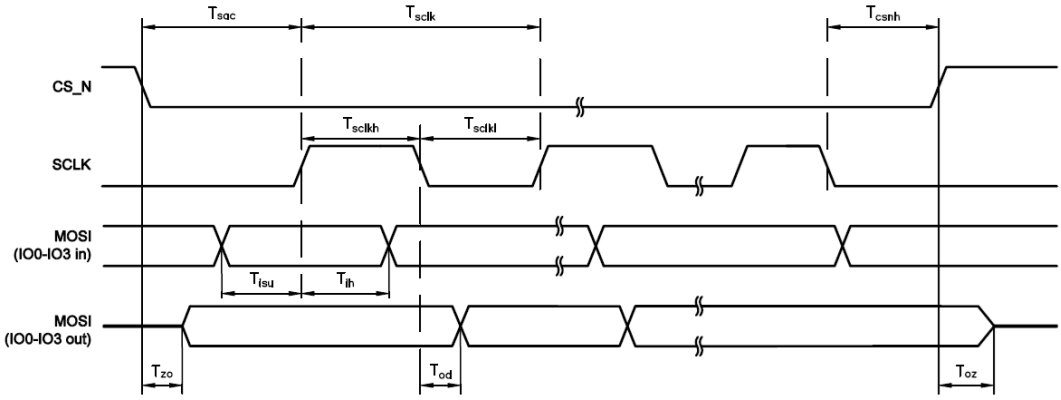
11.2 Block Diagram

Figure 8. BT817Q Block diagram



11.3 Host interface SPI mode 0

Figure 9. SPI timing diagram



The meanings of the timings in the Figure 9 are defined in the table below.

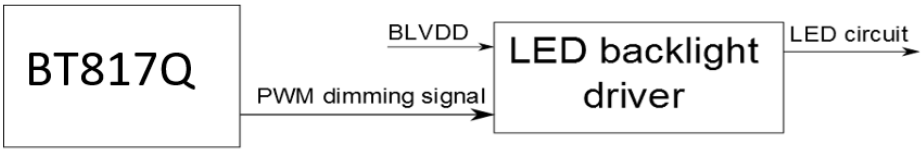
PARAMETER	DESCRIPTION	VCCIO=1.8V Min	VCCIO=1.8V Max	VCCIO=2.5V Min	VCCIO=2.5V Max	VCCIO=3.3V Min	VCCIO=3.3V Max	UNIT
T_{sclk}	SPI clock period	33.3	-	33.3	-	33.3	-	ns
T_{sclkl}	SPI clock low duration	13	-	13	-	13	-	
T_{sclkh}	SPI clock high duration	13	-	13	-	13	-	
T_{sac}	SPI access time	4	-	3.5	-	3	-	
T_{isu}	Input Setup	4	-	3.5	-	3	-	
T_{ih}	Input Hold	0	-	0	-	0	-	
T_{zo}	Output enable delay	-	16	-	13	11	-	
T_{oz}	Output disable delay	-	13	-	11	10		
T_{od}	Output data delay	-	15	-	12	11	-	
T_{csnh}	CSN hold time	0	-	0	-	0	-	

For more information about BT817Q controller please go to official BT81x website.
https://brtchip.com/bt81x/

11.4 Backlight driver block diagram

Backlight enable signal is internally connected to BT817Q backlight control pin. This pin is controlled by two BT817Q's registers. One of them specifies the PWM output frequency, second one specifies the duty cycle. Refer to BT817Q datasheet for more information. After we have done the test on samples, more detailed description will be given in this document.

Figure 10. Backlight driver block diagram



The LED backlight driver used in this module does not burst the LED current. Therefore, it does not generate audible noises on the output capacitor. It is equipped with soft start subsystem, which increases LED lifetime, as LED current peaks are reduced significantly.

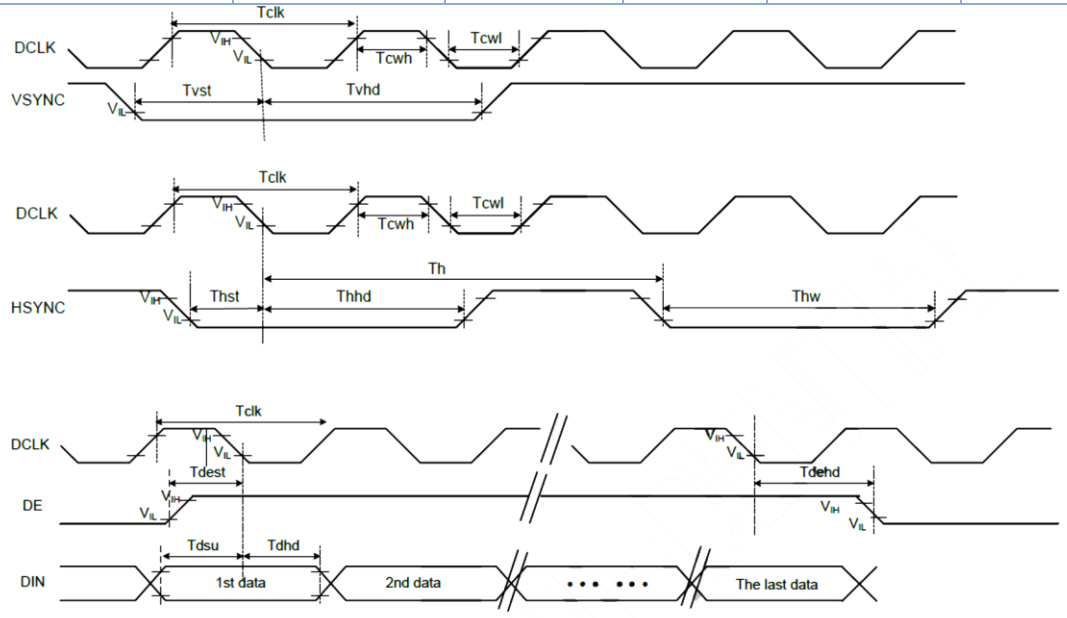
12. TIMING CHARACTERISTICS

12.1 RGB mode

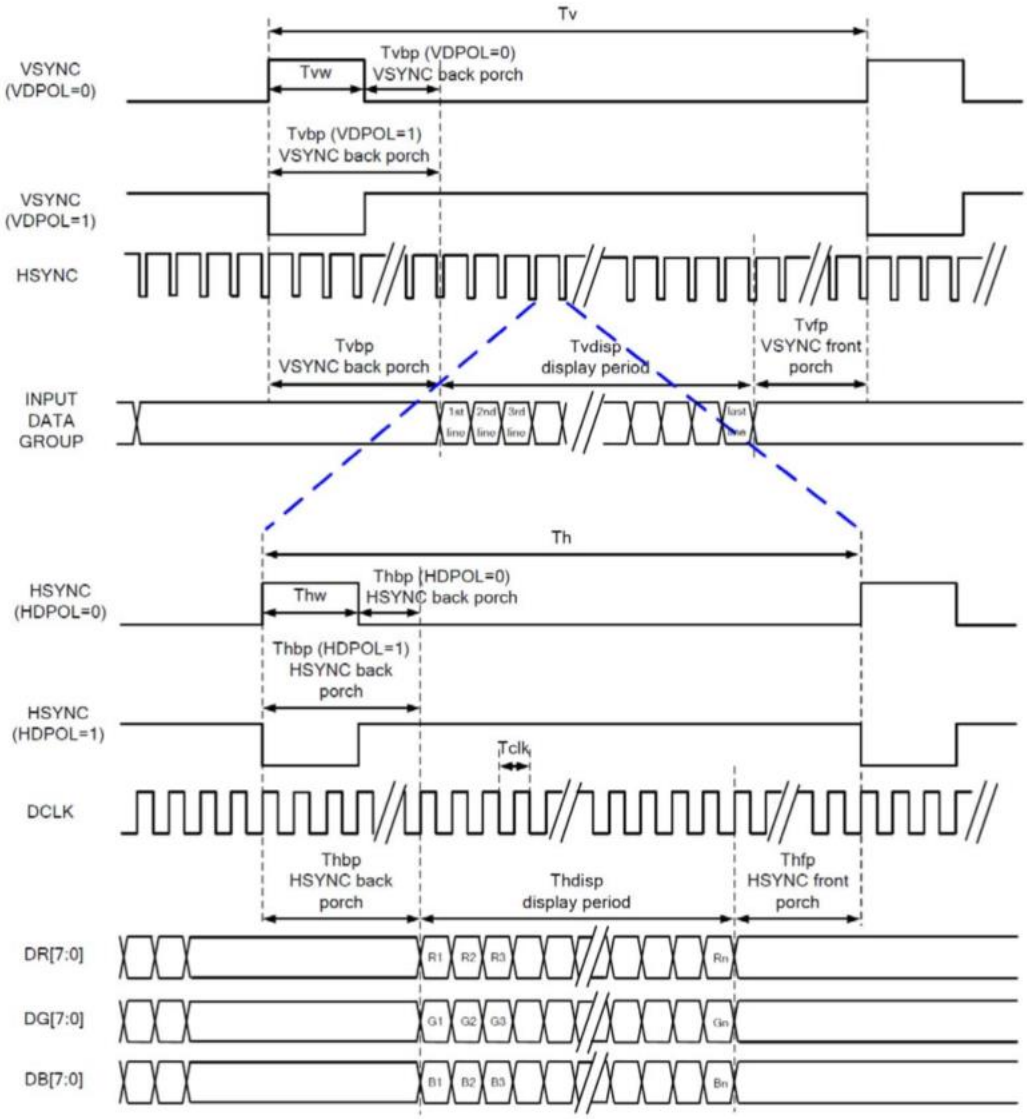
RGB MODE	DCLK	HSYNC	VSYNC	DE
SYNC-DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

12.2 System bus timing for RGB interface

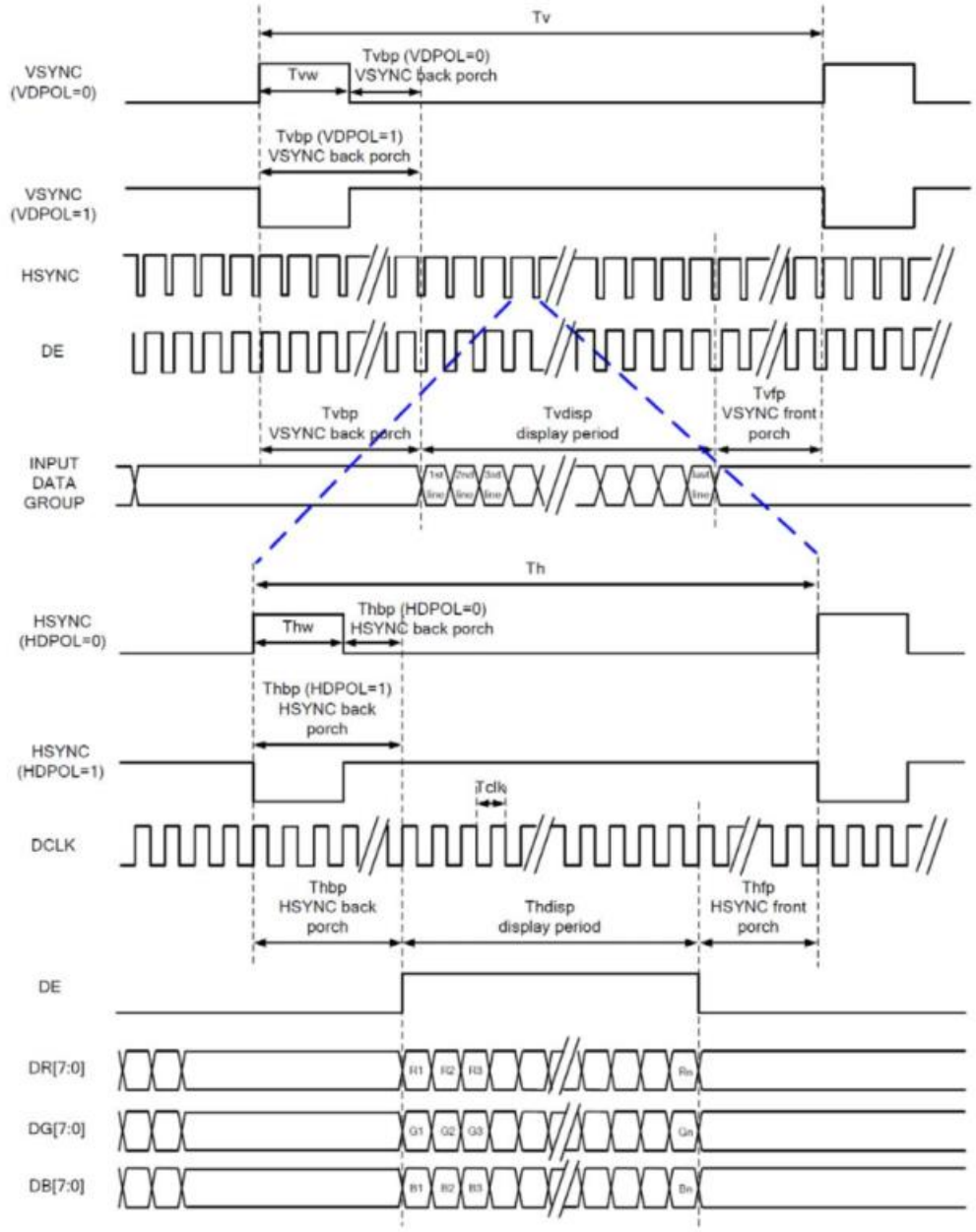
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
CLK Pulse Duty	T_{clk}	40	50	60	%
HSYNC Width	T_{hw}	2	-	-	DCLK
HSYNC Period	T_h	55	60	65	CLK
VSYNC Setup Time	T_{vst}	12	-	-	ns
VSYNC Hold Time	T_{vhd}	12	-	-	
HSYNC Setup Time	T_{hst}	12	-	-	
HSYNC Hold Time	T_{hhd}	12	-	-	
Data Setup Time	T_{dsu}	12	-	-	
Data Hold Time	T_{dhd}	12	-	-	
DE Setup Time	T_{dest}	12	-	-	
DE Hold Time	T_{dehd}	12	-	-	



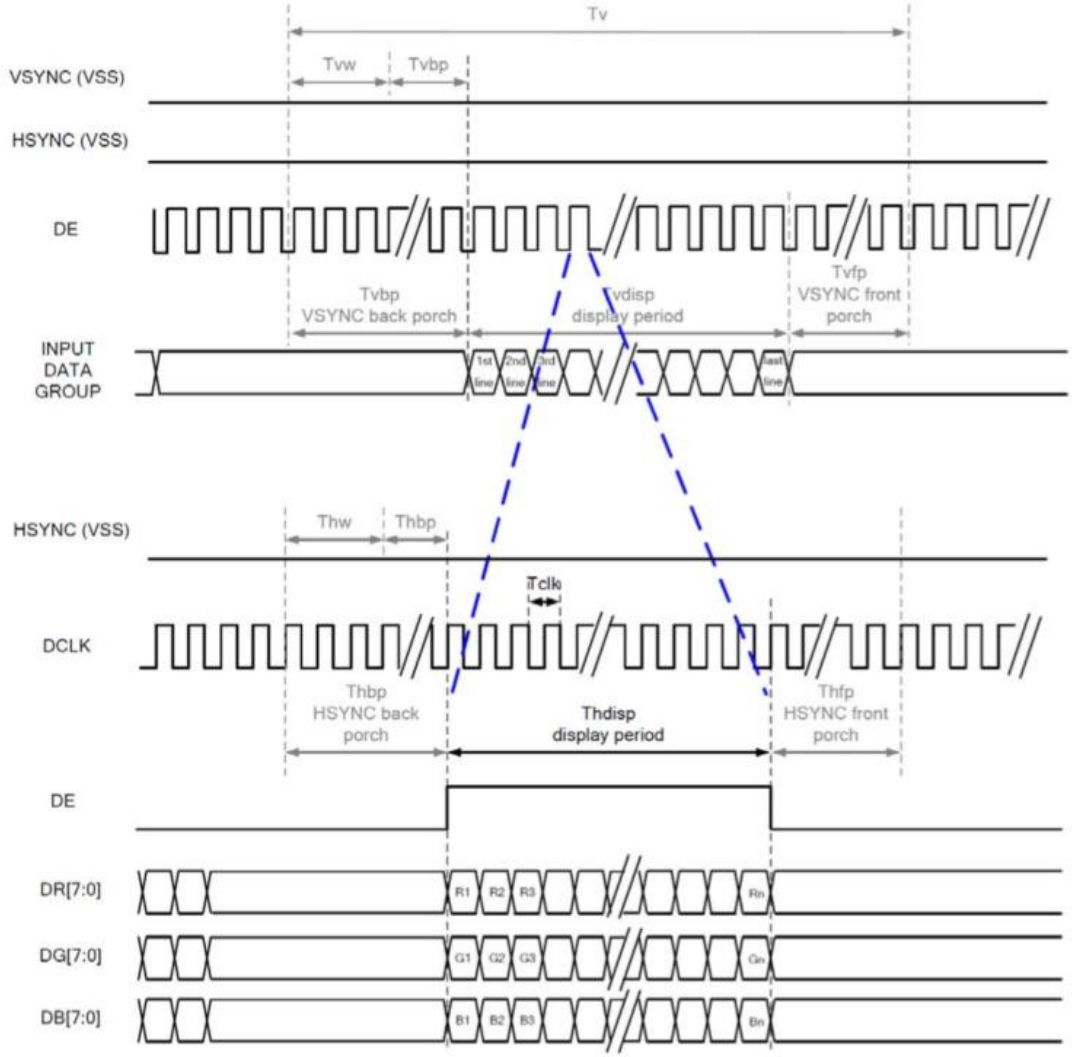
12.2.1 SYNC Mode timing diagram



12.2.2 SYNC-DE Mode timing diagram



12.2.3 DE Mode timing diagram



12.3 Parallel 24-bit RGB input timing table

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
DCLK Frequency		F_{clk}	5	6	8	MHz	
DCLK Period		T_{clk}	125	167	200	ns	
HSYNC	Period Time	T_h	325	371	438	DCLK	
	Display Period	T_{hdisp}		320			
	Back Porch	T_{hbp}	3	43	43		SYNC mode back porch control by H_BLANKING [7:0] setting Thbp= H_BLANKING [7:0]
	Front Porch	T_{hfp}	2	8	75		
	Pulse Width	T_{hw}	2	4	43		
VSYNC	Period Time	T_v	244	260	289	HSYNC	
	Display Period	T_{vdisp}		240			
	Back Porch	T_{vbp}	2	12	12		SYNC mode back porch control by V_BLANKING [7:0] setting Tvbp= V_BLANKING [7:0]
	Front Porch	T_{vfp}	2	8	37		
	Pulse Width	T_{vw}	2	4	12		

Note. It's necessary to keep Tvbp=12 and Thbp=43 in sync mode. It is unnecessary to keep these values in DE mode**.**

13. CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

13.1 Mechanical characteristics

DESCRIPTION	SPECIFICATION	REMARK
Touch Panel Size	3.5 inch	uxTouch
Outline Dimension of CTP	93.96 x 76.44	
Product Thickness	2.15 mm	
Glass Thickness	1.1 mm	
CTP View Area	70.68 mm x 53.16 mm	
Sensor Active Area	72.08 mm x 54.56 mm	
Structure type	Glass + Glass	
Surface Hardness	7H	

13.2 Electrical characteristics

DESCRIPTION		SPECIFICATION	NOTE
Power Consumption (IDD)	Active Mode	93 mA	
	Sleep Mode	10 mA	Note 1
Linearity		+/-1.5mm	
Controller		ILI2132A	
Resolution		320 x 240	

Note 1. The value will be verified on the real samples.

14. MODULE INITIALIZATION

1. Initialization data, timings and example codes are available on the Riverdi GitHub, at address: https://github.com/riverdi/riverdi-eve

2. REGISTER VALUES:

REG_HSIZE	320
REG_VSIZE	240
REG_HCYCLE	371
REG_HOFFSET	43
REG_HSYNC0	0
REG_HSYNC1	4
REG_VCYCLE	260
REG_VOFFSET	12
REG_VSYNC0	0
REG_VSYNC1	4
REG_PCLK	1
REG_SWIZZLE	0
REG_PCLK_POL	1
REG_CSPREAD	0
REG_DITHER	0
REG_PCLK_FREQ	0x22
REG_PCLK_2X	0

15. INSPECTION

Standard acceptance/rejection criteria for TFT module

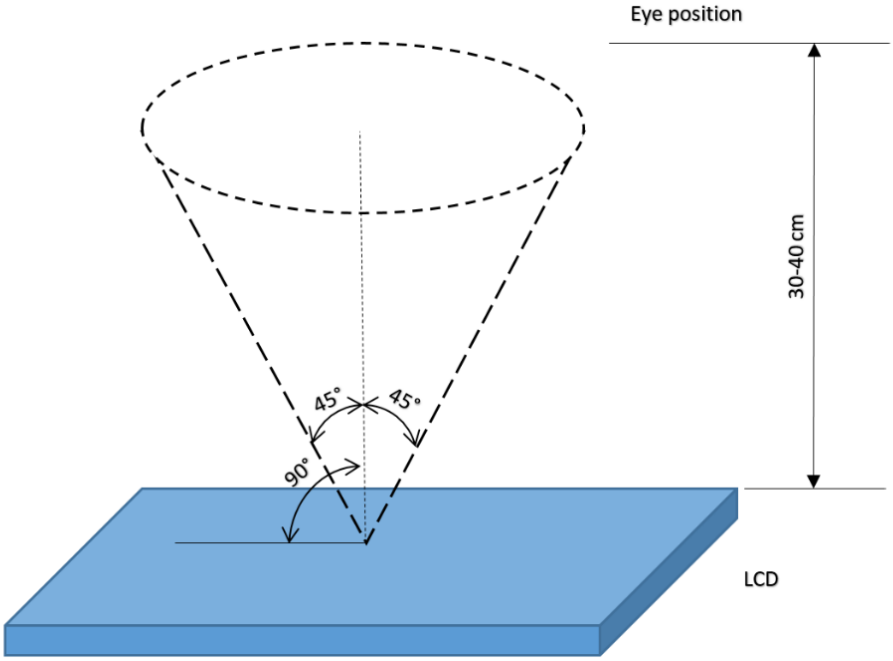
15.1 Inspection condition

Ambient conditions:

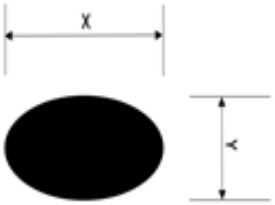
- Temperature: 25 ± 2°C
- Humidity: (60 ± 10) %RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

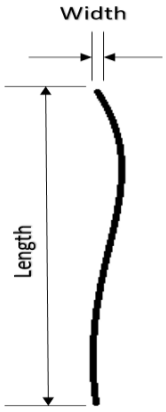
Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

Viewing Angle: U/D: 45°/45°, L/R: 45°/45°



15.2 Inspection standard

<table>
<tr><th>ITEM</th><th colspan="4">CRITERION</th></tr>
<tr><td rowspan="5">Black spots, white spots, light leakage, Foreign Particle (round Type)</td><td rowspan="5"> 

D=(x+y)/2
Spots density: 10 mm</td><td colspan="3">3.5” ≤ Size ≤ 5”</td></tr>
<tr><td colspan="2">Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td colspan="2">D ≤ 0.15 mm</td><td>Ignored</td></tr>
<tr><td colspan="2">0.15 mm < D ≤ 0.3 mm</td><td>N≤3</td></tr>
<tr><td colspan="2">0.3mm < D</td><td>Not allowed</td></tr>
<tr><td rowspan="6">LCD black spots, white spots, light leakage (line Type)</td><td rowspan="6"> 

Spots density: 10 mm</td><td colspan="3">3.5” ≤ Size ≤ 5”</td></tr>
<tr><td>Length</td><td>Width</td><td>Qualified Qty</td></tr>
<tr><td>-</td><td>W ≤ 0.03</td><td>Ignored</td></tr>
<tr><td>L ≤ 3.0</td><td>0.03 < W ≤ 0.05</td><td>2</td></tr>
<tr><td>L ≤ 3.0</td><td>0.05 < W ≤ 0.1</td><td>1</td></tr>
<tr><td>3.0 < L</td><td>0.1 < W</td><td>Not allowed</td></tr>
<tr><td rowspan="5">Bright/Dark Dots</td><td colspan="4">3.5” ≤ Size ≤ 5”</td></tr>
<tr><td colspan="2">Item</td><td colspan="2">Qualified Qty</td></tr>
<tr><td colspan="2">Bright dots</td><td colspan="2">N ≤ 1</td></tr>
<tr><td colspan="2">Dark dots</td><td colspan="2">N ≤ 2</td></tr>
<tr><td colspan="2">Total Bright and Dark Dots</td><td colspan="2">N ≤ 3</td></tr>
<tr><td rowspan="7">Clear spots</td><td colspan="4">Size < 5.0”</td></tr>
<tr><td colspan="2">Average Diameter</td><td colspan="2">Qualified Qty</td></tr>
<tr><td colspan="2">D < 0.2 mm</td><td colspan="2">Ignored</td></tr>
<tr><td colspan="2">0.2 mm < D < 0.3 mm</td><td colspan="2">3</td></tr>
<tr><td colspan="2">0.3 mm < D < 0.5 mm</td><td colspan="2">2</td></tr>
<tr><td colspan="2">0.5 mm < D</td><td colspan="2">0</td></tr>
<tr><td colspan="4">Spots density: 10 mm</td></tr>
<tr><td rowspan="7">Polarizer bubbles</td><td colspan="4">3.5” ≤ Size ≤ 5”</td></tr>
<tr><td colspan="2">Average Diameter</td><td colspan="2">Qualified Qty</td></tr>
<tr><td colspan="2">D ≤ 0.2 mm</td><td colspan="2">Ignored</td></tr>
<tr><td colspan="2">0.2 mm < D ≤ 0.3 mm</td><td colspan="2">2</td></tr>
<tr><td colspan="2">0.3 mm < D ≤ 0.5mm</td><td colspan="2">1</td></tr>
<tr><td colspan="2">0.5 mm < D</td><td colspan="2">0</td></tr>
<tr><td colspan="2">Total Q’ty</td><td colspan="2">3</td></tr>
<tr><td>Touch panel spots</td><td colspan="4">Size < 5.0”</td></tr>
<tr><td colspan="2">Average Diameter</td><td colspan="2">Qualified Qty</td></tr>
</table>

	D < 0.2 mm		Ignored
	0.2 mm < D < 0.4 mm		5
	0.4 mm < D < 0.5 mm		2
	0.5 mm < D		0
Touch panel white line scratch	Size < 5.0”		
	Length	Width	Qualified Qty
	-	W < 0.02	Ignored
	L < 3.0	0.02 < W < 0.05	2
	L < 2.5	0.05 < W < 0.08	2
	-	0.08 < W	0

16.RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	Note 1
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	Humidity 40℃, 90%RH, 120Hrs	
6	Thermal Cycling Test (No operation)	-20℃ for 30min, 70℃ for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)	
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.
Note 2. Before running cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

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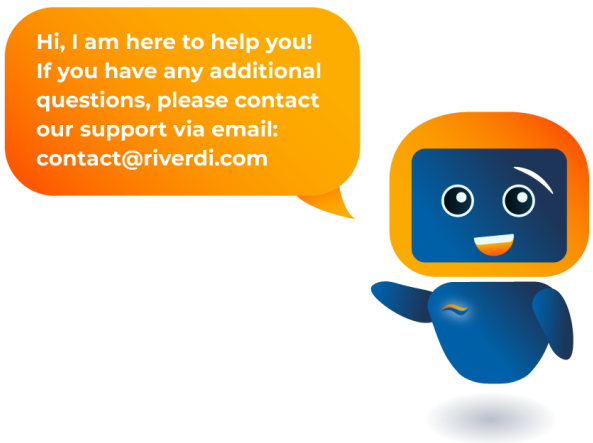

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