

# Break Out Board 36

# User Manual

Re∨.1.2 2015-12-16

CN1		break out board 36
GND 1		
BLE 3		A D∕C
WR 5		- KRR
D0 7		B DI CN2
D2 9	E	10 03
D4 11		12 05
D6 13	(* *)-	12 00 14 D2 16 D9 18 D11 20 D13 22 D15 24 TP INT 26 RESET
D8 15	-	16 D9
D10 17		18 D11
D12 19		20 D13
D14 21	ON AT	-22-D15
NC 23	5 at	-24 TP INT
CS 25	Con Can	26 RESET 36L
DISP ON 27		-28/NC// 36 L
XL/TP SCL 29		30 YU-TP SDA
XR/TP RST 31		32 YD/TP WAKE
BLGND 33		34 BLGND
BLVDD 35		36 BLVDD
	10 10	



# **REVISION RECORD**

REVNO.	REVDATE	CONTENTS	REMARKS
	2015-06-02	Preliminary edition	
1.0	2015-07-03	Initial Release	
1.1	2015-12-10	Add development kit information	
1.2	2015-12-16	Add Connection method	

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#### 1. DESCRIPTION

Break Out Board 36 is an evaluation tool which allows to expand Riverdi TFT module pins to user friendly 2.54 mm pins header. This tool can be used for 3.5", 4.3" and 7.0" Riverdi displays with SSD1963 controller.

Break Out Board 36 has two connectors: CN2, 36 pin downside ZIF connector, for connecting display module via 150 mm FFC and CN1, 36 pin IDC connector, for connecting users free cables.

#### 2. DEVELOPMENT KIT SET

Break Out Board 36 kit contains:

- Break Out Board 36,
- 36 pin, 0.5mm, 15 cm length FFC

#### 3. PIN CONFIGURATION

Pin configuration for displays with SSD1963 controller is shown in the table below.

PIN NO	SYMBOL	DESCRIPTION	
1	GND	Power Ground	
2	VDD	Power Supply: +3.3V	
3	BL_E	Backlight Control Signal, H: On/L: Off (internally pulled-up to BLVDD)	
4	D/C	Data/Command Select	
5	WR	Write Strobe Signal	
6	RD	Read Strobe Signal	
7-22	D0-D15	Data Bus. Pins not used should be floating	
23	NC	No Connection	
24	NC	No Connection	
25	CS	Chip Select	
26	RESET	Hardware reset	
27	DISP ON	Display Control H: On/L: Off (internally pulled-up)	
28	NC	No Connection	
29	XL/TP SCL	Touch left electrode/ Touch Panel I2C SCL Signal	
30	YU/TP SDA	Touch up electrode/ Touch Panel I2C SDA Signal	
31	XR/TP RST	Touch right electrode/ Touch Panel RST Signal, Active Low	
32	YD/TP WAKE	Touch down electrode/ Touch Panel Wake Signal, Active Low	
33	BLGND	Backlight ground, can be connected to GND	
34	BLGND	Backlight ground, can be connected to GND	
35	BLVDD	Backlight power supply, can be connected to VDD	
36	BLVDD	Backlight power supply, can be connected to VDD	

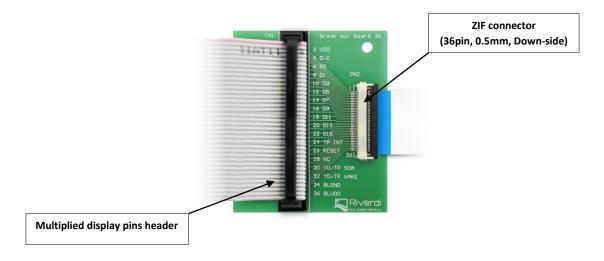
Table 1. PIN configuration for SSD1963 controller



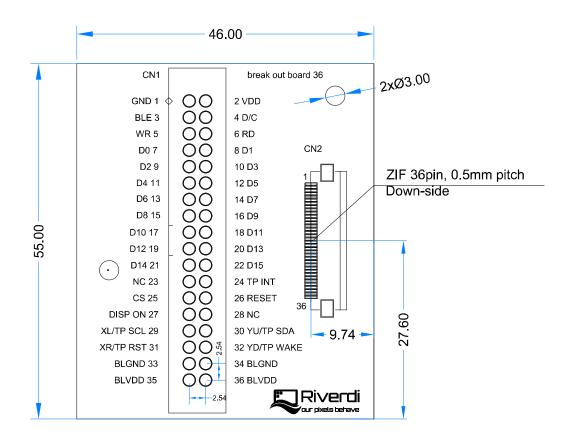
## 4. CONNECTION

Connection method is shown in Figure 1.

Figure 1. Break Out Board 36 connection method

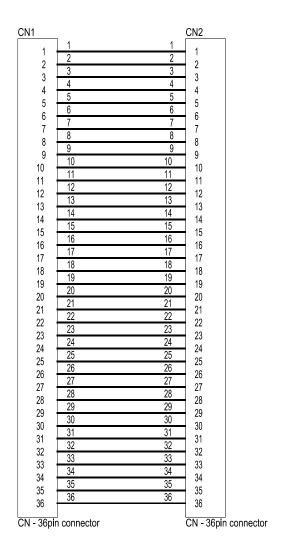


# 5. MECHANICAL DRAWING





## 6. ELECTRICAL SCHEMATIC





## 7. LEGAL INFORMATION

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