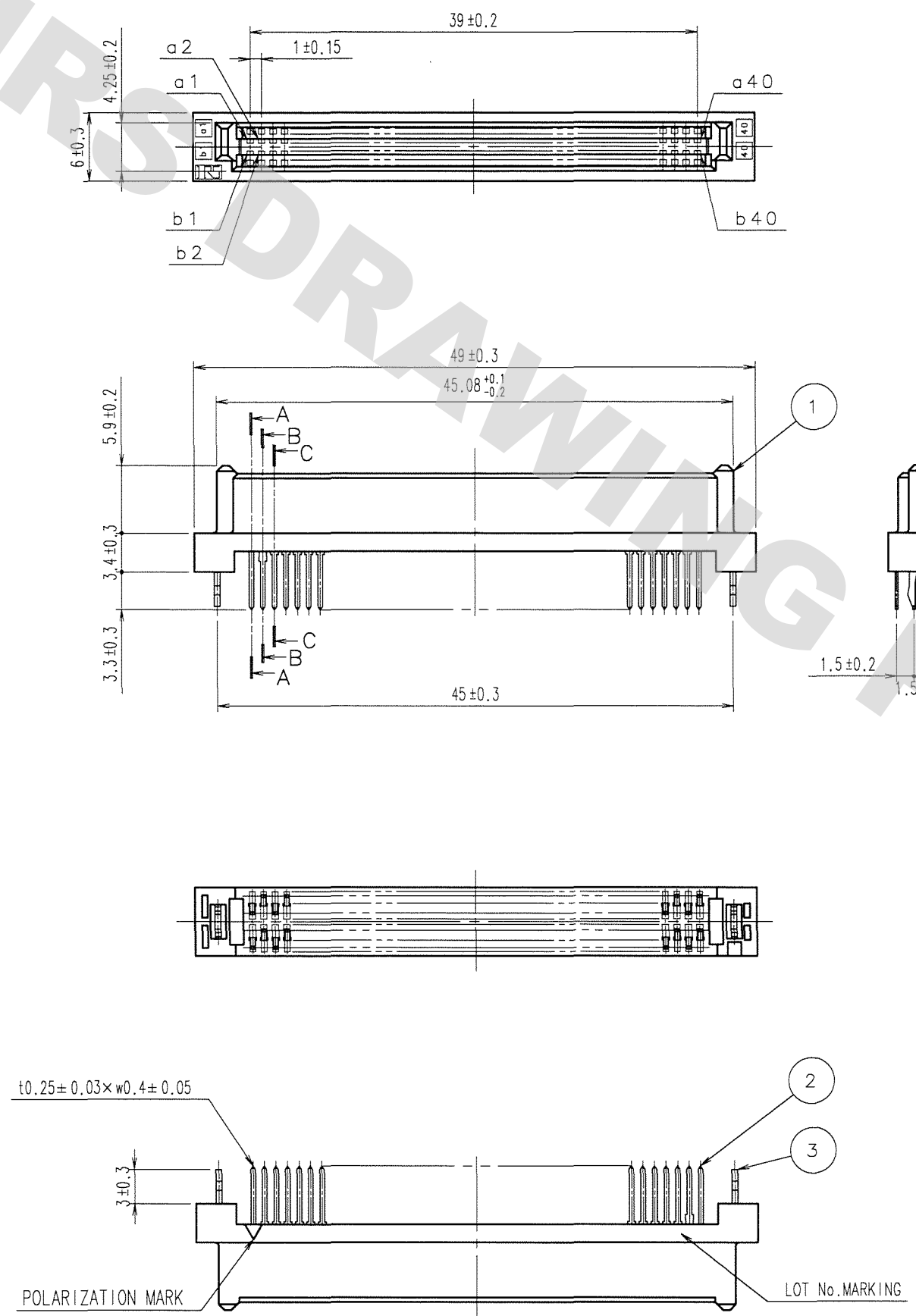


DRAWING FOR REFERENCE: This is subject to change without notice
08/11/2012

TO

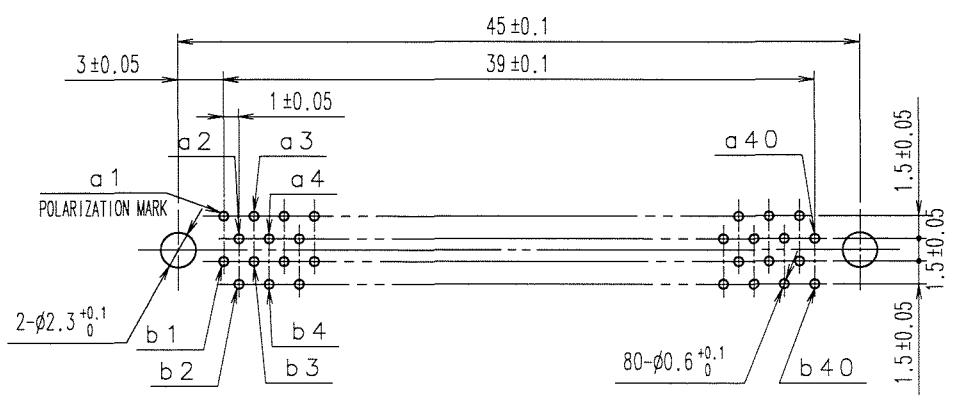
1 2 3 4 5 6 7 8

A
B
C
D
E
F



COUNT	DESCRIPTION OF REVISIONS	BY	CHKD	DATE	COUNT	DESCRIPTION OF REVISIONS	BY	CHKD	DATE
△					△				
△					△				
△					△				

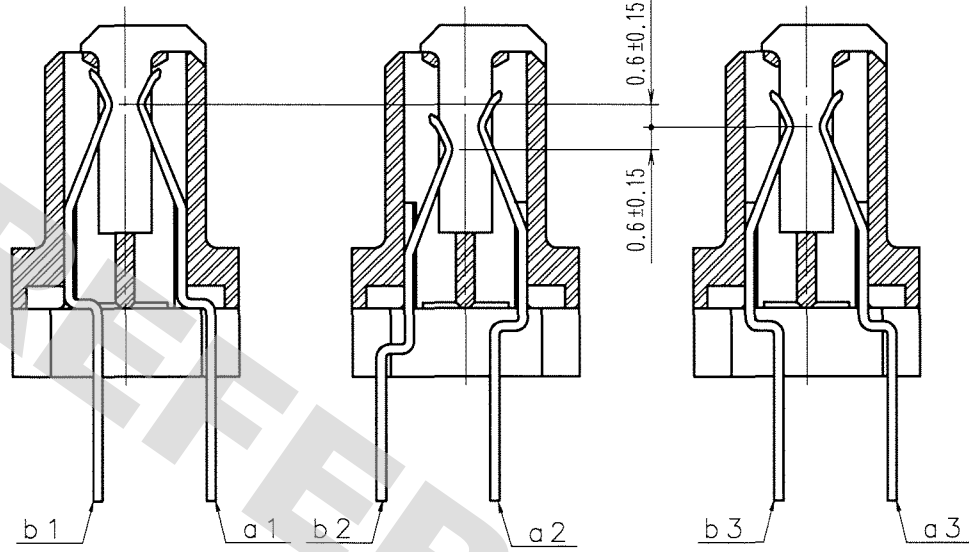
RECOMMENDED PC BOARD HOLE PATTERN (MOUNTING SIDE)
APPLICABLE BOARD THICKNESS, 1.6



CONTACT SEQUENCE

SEQUENCE	PIN NO.
1	a1, a40 b1, b40
2	a2, a38, b3, b39
3	a39, b2

A-A (5:1) B-B (5:1) C-C (5:1)



3	PHOSPHOR BRONZE	TIN-PLATING	2	PHOSPHOR BRONZE	CONTACT AREA : GOLD 0.3μ m min.
1	POLYAMIDE	BEIGE UL94V-0			LEAD AREA : TIN-PLATING 1.2μ m min.
					UNDER PLATING : NICKEL 1μ m min.
NO.	MATERIAL	FINISH, REMARKS	NO.	MATERIAL	FINISH, REMARKS
CODE NO. (OLD) CL			DRAWN	DESIGNED	CHECKED
			K.DOI '07.02.20	H. Doi '07.02.20	H. Ozawa '07.02.20
				APPROVED	RELEASED
				H. Okawa '07.02.20	
DRAWING NO. EDC3-152702-22			PART NO. FX5-80S3C-DSAL (72)		
SCALE 2 : 1			CODE NO. CL575-1006-6-72		
UNITS mm			1/1		
HRS HIROSE ELECTRIC CO., LTD					