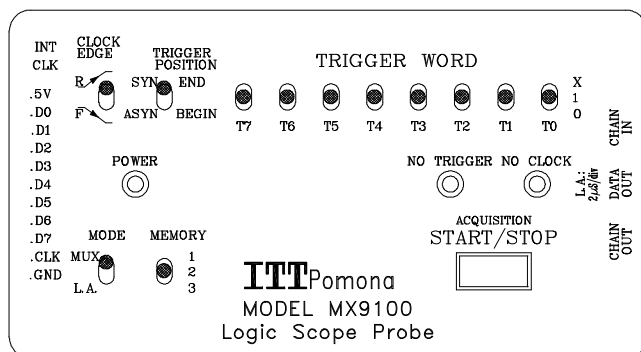
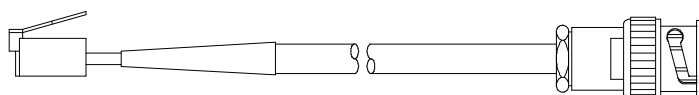
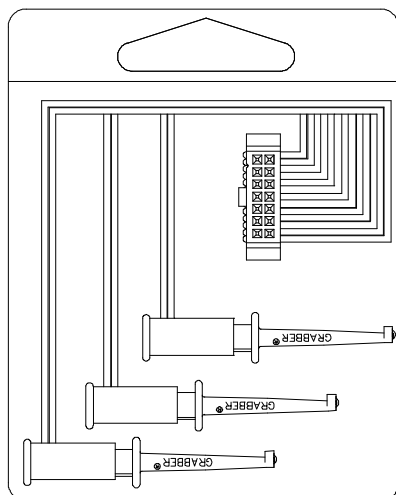


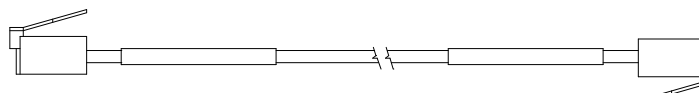
### LOGIC SCOPE PROBE



OSCILLOSCOPE INPUT CABLE



EXTERNAL TRIGGER CABLE



DAISY CHAIN CABLE

### FEATURES:

- Pocket size logic scope probe features logic analyzer, multiplexer and trigger probe modes for analog oscilloscopes.
- Fast retrace of the analog scope is used to generate 8 (up to 24 when daisy chained) “chopped” lines of signal data from a single oscilloscope input channel.
- Daisy chain of up to 3 logic scope probes
- Logic analyzer mode captures 8 digital lines, each line depicts 16 states of input bits.
- Multiplexer mode allows for observation of 8 digital signals simultaneously in real time.
- Trigger mode captures digital and analog signals in synchronization, based on a preset trigger word.
- Eight input data lines, clock, +5V input and ground inputs are connected to micro grabber® test clips for quick attachment.
- BNC output cable connects directly to the oscilloscope-input channel.

All dimensions are in inches. Tolerances (except noted): .xx = ±.02" (.51 mm), .xxx = ±.005" (.127 mm).

All specifications are to the latest revisions. Specifications are subject to change without notice.

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- Rising or falling edge of an external or internal clock are selectable for signal sampling.
- Trigger word is user definable and can be placed at the beginning or ending of a memory acquisition.
- Three independent memories are selectable for signal comparison.

### **SPECIFICATIONS:**

#### **All Modes Functional Characteristics:**

Input data channels: 8

Expandability: 24 data channels (3 chained logic scope probes)

Clock Edge Selection: 0, 1, X (Low, High, Not Specified)

#### **Logic Analyzer Mode:**

Clock Source Selection: External/Internal Clock

Trigger Word Position: Beginning/Ending

Number of Memories: Three

#### **Multiplexer (MUX) and Trigger Probe Mode Functional Characteristics:**

Selection of synchronous or asynchronous operation

#### **Electrical Specifications:**

##### **All Modes Characteristics:**

Operating Frequency Range: DC to 20 MHz.

Data Set-up Time: 8 nanoseconds minimum

Data Hold Time: 8 nanoseconds minimum

Minimum Clock Duration (High or Low): 25ns

Internal Clock Frequency: approximately 800 kHz.

Input Impedance: 100K ohms/5pfd.

Minimum Input Level High: 2.4 Volts

Maximum Input Level Low: 0.8 Volts

Maximum Input Level High: 5.5 Volts

Minimum Input Level Low: -0.5 Volts

Nominal Power Supply voltage: 4.75 to 7 Volts

Maximum Supply Current: 190 Ma

### **ORDERING INFORMATION:** Model 6004

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