

SPEC for Mass Production

Spec No.	TQ3C-8EAF0-E1YAD65-01
Date	August 9, 2023

TYPE : TCG035QVLAAANN-AN00
< 3.5 inch QVGA transmissive color TFT
with LED backlight>

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KYOCERA CORPORATION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
July 20, 2023	<i>T. Onodera</i>	<i>T. Fukui</i>	<i>A. Iwasaki</i>	<i>T. Sawada</i>

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Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

1. Application

This document defines the specification of TCG035QVLAAANN-AN00. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Anti-Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input) (without constant current circuit for LED Backlight)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	76.9(W)×63.9(H)×4.9(D)	mm
Active area	70.56(W)×52.92(H) (8.8cm/3.5 inch (Diagonal))	mm
Dot format	320×(R,G,B)(W)×240(H)	dot
Dot pitch	0.0735(W)×0.2205(H)	mm
Base color 2)	Normally White	-
Mass	40	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage	V _{DD}	-0.3	4.0	V
Input signal voltage 1)	V _{IN}	-0.3	4.0	V
LED forward current 2) 3)	I _F	-	30	mA

1) Input signal : CK, R0~R7, G0~G7, B0~B7, H_{SYNC}, V_{SYNC}, ENAB, REST, CSB, SCK, SDI

2) For each "AN-CA"

3) Do not apply reversed voltage.

4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature 1)	T _{OP}	-20	70	°C
Storage temperature 2)	T _{STO}	-30	80	°C
Operating humidity 3)	H _{OP}	10	4)	%RH
Storage humidity 3)	H _{STO}	10	4)	%RH
Vibration	-	5)	5)	-
Shock	-	6)	6)	-

1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

2) Temp. = -30°C < 48h, Temp. = 80°C < 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock.

An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

3) Non-condensing

4) Temp. ≤ 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10~55 Hz	Acceleration value (0.3~9 m/s ²)
Vibration width	0.15mm	
Interval	10-55-10 Hz	1 minutes

2 hours in each direction X, Y, Z (6 hours total)

EIAJ ED-2531

6) Acceleration: 490 m/s², Pulse width: 11 ms

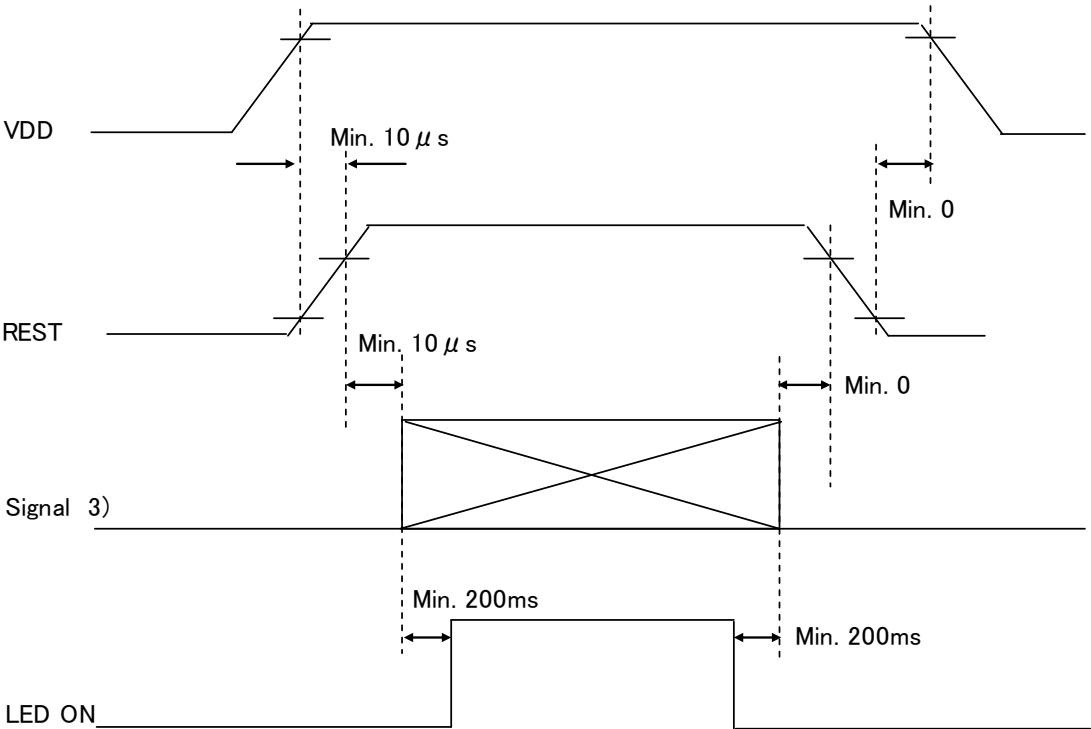
3 times in each direction: ±X, ±Y, ±Z

EIAJ ED-2531

5. Electrical characteristics

Temp. = -20~70°C						
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage 1)	V _{DD}	-	3.0	3.3	3.6	V
Current consumption	I _{DD}	2)	-	12	16	mA
Permissive input ripple voltage	V _{RP}	V _{DD} =3.3V	-	-	100	mVp-p
Input signal voltage 3)	V _{IL}	"Low" level	0	-	0.2V _{DD}	V
	V _{IH}	"High" level	0.8V _{DD}	-	V _{DD}	V

1) V_{DD}-turn-on conditions

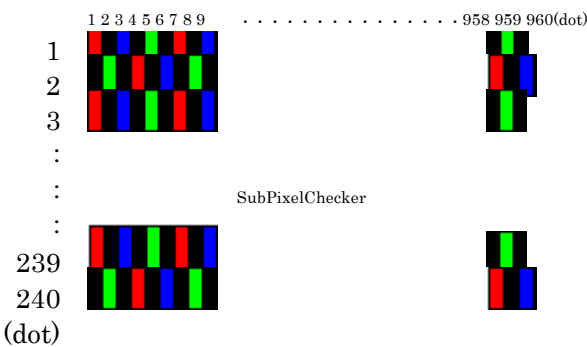


2) I_{DD} measuring conditions

Typ. : V_{DD}=3.3V, Temp. = 25°C

Max. : V_{DD}=3.6V, Temp. = 70°C

Display pattern



3) Input signal : CK, R0~R7, G0~G7, B0~B7, H_{SYNC}, V_{SYNC}, ENAB, REST, CSB, SCK, SDI

6. Optical characteristics

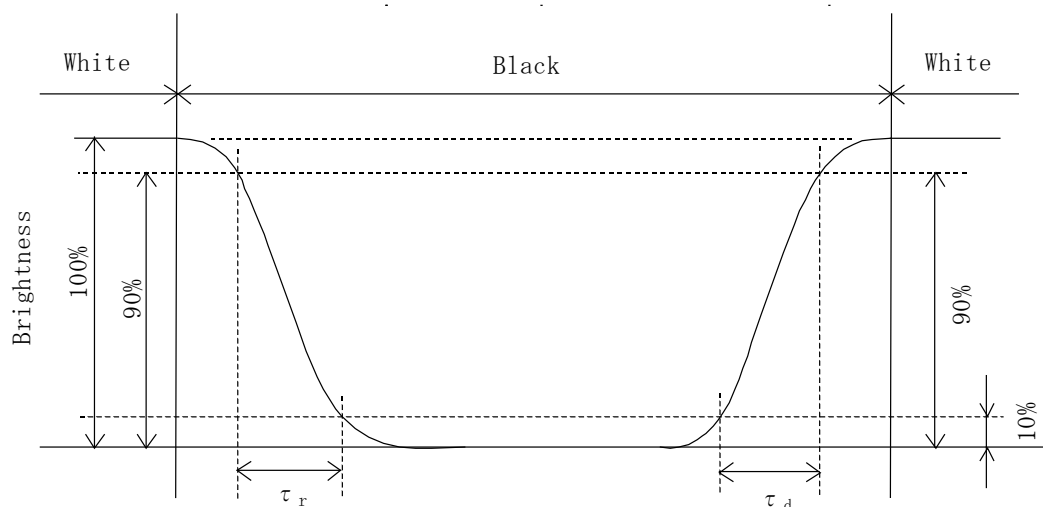
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_{r}	$\theta = \phi = 0^{\circ}$	-	8	-	ms
	Down	τ_{d}	$\theta = \phi = 0^{\circ}$	-	22	-	ms
Viewing angle range View direction : 12 o'clock (Gray inversion)		θ_{UPPER}	$\text{CR} \geq 10$	-	80	-	deg.
		θ_{LOWER}		-	60	-	
		ϕ_{LEFT}		-	80	-	deg.
		ϕ_{RIGHT}		-	80	-	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	700	1,000	-	-
Brightness		L	IF=15mA/Line	420	600	-	cd/m ²
Chromaticity coordinates	Red	x	$\theta = \phi = 0^{\circ}$	0.550	0.600	0.650	-
		y		0.300	0.350	0.400	
	Green	x	$\theta = \phi = 0^{\circ}$	0.275	0.325	0.375	
		y		0.510	0.560	0.610	
	Blue	x	$\theta = \phi = 0^{\circ}$	0.110	0.160	0.210	
		y		0.070	0.120	0.170	
	White	x	$\theta = \phi = 0^{\circ}$	0.245	0.295	0.345	
		y		0.265	0.315	0.365	

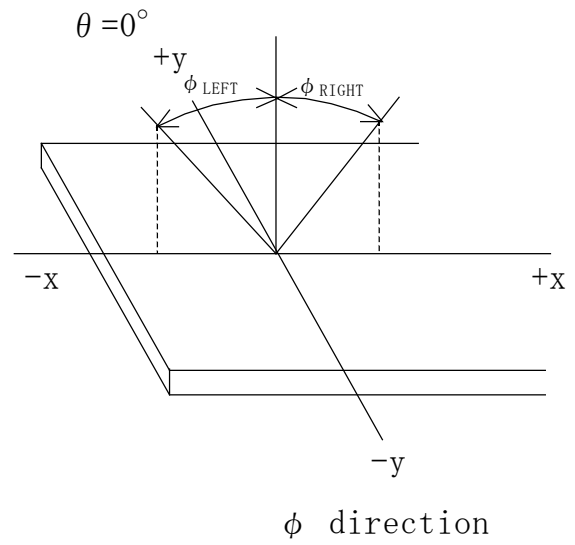
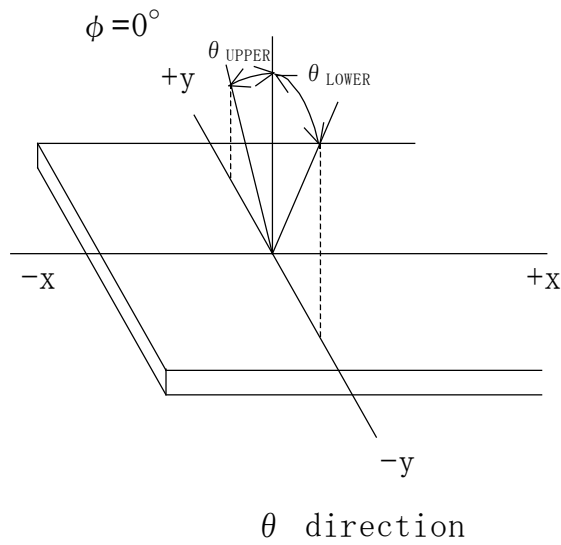
6-1. Definition of contrast ratio

$$CR(\text{Contrast ratio}) = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$

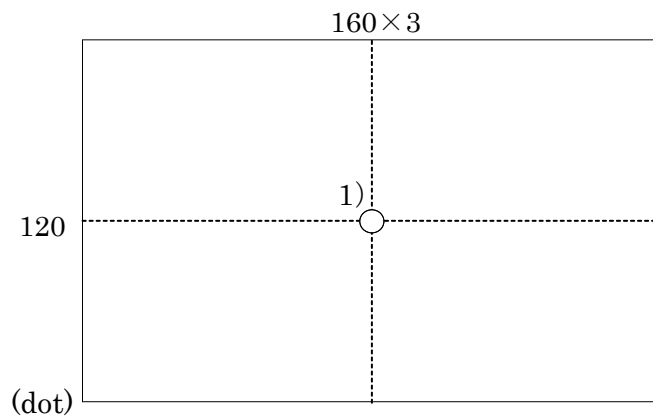
6-2. Definition of response time



6-3. Definition of viewing angle



6-4. Brightness measuring point



- 1) Rating is defined as the white brightness at center of display screen.
- 2) 5 minutes after LED is turned on. (Ambient Temp.= 25°C)

7. Interface signals

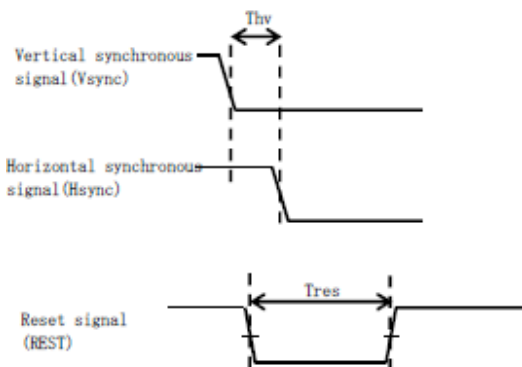
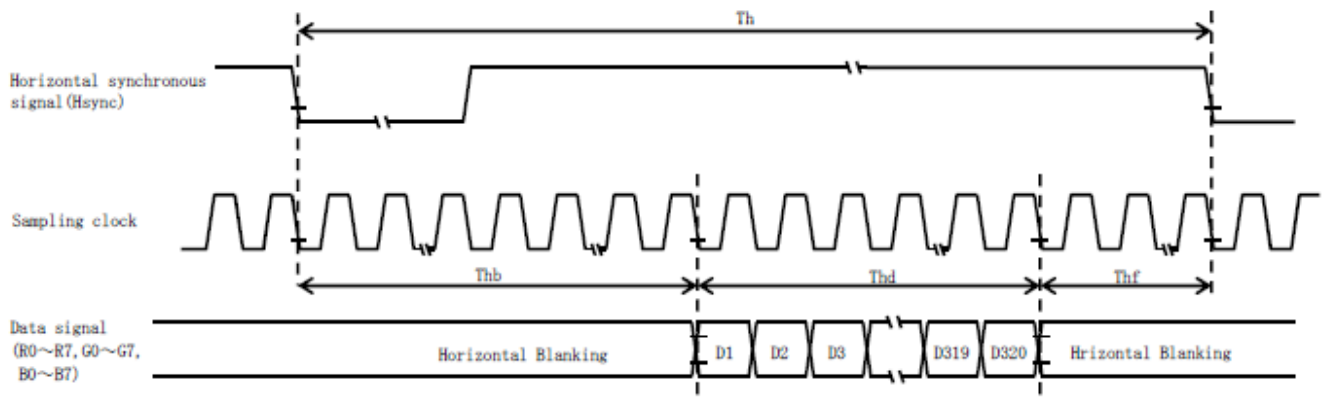
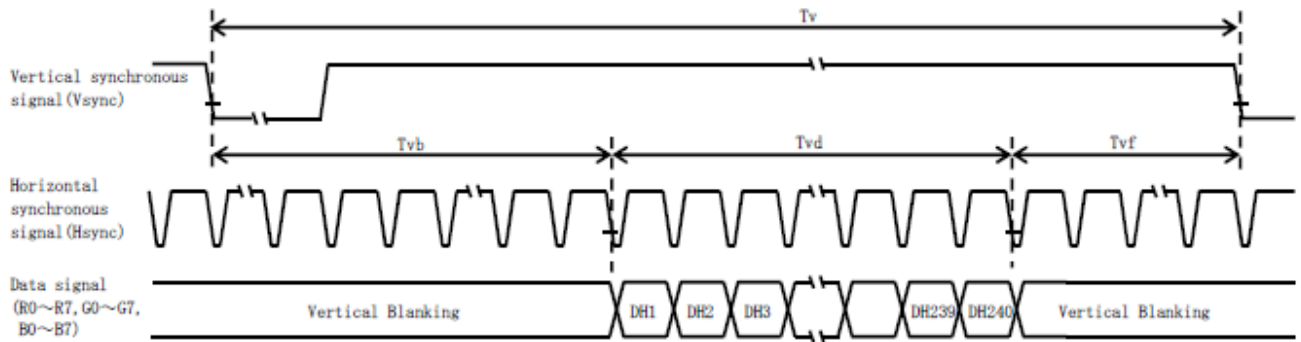
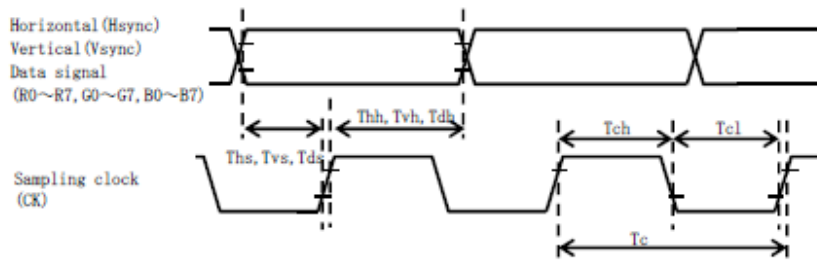
No.	Symbol	Description	Note
1	GND	GND	
2	GND	GND	
3	V _{DD}	3.3V power supply	
4	V _{DD}	3.3V power supply	
5	R0	RED data signal(LSB)	
6	R1	RED data signal	
7	R2	RED data signal	
8	R3	RED data signal	
9	R4	RED data signal	
10	R5	RED data signal	
11	R6	RED data signal	
12	R7	RED data signal(MSB)	
13	G0	GREEN data signal(LSB)	
14	G1	GREEN data signal	
15	G2	GREEN data signal	
16	G3	GREEN data signal	
17	G4	GREEN data signal	
18	G5	GREEN data signal	
19	G6	GREEN data signal	
20	G7	GREEN data signal(MSB)	
21	B0	BLUE data signal(LSB)	
22	B1	BLUE data signal	
23	B2	BLUE data signal	
24	B3	BLUE data signal	
25	B4	BLUE data signal	
26	B5	BLUE data signal	
27	B6	BLUE data signal	
28	B7	BLUE data signal(MSB)	
29	GND	GND	
30	CK	Sampling clock	
31	CSB	Select signal(SPI)	
32	H _{SYNC}	Horizontal synchronous signal(negative)	
33	V _{SYNC}	Vertical synchronous signal(negative)	
34	ENAB	Data Enable (Low signal only)	
35	GND	GND	
36	REST	Reset signal	
37	SCK	Clock (SPI)	
38	SDI	Data signal(SPI)	
39	GND	GND	
40	NC	NC(Open)	
41	NC	NC(Open)	
42	NC	NC(Open)	
43	NC	NC	
44	GND	GND	
45	CA1	Cathode1	
46	NC	NC	
47	AN1	Anode1	
48	AN2	Anode2	
49	NC	NC	
50	CA2	Cathode2	

8. Input timing characteristics

8-1. LCD (Necessity of V·H_{SYNC}) 1)

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	-	6.5	-	MHz	2)
	Period	Tc	-	154	-	ns	
	High time	Tch	50	-	-	ns	
	Low time	Tcl	50	-	-	ns	
Data (R0~R5,G0~G5, B0~B5)	Set up time	Tds	12	-	-	ns	
	Hold time	Tdh	12	-	-	ns	
Horizontal sync. Signal (H _{SYNC})	Set up time	Ths	20	-	-	ns	
	Hold time	Thh	20	-	-	ns	
	Frequency	1/Th	-	15.9	-	kHz	
	Period	Th	-	408	-	Tc	
	Front porch	Thf	-	20	-	Tc	
	Back porch	Thb	-	68	-	Tc	
Horizontal display period		Thd	320			Tc	
Vertical sync. Signal (V _{SYNC})	Set up time	Tvs	20	-	-	ns	
	Hold time	Tvh	20	-	-	ns	
	Period	Tv	-	262	-	Th	
	Front porch	Tvf	-	4	-	Th	
	Back porch	Tvb	-	18	-	Th	
Vertical display period		Tvd	240			Th	
Synchronous signal phase lag		Thv	0	-	240	Tc	
Refresh rate		1/Tv	-	60	-	Hz	
Reset signal (REST)	Pulse width	Tres	10	-	-	μs	

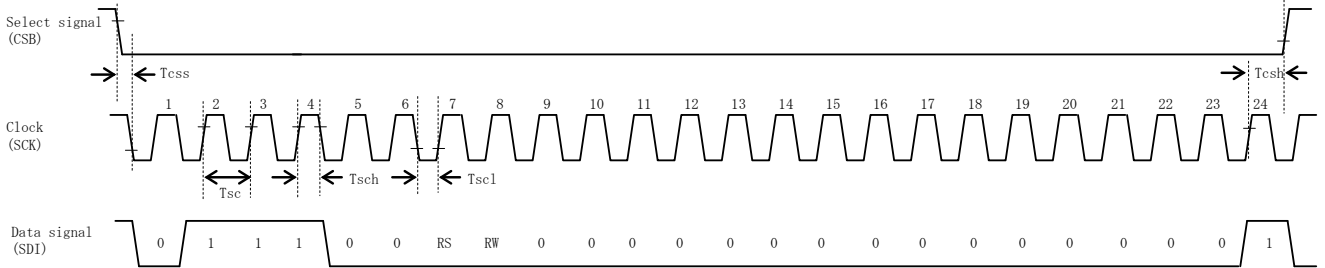
- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur. Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.



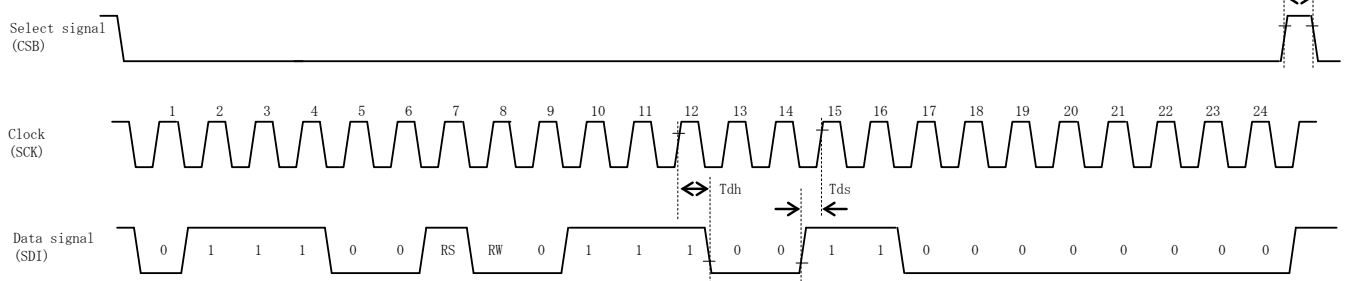
8-2. SPI

	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock (SCK)	Period	Tsc	50	-	-	ns	
	High time	Tsch	25	-	-	ns	
	Low time	Tscl	25	-	-	ns	
Select signal (CSB)	Set up time	Tcss	50	-	-	ns	
	Hold time	Tcsh	50	-	-	ns	
	High time	Tcs	50	-	-	ns	
Data signal (SDI)	Set up time	Tds	15	-	-	ns	
	Hold time	Tdh	15	-	-	ns	

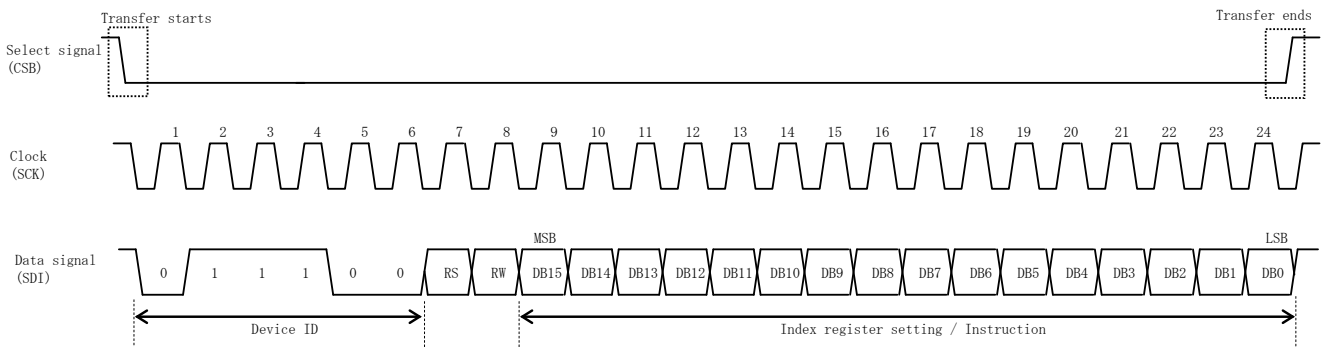
First Transmission (Register) Ex.R01h



Second Transmission (Data) Ex.7300



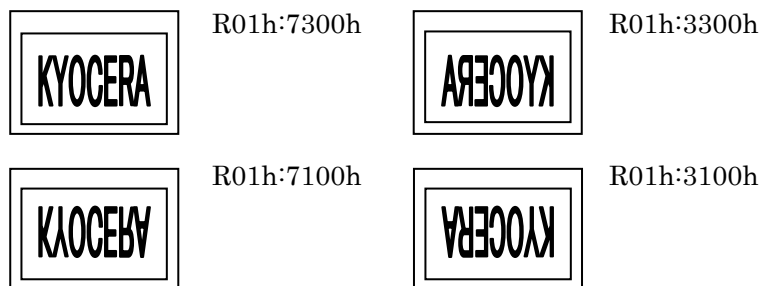
Transmission Format



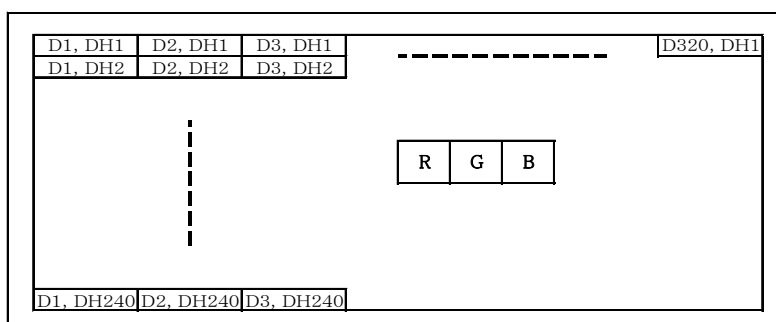
8-3. Register setting

Reg#	Hex Code	Description	Note
R01h	7300	Basic format	1)
R02h	0200	Display driving	
R03h	6466	Power supply	
R04h	04C7	Basic format	
R05h	F444		
R06h	E860	Display driving	
R08h	06FF	Power supply	
R0Ah	4008	Display quality	
R0Bh	F000	Panel driving	
R0Dh	322A	Power supply	
R0Eh	1080		
R0Fh	0000	Display driving	
R16h	9F80		
R17h	2212	Power supply	
R1Eh	0067		
R30h	0700	Display quality	
R31h	0507		
R32h	0000		
R33h	0305		
R34h	0307		
R35h	0003		
R36h	0207		
R37h	0101		
R3Ah	0000		
R3Bh	0000		

1) Reverse scan control



8-4. Input data signals and display position on the screen



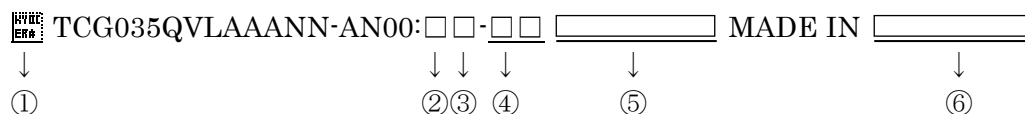
9. Backlight characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward current 1)	IF	-	15	-	mA	Ta=-20~70°C
Forward voltage 1)	VF	-	13.0	13.8	V	IF=15mA, Ta=-20°C
		-	12.5	13.3	V	IF=15mA, Ta=25°C
		-	12.2	13.0	V	IF=15mA, Ta=70°C
Operating life time 2) 3)	T	-	50,000	-	h	IF=15mA, Ta=25°C

- 1) For each "AN-CA"
- 2) When brightness decrease 50% of minimum brightness.
The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 3) Life time is estimated data. (Condition: IF=15mA, Ta=25°C in chamber).
- 4) An input current below 5mA may reduce the brightness uniformity of the LED backlight.
This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 5) LED formation: 4 series, 2 parallel

10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.



No① – No⑥ above indicate

- ① Data matrix (For internal control purpose only)
- ② Year code (The last digit of the year)
- ③ Month code
- ④ Day code
- ⑤ Version number (Max. 7 characters)
- ⑥ Country of origin

③ Month code

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

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12. Precautions for use

12-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.
Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

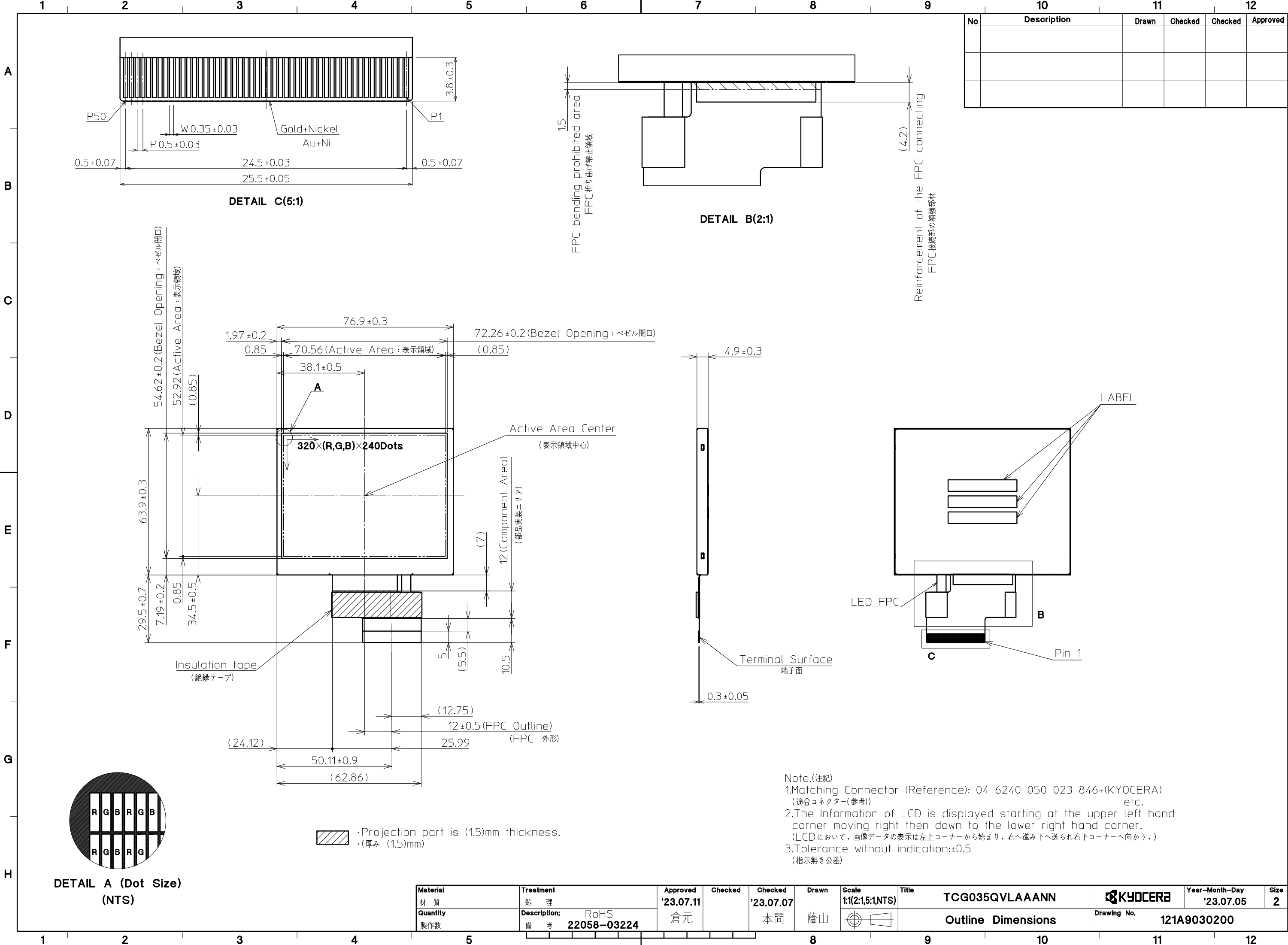
12-5. Usage

- 1) **DO NOT** store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

13. Reliability test data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	80°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Low temp. atmosphere	-30°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. operation	70°C	500h	Display function : No defect Display quality : No defect Current consumption : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.



No	Description	Drawn	Checked	Checked	Approved

Note.(注記)
1.Matching Connector (Reference): 04 6240 050 023 846+(KYOCERA)
(適合コネクタ(参考)) etc.
2.The Information of LCD is displayed starting at the upper left hand corner moving right then down to the lower right hand corner.
(LCDにおいて、画像データの表示は左上コーナーから始まり、右へ進み下へ送られ右下コーナーへ向かう。)
3.Tolerance without indication:±0.5
(指示無き公差)

Material 材質	Treatment 処理	Approved '23.07.11	Checked	Checked '23.07.07	Drawn 蔭山	Scale 1:1(2:1,5:1,NTS)	Title TCG035QVLAANN	KYOCERA	Year-Month-Day '23.07.05	Size 2
Quantity 製作数	Description; 備考 RoHS 22058-03224	倉元		本間			Outline Dimensions	Drawing No. 121A9030200		

△ ○ FPC引き出し方法(参考例)
Wiring methods FPC(Reference Example)

△ × FPC引き出し方法(NG)
Wiring methods FPC(cases NG)

△ × FPC引き出し方法(NG)
Wiring methods FPC(cases NG)

下図のような、セット側基板などをまいた引き出しは、実装エリアに応力がかかり、部品の損傷が発生する恐れがあります。
As shown below, wiring across the board and set implementation takes the stress area, may cause damage to components.

FPCを表示面側に折り曲げての取り付けは、FPCの断線が発生する恐れがあります。
Installing the folded side to view the FPC is May cause breakage of the FPC.

No	Description	Drawn	Checked	Checked	Approved
△	・Added reference value. (参考値追加)	'11.02.28 蔭山	'11.02.28 朝倉		'11.02.28 朝倉
△	・Added Wiring methods FPC (Reference Example) (FPC引き出し方法(参考例追加))	'11.06.17 蔭山	'11.07.04 倉元		'11.07.04 朝倉

参考(for Reference)

FPC引き出し部は、1Minのクリアランスを設けてください
FPC drawer unit, please provided a clearance of 1Min

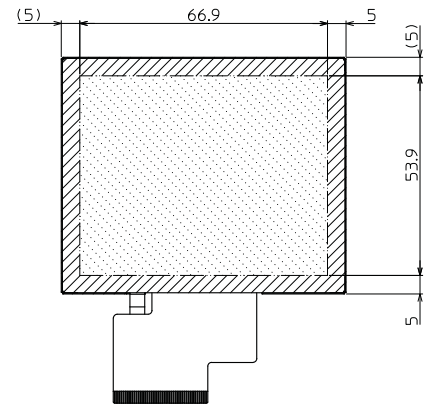
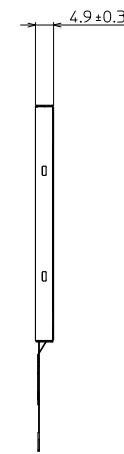
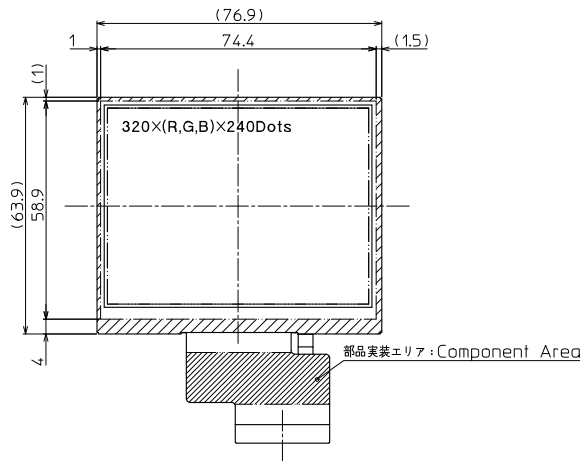
セット側コネクタ(The set of connectors)
セット側基板(The board set)

実装部品がセット基板などと接触しないような引き出しをしてください
Please do not contact to the routing of printed circuit boards and components

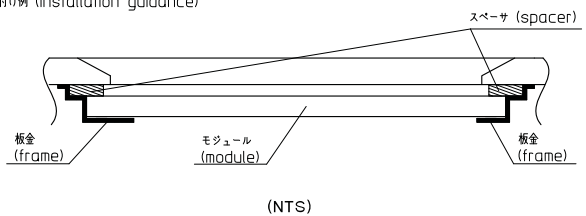
部品実装エリア12(FPC自身の反発力で変形しない引き出しをしてください)
Mounting area 12(please do not deform in the resilience of its routing FPC)

セット側コネクタ
(The set of connectors)
セット側基板(The board set)
セット側コネクタ
(The set of connectors)

セット側コネクタ
(The set of connectors)



※取り付け例 (Installation guidance)



△ ※裏面の押さえつけ可能領域以外 (押さえつけ禁止エリアを除く) を押さえると、取付構造によっては、表示ムラが発生する場合があります。
しかしながら、GND接続等で押さえる必要がある場合は、25mm2 (5mm×5mm) に対して、0.33kgf (3.2N) を目安として下さい。
なお、上記値は、参考数値であり、実際の取付構造により十分評価頂きますようお願いいたします。

(※ If area other than pressure available area on the rear side is pressured, there is a case that display variation is caused.
However, when pressure is needed for GND connection or so, 0.33kgf (3.2N) per 25mm2 (5mm×5mm) will be a referential figure.
Above figure is reference only. Therefore, please evaluate well under the actual installation structure.)

取り付け方法
(Installation of the LCD)

ケースにモジュールをはめ込み(X,Y固定)、裏面からZ方向を固定する事が可能。
(The LCD module shall be held in the X/Y direction by the housing, and in the Z direction using a backboard.)

斜線部の領域にて、下記の条件を満たし押さえることが望ましい。
(To hold the LCD module in place, it shall be supported with pressure applied to the hatched areas indicated by the descriptions below.)

△ 押さえつけ可能領域
(Pressure may be applied in this area.)

押さえつけ可能領域面積 (裏面) : 1308.0mm2
(Pressure may be applied in this area (Back side) : 1308.0mm2)
参考全体荷重 : 4.4kgf (43.6N)
(Referential total load : 4.4kgf (43.6N))
参考平均圧力 : 3.4gf/mm2 (33.7kPa)
(Referential average pressure : 3.4gf/mm2 (33.7kPa))
※上記値は参考数値であり、実際の取付構造により十分評価頂きますようお願いいたします。
(Above figure is reference only. Therefore, please evaluate well by the actual installation structure.)

押さえつけ不可領域
(Pressure failure area.)

表面取り付け条件
(Installation conditions (Front side))

1.広い範囲で均一に押さえることが望ましい
(1. The LCD shall be uniformly supported over as wide an area as possible.)

裏面取り付け条件
(Installation conditions (Back side))

1.FPC部品実装エリアへの接触無きこと
(1. Do not allow any foreign material to contact the FPC Component Area.)
2.モジュールがたわむこと無きよう押さえつけること
(2. Do not allow the LCD module to bend or twist.)
3.広い範囲で均一に押さえることが望ましい
(3. Support the LCD with uniform pressure over as wide an area as possible.)

注記 (Note)
指定外公差 (Tolerance without indication) : ±0.5

Material 材質	Treatment 処理	Approved '10.11.30	Checked '10.11.30	Drawn 蔭山	Scale 1:1(NTS)	Title 3.5inch QVGA	Year—Month—Day '10.11.29	Size 2
Quantity 製作数	Description 備考	朝倉	朝倉	朝倉	蔭山	Module Installation	Drawing No. 10282-03224-2	

Spec No.	TQ3C-8EAF0-E2YAD65-00
Date	July 20, 2023

KYOCERA INSPECTION STANDARD

TYPE : TCG035QVLAANN-AN00

KYOCERA CORPORATION

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
July 20, 2023	<i>N. Yamawaki</i>	<i>T. Fukui</i>	<i>A. Iwasaki</i>	<i>T. Sawada</i>

Spec No.

TQ3C-8EAF0-E2YAD65-00

Part No.

TCG035QVLAAANN-AN00

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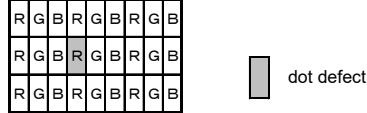
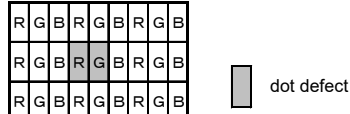
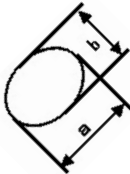
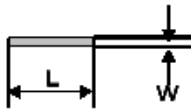
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Revision record

Date		Designed by : Engineering dept.			Confirmed by : QA dept.
		Prepared	Checked	Approved	Approved
Rev.No.	Date	Page	Descriptions		

Visuals specification

1) Note

	Note		
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area. 3. Inspection conditions Luminance : 500 Lux min. Inspection distance : 300 mm. Temperature : 25 ± 5°C Direction : Directly above		
Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly “on” when power applied to the LCD, even when all “Black” data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter. 
		Black dot defect	The dot is constantly “off” when power applied to the LCD, even when all “White” data sent to the screen. Similar size compared to bright dot.
		White dot (Circular/foreign particle)	Pixel works electrically, however, circular/foreign particle makes dot appear to be “on” even when all “Black” data is sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects. 
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixels “Black” or “White”) and non operating.
		Appearance inspection	Does not satisfy the value at the spec.
	Definition of size	Definition of circle size  a: major axis, b: minor axis $d = (a + b) / 2$ Definition of linear size 	

2) Standard

Classification		Inspection item		Judgement standard		
Defect (in LCD glass)	Single dot	Bright dot defect		Acceptable number : 4 Bright dot spacing : 5 mm or more		
		Black dot defect		Acceptable number : 5 Black dot spacing : 5 mm or more		
	Adjacent dot	2 dots	Bright dot defect	Acceptable number : 2		
			Black dot defect	Acceptable number : 3		
		3 or more dots		Acceptable number : 0		
	Total dot defects		Acceptable number : 5 Max			
	Others	White dot, Dark dot (Circle)				
				Size (mm)		Acceptable number
d ≦ 0.2				(Neglected)		
0.2 < d ≦ 0.4				5		
0.4 < d ≦ 0.5				3		
0.5 < d				0		
External inspection (Defect on Polarizer or between Polarizer and LCD glass)	Polarizer (Scratch)					
			Width (mm)		Length (mm)	Acceptable number
			W ≦ 0.1		—	(Neglected)
			0.1 < W ≦ 0.3		L ≦ 5.0	(Neglected)
					5.0 < L	0
	Polarizer (Bubble)					
			Size (mm)		Acceptable number	
			d ≦ 0.2		(Neglected)	
			0.2 < d ≦ 0.3		5	
			0.3 < d ≦ 0.5		3	
	Foreign particle (Circular shape)					
			Size (mm)		Acceptable number	
			d ≦ 0.2		(Neglected)	
			0.2 < d ≦ 0.4		5	
			0.4 < d ≦ 0.5		3	
	Foreign particle (Linear shape) Scratch					
			Width (mm)		Length (mm)	Acceptable number
			W ≦ 0.03		—	(Neglected)
0.03 < W ≦ 0.1			L ≦ 2.0	(Neglected)		
			2.0 < L ≦ 4.0	3		
0.1 < W		4.0 < L	0			
		—		(According to circular shape)		
Color variation (Mura)						
		Not to be significantly visible. Consultation shall be held as necessary.				

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Date	July 20, 2023

KYOCERA PACKAGING STANDARD

TYPE : TCG035QVLAANN-AN00

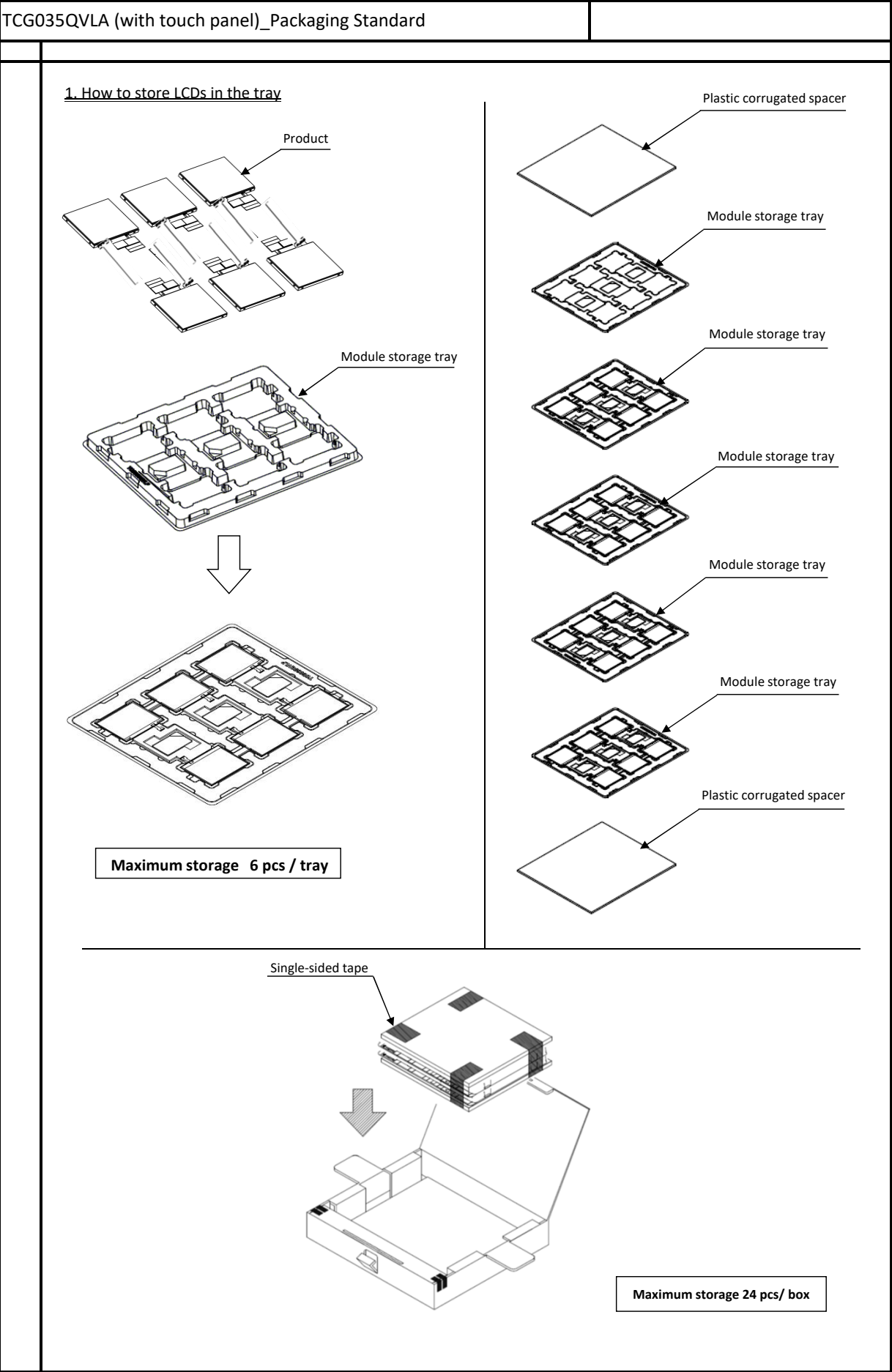
KYOCERA CORPORATION

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
July 20, 2023	<i>N. Yamawaki</i>	<i>T. Fukui</i>	<i>A. Iwasaki</i>	<i>T. Sawada</i>

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Revision record

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		Prepared	Checked	Approved	Approved
Rev.No.	Date	Page	Descriptions		



TCG035QVLA (with touch panel)_Packaging Standard

2. How to store LCDs in the outer case

PP band

Shock absorbing material

Confirmation label

Outer case

Maximum storage 96 pcs/ outer case

客先 (Customer)				京セラ株式会社 KYOCERA CORPORATION
品名 (Parts No.)				
数量 (QTY)				
検査者 (Check)				
検査者 (Check)				

3. Location of the labels

PP band

Single-sided tape

Shipping label

Confirmation label

客先 (Customer)				京セラ株式会社 KYOCERA CORPORATION
品名 (Parts No.)				
数量 (QTY)				
検査者 (Check)				
検査者 (Check)				

CONFIDENTIAL

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