



FSM:GO – IMX900

3.2MP Global Shutter optical sensor module with Sony Pregius S image sensor.

What is the FSM:GO?

- Family of ready to mount image sensor modules
- Paired with a standard lens from the selected range
- Focused to your specific working distance
- Assembled with your interface choice
- Simple and off-the-shelf

Contact Information

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Product Specification - Overview

Sensor Module

Type	Sony IMX900AQR/AMR
Shutter Type	CMOS Global Shutter
Technology	Pregius S
Spectrum	Mono or Color (RGB)
Optical Format	1/3.1"
Pixel Size	2.25 x 2.25 μm
Resolution (max.)	2064 x 1552
Framerate (max.)*	8bit: 125 FPS 10Bit: 117 FPS 12Bit: 72 FPS
Bit Depth*	8/10/12 bit

*Platform dependent

Platform Support (Driver, ISP)

NVIDIA	NVIDIA Orin Family
NXP	i.MX8MP

Lens Option(s)

Coming soon



Interface Option(s)

Data Interface	MIPI CSI-2 v1.2 and D-PHY v2.1	GMSL3 (12Gbps)
Physical Interface	PixelMate (60 pin) Micro Coax (50 pin) FFC (40 pin) (unshielded)	FAKRA (Single Coax)
Communication	I ² C	I ² C
Power Supply	Two Rails (3.8V, 1.8V)	Power over Coax (12V)
Power Cons. (Max)	420mW	1200mW

Mechanical Specification

Dimensions [mm]	26.5 (W) x 26.5 (D) x 2.77 (H)
Housing	None/Boardlevel
Lens Mount	M12 x 0.5 (var. options)

Applications:

- Barcode scanning
- Drones - AGV and AGVs
- Industrial robotics
- Automated inspection

Environmental

Ingress Protection	None
Operating Temperature	-30° to +75°C*
Storage Temperature	-40° to +85°C*
Ambient Humidity	Max 95% RH, non-condensing

*Sensor module without lens, check [Lens Options](#) for configurations with Lens.

The comprehensive specifications are outlined within the contents of this datasheet.



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1 Order Code Scheme/Options

FSM:GO-IMX900-[LM]-[L]-[FS][FD]-[IF]-[MH]-A1Q1

- **LM:** Lens Holder (Optional)
- **M12B:** 12.5 mm Height
- **L:** Lens Type (Optional)
 - Coming soon
- **FS:** Focusing Service (Optional)
 - FCS: Center Focusing (L: Required)
- **FD:** Focusing Distance
 - HYP: Hyperfocal Distance`0
 - xxxx: Custom Distance in mm (FS: Required)
- **IF:** Interface (Mandatory)
 - PM: PixelMate (60 pin)
 - MC50A: MicroCoax (50 pin)
 - FFC40A: Flat-Flex Cable (40 pin)
 - GMSL3A: GMSL3 (FAKRA, Single Coax)
- **MH:** Mechanicals/Housing
 - None: Board Level



2 Lens Options

Coming Soon

3 Focusing Service and Focusing Distance

Specification Description	No Focusing ¹	Focusing to Hyperfocal	Focusing to Custom Distance
Type (Code)	-	FCSHYP	FCS[FD]
Focus Distance	N/A	Hyperfocal Distance [HYP] ²	Custom Focus Distance [FD] ³
Focus Target	N/A	Virtual Image (Collimator)	Virtual Image (Collimator)
Focus Area ⁴	N/A	Image Center	Image Center
Lens Fixation	N/A	Permanent (Epoxy)	Permanent (Epoxy)

¹ Lens is properly screwed into the lens mount without focusing and fixation.
² Please see “Hyperfocal Distance [m]” in Focusing Specifications – Table 2.2.
³ Please see “Min. Object Distance [m]” to infinity in Focusing Specifications – Table 2.2.
⁴ Please see Figure 3.1 below showing the virtual target at image center as the focus area.

Focus Area

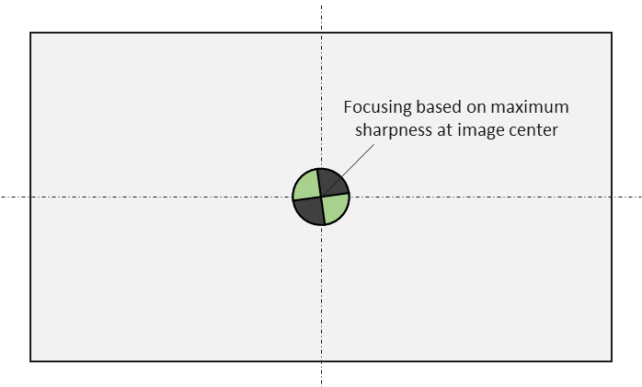


Figure 3.1



4 Lens Mount Options

Specification	
Type (Code)	M12B
Type (PN)	FLA-MI-M-12522-00
Thread Type	M12 x 0.5
Height [mm]	12.5
Barrel Diameter (Outer) [mm]	13.8
Thread Length	7.9
Mounting Hole Distance [mm]	22
Mounting Screw Size	M1.6
Material	ABS
Appearance [Color]	Black
Mating Std. Lens (Code)	-



5 Interface Options

Type (Code)	PM	MC50A	FFC40A	GMSL3A
Description	PixelMate	Micro-Coax	Flat-Flex Cable	GMSL/SerDes
FSM:GO Integration	Default FSM:GO Interface	Adapter Board (Piggyback)	Adapter Board (Piggyback)	Adapter Board (Piggyback)
Interface Standard	FRAMOS PixelMateC	Proprietary	Proprietary	GMSL, FAKRA Coaxial
Connector Type	Hirose DF40C-60DP-0.4V(51)	I-PEX 20525-050E-02	Molex 5051104091	Amphenol RF 2FA1-NZSP-PCBB6
Pin Count [#]	60	50	40	1+GND
Pin Pitch [mm]	0.4mm	0.4mm	0.5mm	-
Locking Style	Self-locking	Mechanical locking	Mechanical locking	Mechanical locking
Shielding	Yes	Yes	No	Yes
Power Supply [V]	3V8, 1V8	3V8, 1V8	3V8, 1V8	12V (Power over Coax)
Data Lanes/Bandwidth	4-Lanes, 2.5 Gbps (ea.)	4-Lanes, 2.5 Gbps (ea.)	4-Lanes, 2.5 Gbps (ea.)	1-Line at 12Gbps (10Gbps Effective)
Mating Connector	Hirose DF40C-60DS-0.4V(51)	I-PEX 20525-050E-02	Molex 5051104091	Amphenol RF 2FA1-NZSP-PCBB6
Mating Cable(s)	FMA-FC-150/60	FMA-CBL-MC50-0.3m	Molex 0150200440	FMA-CBL-FAK.LD302
Receiver Boards	Various FPAs	FFA-MC50/A	FFA-FFC40/A	FFA-GMSL-DES-V2

5.1 Pinouts

WARNING Pin 1 is identified on the board. Orient accordingly, paying close attention to the pin number in reference to the locator view illustrated below. Failure to align correctly will cause permanent damage.

PixelMate:

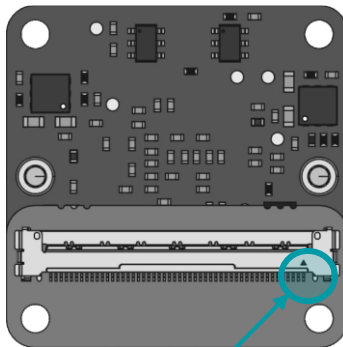
Type: Hirose DF40C-60DP-0.4V(51)

PIN 1

Pinout

1	3V8_VDD	2	1V8_VDD
3	3V8_VDD	4	1V8_VDD
5	NC	6	NC
7	NC	8	NC
9	NC	10	NC
11	GND	12	GND
13	GND	14	GND
15	RST_0	16	NC
17	NC	18	NC
19	XMASTER	20	GPO1
21	I2C_SCL	22	NC
23	NC	24	SLAMODE2
25	XVS	26	GPO2
27	I2C_SDA	28	NC
29	XHS	30	XTRIG2
31	XTRIG1	32	GPO0
33	NC	34	NC

35	SLAMODE0	36	SLAMODE1
37	GND	38	GND
39	NC	40	NC
41	NC	42	NC
43	GND	44	GND
45	NC	46	D_DATA_3_P
47	NC	48	D_DATA_3_N
49	GND	50	GND
51	D_DATA_0_N	52	D_DATA_1_N
53	D_DATA_0_P	54	D_DATA_1_P
55	GND	56	GND
57	D_DATA_2_P	58	D_CLK_0_P
59	D_DATA_2_N	60	D_CLK_0_N

MC50:**Type: I-PEX 20525-050E-02**

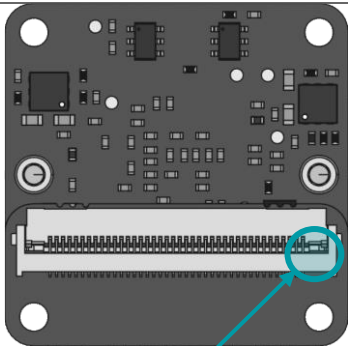
PIN 1

Pinout

1	GND	26	I2C_SDA
2	D_CLK_0_N	27	GPO2
3	D_CLK_0_P	28	XVS
4	GND	29	SLAMODE2
5	D_DATA_2_N	30	NC
6	D_DATA_2_P	31	I2C_SCL
7	GND	32	GPO1
8	D_DATA_1_P	33	XMASTER
9	D_DATA_1_N	34	NC
10	GND	35	NC
11	D_DATA_0_P	36	NC
12	D_DATA_0_N	37	RST_0
13	GND	38	GND
14	D_DATA_3_N	39	NC
15	D_DATA_3_P	40	NC
16	GND	41	NC
17	NC	42	NC
18	SLAMODE1	43	NC
19	SLAMODE0	44	NC
20	NC	45	GND
21	NC	46	1V8_VDD
22	GPO0	47	1V8_VDD
23	XTRIG1	48	GND
24	XTRIG2	49	3V8_VDD
25	XHS	50	3V8_VDD

FFC40:**Type: Molex 5051104091****Pinout**

1	GND	21	XTRIG1
2	D_CLK_0_N	22	XHS
3	D_CLK_0_P	23	XVS
4	GND	24	I2C_SDA
5	D_DATA_2_N	25	I2C_SCL
6	D_DATA_2_P	26	XMASTER
7	GND	27	RST_0
8	D_DATA_1_P	28	NC
9	D_DATA_1_N	29	NC



PIN 1

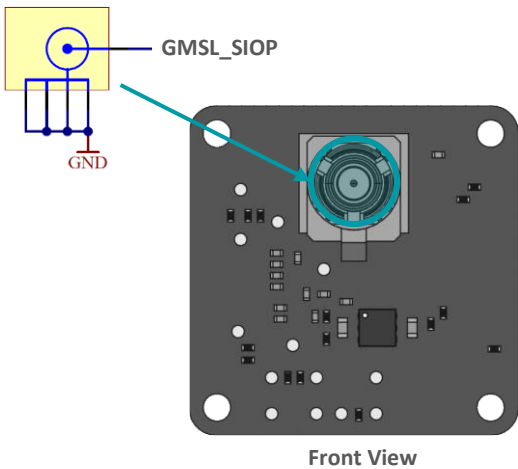
10	GND	30	NC
11	D_DATA_0_P	31	NC
12	D_DATA_0_N	32	NC
13	GND	33	NC
14	D_DATA_3_N	34	GND
15	D_DATA_3_P	35	1V8_VDD
16	GND	36	1V8_VDD
17	NC	37	GND
18	GND	38	3V8_VDD
19	NC	39	3V8_VDD
20	NC	40	GND

GMSL3A:

Type: 2FA1-NZSP-PCBB6

Pinout

1	GMSL_SIOF
2	GND



Front View

5.2 Signal Description

3V8_VDD

I/O Type Power
Function 3.8V Power supply
Connected to LDO_IC
DC Charact. 3V8_VDD=3.7V-5.1V,
max. 0.3A per line

1V8_VDD

I/O Type Power
Function 1.8V Power supply
Connected to LDO_IC
DC Charact. 1V8_VDD=1.7V-1.9V,
max. 0.3A per line

IS_RST_0

I/O Type	IN
Function	Image sensor reset (XCLR)
Connected to	Reset_IC
I/O Standard	LVC MOS18 (1.8V)
I/O State	Normal: High, Reset: Low
DC Charact.	VILmax=0.36V, VIHmin=1.44V

SLAMODE0/1/2

I/O Type	IN
Function	I2C slave address configuration
Connected to	Image Sensor, pull-down res. 10k
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

I2C_SCL

I/O Type	IN/OUT
Function	I2C clock
Connected to	Image Sensor, no pull-up
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

I2C_SDA

I/O Type	IN/OUT
Function	I2C data
Connected to	Image Sensor, no pull-up
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

XMASTER

I/O Type	IN
Function	Image sensor master/slave
Connected to	Image Sensor, pull-down res. 10k
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

XVS / XHS

I/O Type	IN/OUT
Function	Vertical/Horizontal sync signal
Connected to	Image Sensor, pull-up res. 10k
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

XTRIG1/2

I/O Type	IN
Function	Trigger input 1/2
Connected to	Image Sensor
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

D_DATA_#_P/N

I/O Type	OUT
Function	MIPI-CSI2 output data (Lane #, Positive/Negative)
Connected to	Image sensor
I/O Standard	MIPI D-PHY

D_CLK_0_P/N

I/O Type	OUT
Function	MIPI-CSI2 output clock (Positive/Negative)
Connected to	Image sensor
I/O Standard	MIPI D-PHY

GPO0/1/2

I/O Type	OUT
Function	Digital Output
I/O Standard	LVC MOS18 (1.8V)
DC Charact.	VILmax=0.54V, VIHmin=1.26V

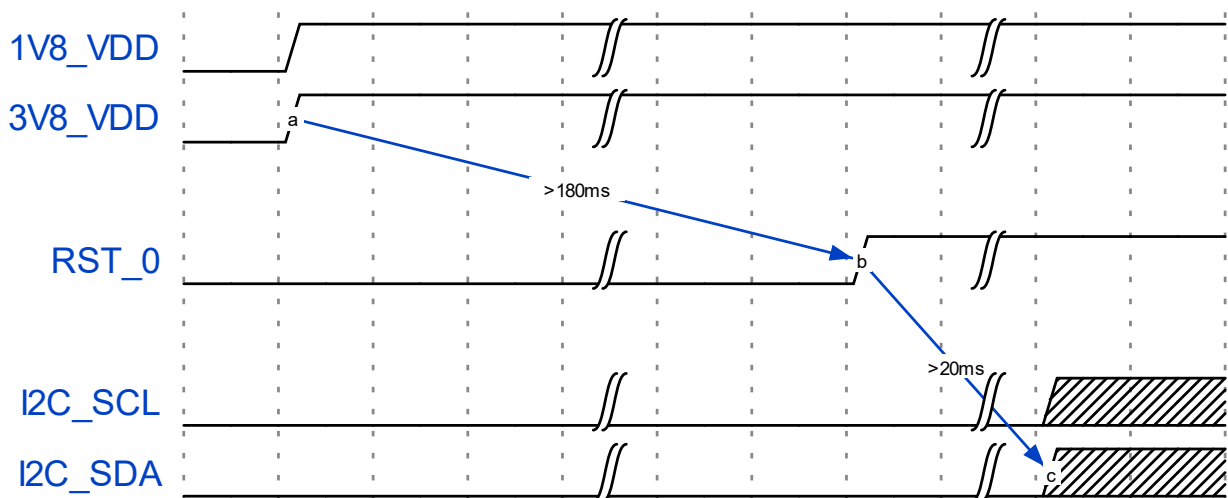
5.3 I2C Address Configuration (SLAMODE)

SLAMODE0	SLAMODE1	SLAMODE2	I2C Address
0	0	0	0x36
0	0	1	0x37
0	1	0	0x38
0	1	1	0x39
1	0	0	0x3A
1	0	1	0x3B
1	1	0	0x3C
1	1	1	0x3D

Note: 1st slave address is common in all SLAMODE signals: **0x1A**

5.4 Power-On Sequencing

For correct function, the host system must follow the below timing to properly power up or reset the module: 3V8_VDD should be generated after 1V8_VDD, or, ideally at the same time. RST_0 pin low after powering up PixelMate voltage rails >180 ms.



Legend

Times and voltages which are represented in the above figure are as follows:

- 3V8_VDD - 3V8 voltage supply from host
- 1V8_VDD - 1V8 voltage supply from host
- RST_0 - reset signal driven from the host
- I2C_SCL - I2C Clock
- I2C_SDA - I2C Data

6 Mechanical Drawings

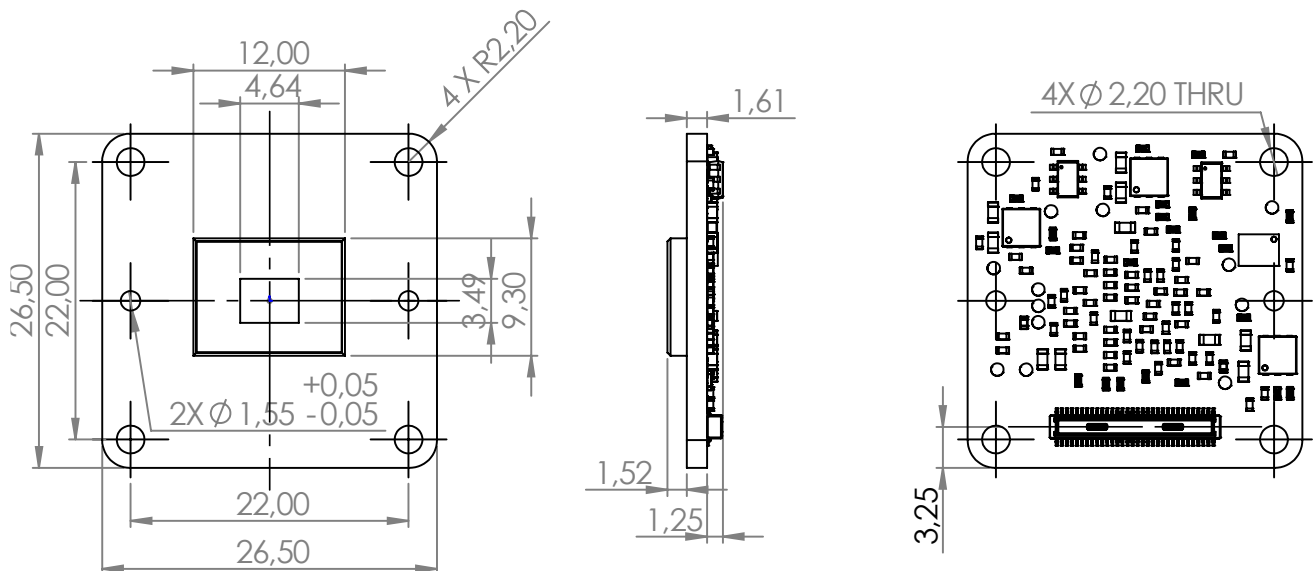
The following chapter contains the measured drawings split into the core segments of the product.

- Bare Sensor Module
- with Lens
- with Lens Mount only
- with Interface Adapter

All measures refer to the backside of the sensor module PCB and allow this way to obtain the overall measures. Unit of measurements is millimeters [mm].

6.1 Sensor Module (Bare FSM:GO)

Interface: PixelMate

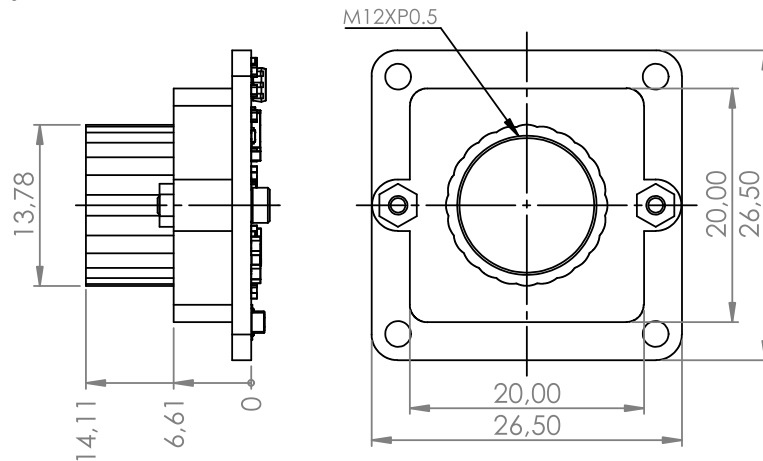


6.2 FSM:GO with Lens

Coming soon

6.3 FSM:GO with Mount

M12B: 12.5 mm Height

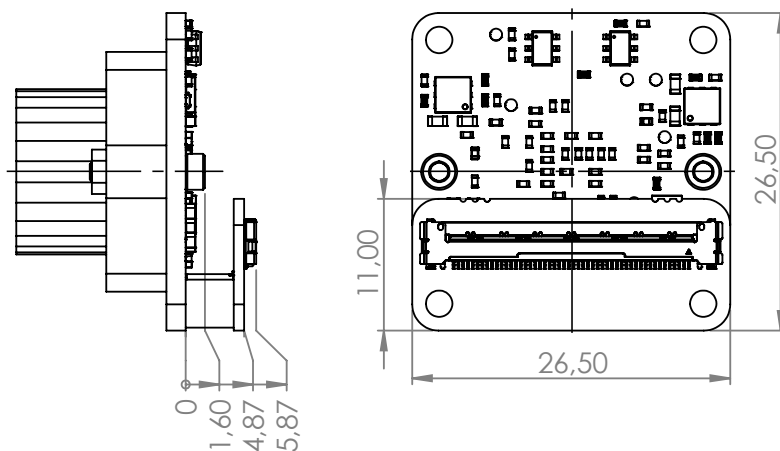


6.4 FSM:GO with Interface Adapter

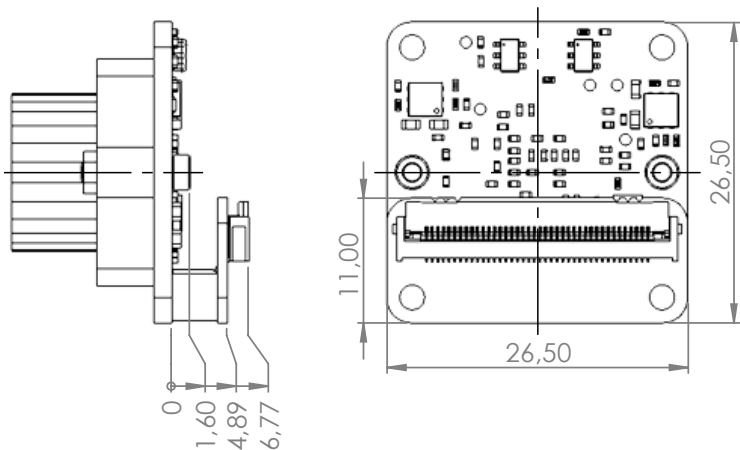
PM: See bare "Sensor Module"

NOTE Interface adapters fixed with screws for transport, mounting screws not included in product photos and drawings.

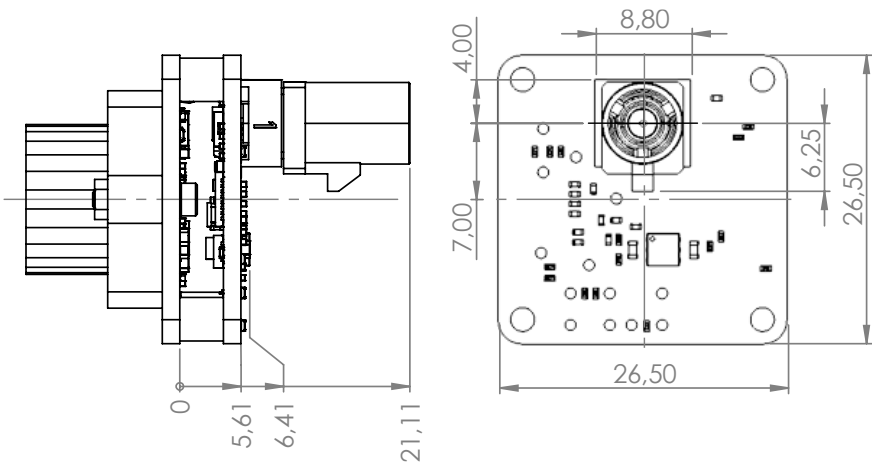
MC50A: MicroCoax (50 pin)



FFC40A: Flat-Flex Cable (40 pin)



GMSL3A (FAKRA, Single Coax)





7 Platform and Software Specification

7.1 Nvidia Jetson Family Support

Note: The specifications are valid as of driver version 3.1.0.

Supported Platforms:

Jetson AGX Orin Devkit			Jetson Orin Nano and Jetson Orin NX Devkits	
Image sensor	Support (Native)	GMSL	Support (Native)	GMSL
IMX900	+	+	+	+

Supported Features:

Mode	Resolution	Description	1lane 8b	1lane 10b	1lane 12b	2lane 8b	2lane 10b	2lane 12b	4lane 8b	4lane 10b	4lane 12b
0	2064x1552	FULL-PIX	79	64.7	54.8	125.1	117	72	125.1	117	72
1	1920x1080	CROP	109.5	90	76.4	171.2	160.5	100	171.2	160.5	100
2	1032x776	SUBSAMPLING 1/2 (Color)	222.5	215.8	136.6	222.5	215.8	136.6	222.5	215.8	136.6
		SUBSAMPLING 1/2 (Monochrome)	255.9	217.1	187.8	396.5	351.3	249.4	396.5	376.3	249.4
3	2064x154	SUBSAMPLING 1/10	485.6	419.7	368.7	655.7	631.5	452.5	655.7	631.5	452.5
4	1024x720	BINNING + CROP	255.9	217.1	187.8	396.5	351.3	249.4	396.5	376.3	249.4

Implemented Sensor Features:

Sensor Feature	Support
Gain (Analog/Digital)	+
Frame Rate	+
Exposure	+
Flip/Mirror	-
IS Mode (Master/Slave)	+
Sensor Mode ID	+
Test Pattern	+
Black Level	+
HDR	-
Data Rate	+
Synchronizing (Master/Slave)	-
Fast Trigger Mode	+

Supported ISP Features:

Image Signal Processing Feature	Support
Demosaic	+
Black Level Compensation	+
Bad Pixel Correction	+
Color Correction	+
Auto White Balance (A, TL84, D65)	+
Manual White Balancing	+
Lens Shading/Falloff Correction	+
Noise Reduction	+
Sharpening	+
Auto Exposure, Gain, Gamma, Color/Tone, Contrast Tuning	+

7.2 NXP i.MX8MP Support

Supported Platforms:

i.MX 8M PLUS Devkit			
Image sensor	Driver	Support (Native)	GMSL
IMX900	IMX900	+	+

Supported Features:

Mode	Resolution	Description	4lane/10b [FPS]	4lane/12b [FPS]
0	2048 x 1536	FULL_PIX	113.7	72
1	1920x1080	CROPPING MODE	155	100
2	1024x768	Vertical / Horizontal 1/2 subsampling (COLOR)	215.9	136.6
2		Vertical / Horizontal 1/2 subsampling (MONO)	330.5	249.4
3	2048x154	Vertical 1/10 subsampling	617.7	452.5
4	1008x704	Binning + Cropping (MONO)	348.6	264

Implemented Features:

Supported Features	FSM:GO-IMX900
Gain (Analog/Digital)	+
Frame Rate	+
Exposure	+
Flip/Mirror	-
IS Mode (Master/Slave)	-
Test Pattern	+
Black Level	+
HDR	-



Broadcast	-
Data Rate	+
Synchronizing Master mode	+

FSM:GO



Contact Information

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Technical Support:

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