



## Product Change Notification: SYST-26WZXJ970

**Date:**

20-Oct-2025

**Product Category:**

Wireless IC, Wireless Modules

**Notification Subject:**

PIC32CX-BZ6 Family Silicon Errata

**Affected CPNs:**

[SYST-26WZXJ970\\_Affected\\_CPN\\_10202025.pdf](#)

[SYST-26WZXJ970\\_Affected\\_CPN\\_10202025.csv](#)

**Notification Text:**

SYST-26WZXJ970

Microchip has released a new Document for the PIC32CX-BZ6 Family Silicon Errata of devices. If you are using one of these devices please read the document located at [PIC32CX-BZ6 Family Silicon Errata](#).

**Notification Status:** Final

**Description of Change:**

Revision B

Added the following erratum to sections "Silicon Errata Summary" and "Silicon Errata Issues":

1. Analog-to-Digital Converter (ADC)
  - a. Glitches in ADC Conversion Result
2. Configurable Custom Logic (CCL)
  - a. The LUT Output is Corrupted after Enabling the CCL when using Sequential Logic
  - b. Interrupt Access to Reserved Area
3. Event System (EVSYS)
  - a. Software Event
  - b. Spurious Overrun

4. External Interrupt Controller (EIC)
  - a. Asynchronous Edge Detection
5. General-Purpose Input/Output (GPIO)
  - a. GPIO Port Value Unstable in Debug and Freeze Mode
6. Peripheral Access Controller (PAC)
  - a. PAC Protection Error in FREQM
7. Real-Time Counter and Calendar (RTCC)
  - a. ACTL Detection not Gated by rtc\_enable
  - b. Write Corruption
  - c. Tamper Detection Timestamp
  - d. General Purpose Register
  - e. COUNTSYNC
8. Serial Communication Interface (SERCOM)
  - a. SERCOM-I2C: Client Error Interrupt INTFLAG.ERROR Repeated Start
  - b. SERCOM-I2C: I2C in Client Mode
  - c. SERCOM-I2C: Client Mode with DMA
  - d. SERCOM-USART: Collision Detection
  - e. SERCOM-USART: Error Interrupts
  - f. SERCOM-USART: Flow Control in 32-Bit Extension Mode
  - g. SERCOM-USART: SERCOM USART in TX Mode Only
  - h. SERCOM-SPI: Client Data Preload
  - i. SERCOM-SPI: Data Preload
  - j. SERCOM-SPI: Hardware Client Select Control
  - k. SERCOM-I2C Client: I2C DRDY Raised when no Data is to be Fed
9. Supply Voltage and Power Mode
  - a. GPIO Change Notification Interrupt not Firing After Sleep
10. Timer/Counter for Control Applications (TCC)
  - a. 2RAMP Mode with Hi-resolution Reference Multiple Restarts
  - b. RAMP2 Feature in Down-Counting Mode

**Impacts to Data Sheet:** None

Reason for Change: To improve productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 20 October 2025

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

**Attachments:****PIC32CX-BZ6 Family Silicon Errata**

Please contact your local [\*\*Microchip sales office\*\*](#) with questions or concerns regarding this notification.

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