



Product Change Notification: SYST-19XMP851

Date:

15-Jan-2025

Product Category:

32-Bit Microcontrollers

Notification Subject:

PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications

Affected CPNs:

SYST-19XMP851_Affected_CPN_01152025.pdf

SYST-19XMP851_Affected_CPN_01152025.csv

Notification Text:

SYST-19XMP851

Microchip has released a new Document for the PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at **PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications**.

Notification Status: Final

Description of Change:

Added the following new errata:

- Device: 2.4.4 Performance Level 0 Mode (PLO)

Impacts to Data Sheet: None

Change Implementation Status: Complete

Date Document Changes Effective: 15 Jan 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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