MICROCHIP

# Product Change Notification / SYST-05WEWN441

## Date:

30-Sep-2022

## Product Category:

Microprocessors

## PCN Type:

Document Change

## Notification Subject:

ERRATA - SAMA5D29 Silicon Errata and Data Sheet Clarification

## Affected CPNs:

SYST-05WEWN441_Affected_CPN_09302022.pdf
SYST-05WEWN441_Affected_CPN_09302022.csv

## Notification Text:

SYST-05WEWN441

Microchip has released a new Errata for the SAMA5D29 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at SAMA5D29 Silicon Errata and Data Sheet Clarification.

**Notification Status:** Final

**Description of Change:** Updated 1. Silicon Issue Summary. Added in 11. Secure Digital MultiMedia Card Controller (SDMMC): - 11.3. SDMMC I/O calibration does not work Deleted errata: - Fuse matrix programming requires a main clock (MAINCK) frequency between 10 and 15 MHz - Fuse matrix read requires a main clock (MAINCK) frequency below 28 MHz

**Impacts to Data Sheet:** None

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 30 Sep 2022

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

SAMA5D29 Silicon Errata and Data Sheet Clarification

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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