

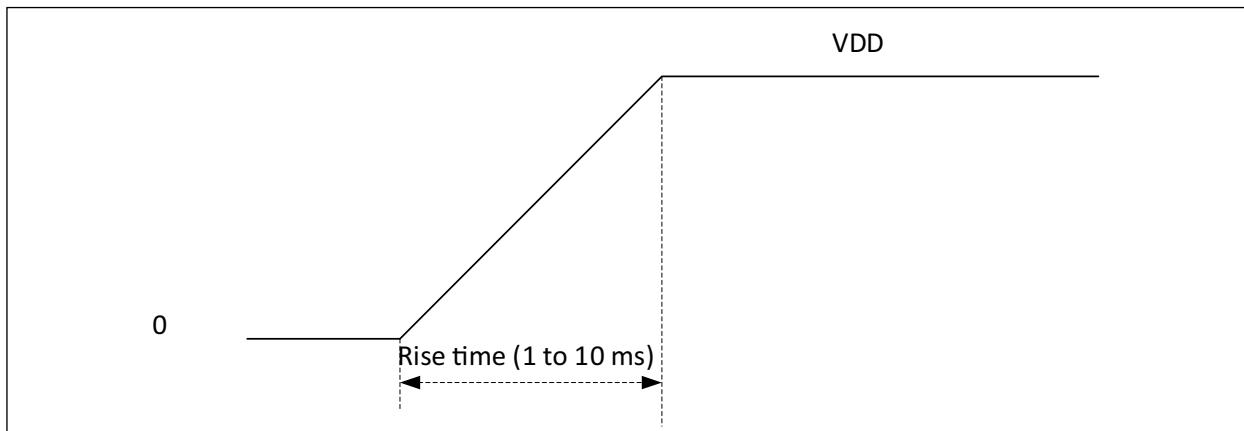
SY75608/612 and SYA75608/612 Silicon Errata

This Errata documents an issue with SY75608/612 and SYA75608/612 devices that have the silicon revision number in the Device Identification Register (Address 0x0B), bits 6:3 set to 4'b0001.

1.0 START-UP ISSUE

1.1 Problem Description

Internal logic in SY75608/612 and SYA75608/612 devices might not start properly on the power-up, which will result in disabled outputs. This occurs for about 1% of devices when the power-up ramp is between 1 ms and 10 ms.



1.2 Affected Silicon Revisions

Device Identification Register at Address 0x0B, Silicon Revision Number bits: 6:3.

TABLE 1-1: AFFECTED SILICON REVISIONS

Device Revision	Code	Affected/Unaffected
A3	4'b0001	Affected
A4	4'b0010	Unaffected

1.3 Work Around

If you do not see any problem with the power-up in your system, the issue can be ignored assuming that the device powers up at a similar ambient temperature. For devices that are affected by this problem, the work around is to use the new silicon (Revision 4'b0010). Please contact your local sales office so they may direct you to this solution.

1.4 Fix

New production devices with the Silicon Revision Number: 4'b0010 have eliminated this issue.

APPENDIX A: REVISION HISTORY

Revision A (November 2023)

- Initial release to inform of a new silicon release (A4) that resolves the start-up issue.

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