MICROCHIP

Product Change Notification / SYST-19LEID651

Date:

28-Aug-2021

Product Category:

Interface- LIN Transceiver

PCN Type:

Document Change

Notification Subject:

ERRATA - SAMHAxGxxA-B Errata Errata Document Revision

Affected CPNs:

SYST-19LEID651_Affected_CPN_08282021.pdf
SYST-19LEID651_Affected_CPN_08282021.csv

Notification Text:

SYST-19LEID651

Microchip has released a new Product Documents for the SAMHAxGxxA-B Errata of devices. If you are using one of these devices please read the document located at SAMHAxGxxA-B Errata.

Notification Status: Final

Description of Change:
• Adding 128 kB version of the device.
• The SPI and I2C standards use the terminology "Master" and "Slave". The equivalent Microchip terminology used in this document is "Host" and "Client" respectively. These terms have been updated throughout this document for this revision.
• The following Silicon errata were added in this revision:
    • ADC: 1.4.1 Offset Correction
    • DEVICE: 1.5.5 Standby Entry
    • DEVICE: 1.5.6 Standby Wake-up
    • SYSCTRL: 1.15.3 BOD33 hysteresis

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 28 Aug 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SAMHAxGxxA-B Errata

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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