



Product Change Notification / SYST-19LAID193

Date:

20-Oct-2020

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - AVR128DB28/32/48/64 Silicon Errata and Data Sheet Clarifications

Affected CPNs:

[SYST-19LAID193_Affected_CPN_10202020.pdf](#)
[SYST-19LAID193_Affected_CPN_10202020.csv](#)

Notification Text:

SYST-19LAID193

Microchip has released a new Product Documents for the AVR128DB28/32/48/64 Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [AVR128DB28/32/48/64 Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final

Description of Change:**Added errata:**

1) Device: Increased Current Consumption May Occur When VDD Drops 2) CLKCTRL: The PLL Will Not Run When Using XOSCHF With an External Crystal 3) TCB: CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode 4) TCD: Asynchronous Input Events Not Working When TCD Counter Prescaler is Used and CMPAEN Controls All WOx For Alternative Pin Functions

Updated errata: 1) CCL: The CCL Must be Disabled to Change the Configuration of a Single LUT and The LINK Input Source Selection for LUT3 is Not Functional on 28- and 32- pin Devices 2) ZCD: All ZCD Output Selection Bits are Tied to the ZCD0 Bit

Added data sheet clarification: Added Typical Characteristics section with additional plots for OPAMP peripheral.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 20 Oct 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[AVR128DB28/32/48/64 Silicon Errata and Data Sheet Clarifications](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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