



Product Change Notification / SYST-18SFAM011

Date:

24-Nov-2020

Product Category:

8-bit Microcontrollers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - AVR128DA28/32/48/64 Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-18SFAM011_Affected_CPN_11242020.pdf](#)
[SYST-18SFAM011_Affected_CPN_11242020.csv](#)

Notification Text:

SYST-18SFAM011

Microchip has released a new Product Documents for the AVR128DA28/32/48/64 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [AVR128DA28/32/48/64 Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

This revision includes the following updates to Data Sheet Clarifications:

- 1) Add new device revision (A8)
- 2) Added errata:
 - Device: Some Reserved Fuse Bits Are '1'
 - Device: CRC Check During Reset Initialization Is not Functional
 - CCL: The LINK Input Source Selection for LUT3 Is not Functional on 28- and 32-Pin Device
 - RSTCTRL: BOD Registers not Reset When UPDI Is Enabled
 - TCA: Restart Will Reset Counter Direction in NORMAL and FRQ Mode
 - TCB: CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode
 - TCD: Asynchronous Input Events not Working When TCD Counter Prescaler Is Used
 - TCD: CMPAEN Controls All WOx for Alternative Pin Functions
 - USART: Start-of-Frame Detection Can Unintentionally Be Enabled in Active Mode When RXCIF Is '0'

– ZCD: All ZCD Output Selection Bits Are Tied to the ZCD0 Bit

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 24 Nov 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[AVR128DA28/32/48/64 Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to [receive Microchip PCNs via email](#) please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to [change your PCN profile, including opt out](#), please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.