



## Product Change Notification: SYST-18DLZI542

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**Date:**

11-Mar-2026

**Product Category:**

32-Bit Microcontrollers

**Notification Subject:**

PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications

**Affected CPNs:**

[SYST-18DLZI542\\_Affected\\_CPN\\_03112026.pdf](#)

[SYST-18DLZI542\\_Affected\\_CPN\\_03112026.csv](#)

**Notification Text:**

SYST-18DLZI542

Microchip has released a new Document for the PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications](#).

**Notification Status:** Final

**Description of Change:**

Added the following new errata:

- SERCOM I2C: 2.12.2 I2C Host Mode

**Impacts to Data Sheet:** None

**Reason for Change:** To improve productivity.

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 11 Mar 2026

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## **Attachments:**

### **PIC32CM LE00/LS00/LS60 Family Silicon Errata and Data Sheet Clarifications**

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

## **Terms and Conditions:**

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