



Product Change Notification / SYST-18CWHD261

Date:

24-Nov-2020

Product Category:

8-bit Microcontrollers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - AVR64DA28/32/48/64 Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-18CWHD261_Affected_CPN_11242020.pdf](#)

[SYST-18CWHD261_Affected_CPN_11242020.csv](#)

Notification Text:

SYST-18CWHD261

Microchip has released a new Product Documents for the AVR64DA28/32/48/64 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [AVR64DA28/32/48/64 Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

This revision includes the following updates to Data Sheet Clarifications:

- 1) Add new device revision (A4)
- 2) Added errata:
 - Device: Some Reserved Fuse Bits Are '1'
 - Device: CRC Check During Reset Initialization Is not Functional
 - RSTCTRL: BOD Registers not Reset When UPDI Is Enabled
 - TCA: Restart Will Reset Counter Direction in NORMAL and FRQ Mode
 - TCB: CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode
 - TCD: Asynchronous Input Events not Working When TCD Counter Prescaler Is Used
 - USART: Start-of-Frame Detection Can Unintentionally Be Enabled in Active Mode When RXCIF Is '0'

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 24 Nov 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

AVR64DA28/32/48/64 Silicon Errata and Data Sheet Clarification

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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