MICROCHIP

Product Change Notification / SYST-15IXFZ775

Date:

16-Jul-2021

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - SAM L10/L11 Family Silicon Errata and Data Sheet Clarification Document Revision

Affected CPNs:

SYST-15IXFZ775_Affected_CPN_07162021.pdf
SYST-15IXFZ775_Affected_CPN_07162021.csv

Notification Text:

SYST-15IXFZ775

Microchip has released a new Product Documents for the SAM L10/L11 Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at SAM L10/L11 Family Silicon Errata and Data Sheet Clarification.

Notification Status: Final

Description of Change:
The SPI, I2S, and I2C standards use the terminology "Master" and "Slave". The equivalent Microchip terminology used in this document is "Host" and "Client" respectively. These terms have been updated throughout this document for this revision.

This revision contains numerous typographical updates, and formatting updates.

Obsolete Data Sheet Clarifications have been removed in this update.

The following errata were added in this revision:

• RTC: 2.9.12 Active Layer Protection

• SERCOM I2C: 2.10.11 Automatic Acknowledge

• TC: 2.13.4 PER register (8-bit mode)

• EVSYS: 2.18.3 Spurious Overrun

• EVSYS: 2.18.4 Software Event

• EVSYS: 2.18.5 PAC Write-Protection

• OSCCTRL: 2.19.4 FDPLL96M On Demand in Standby

• OSCCTRL: 2.19.5 DFLLULP Dithering Mode

• NVMCTRL: 2.22.1 Data FLASH Silent Access and Scrambling

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 15 Jul 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SAM L10/L11 Family Silicon Errata and Data Sheet Clarification

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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