



## Product Change Notification / SYST-13LRGR126

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### Date:

14-Apr-2021

### Product Category:

8-bit Microcontrollers

### PCN Type:

Document Change

### Notification Subject:

ERRATA - ATtiny804/806/807/1604/1606/1607 Silicon Errata and Data Sheet Clarification

### Affected CPNs:

[SYST-13LRGR126\\_Affected\\_CPN\\_04142021.pdf](#)

[SYST-13LRGR126\\_Affected\\_CPN\\_04142021.csv](#)

### Notification Text:

SYST-13LRGR126

Microchip has released a new Product Documents for the ATtiny804/806/807/1604/1606/1607 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [ATtiny804/806/807/1604/1606/1607 Silicon Errata and Data Sheet Clarification](#).

**Notification Status:** Final

#### Description of Change:

1) Initial document release

The content of the document has been restructured from:

- ATtiny804/1604 Silicon Errata and Data Sheet Clarification
- ATtiny806/1606 Silicon Errata and Data Sheet Clarification
- ATtiny807/1607 Silicon Errata and Data Sheet Clarification

to:

- ATtiny804/806/807/1604/1606/1607 Silicon Errata and Data Sheet Clarification (this document)

Refer to 4.2 Appendix - Obsolete Revision History for further details.

The following items are referring to changes between the latest revisions of the obsolete documents and this document:

- Added new errata:
  - Device: 2.2.1 Writing the OSCLOCK Fuse in FUSE.OSCCFG to '1' Prevents Automatic Loading of Calibration Values



- CCL: 2.4.3 The CCL Must be Disabled to Change the Configuration of a Single LUT
- TCA: 2.6.1 Restart Will Reset Counter Direction in NORMAL and FRQ Mode
- TCB: 2.7.3 CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode
- USART:
  - 2.8.2 Open-Drain Mode Does Not Work When TXD is Configured as Output
  - 2.8.3 Start-of-Frame Detection Can Unintentionally Be Triggered in Active Mode
  - Removed old data sheet clarifications, as the corresponding data sheet has been updated with correct information

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 14 Apr 2021

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[ATtiny804/806/807/1604/1606/1607 Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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