## Product Change Notification / SYST-09BDKE406

## Date:

10-Dec-2021

## Product Category:

System On Chip FPGAs

## PCN Type:

Document Change

## Notification Subject:

ERRATA - SmartFusion®2 M2S050 (T, TS) Errata Revision

## Affected CPNs:

SYST-09BDKE406_Affected_CPN_12102021.pdf
SYST-09BDKE406_Affected_CPN_12102021.csv

## Notification Text:

SYST-09BDKE406

Microchip has released a new Product Documents for the SmartFusion®2 M2S050 (T, TS) Errata of devices. If you are using one of these devices please read the document located at SmartFusion®2 M2S050 (T, TS) Errata

**Notification Status:** Final

**Description of Change:** The document was updated with the following changes:
  • Converted the document to Microchip standards
  • Added 2.17. Arm® Cortex®-M3 Register Corruption Under Very Specific Literal Loads from eNVM and Cache Miss/Hit Sequences

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 10 December 2021

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

# Attachments:

SmartFusion®2 M2S050 (T, TS) Errata

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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