



Product Change Notification / SYST-04SBWB795

Date:

06-Oct-2021

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - ATtiny1614/1616/1617 Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-04SBWB795_Affected_CPN_10062021.pdf](#)

[SYST-04SBWB795_Affected_CPN_10062021.csv](#)

Notification Text:

SYST-04SBWB795

Microchip has released a new Product Documents for the ATtiny1614/1616/1617 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [ATtiny1614/1616/1617 Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

- Added erratum:
 - TCD: 2.9.4 Halting TCD and Wait for SW Restart Does Not Work if Compare Value A is '0' or Dual Slope Mode is Used
- Added data sheet clarifications:
 - Memories: 3.1.1 Fuses - Factory Default Values
 - SLPCTRL: 3.2.1 Sleep Mode Activity Overview
 - USART: 3.3.1 TXDATA Buffer
- Updated erratum:
 - TWI : 2.10.1 TIMEOUT Bits in the TWI.MCTRLA Register are Not Accessible
- Removed invalid erratum:
 - Start-of-Frame Detection Can Unintentionally Be Triggered in Active Mode
- Editorial updates

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 6 Oct 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[ATtiny1614/1616/1617 Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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