



## Product Change Notification / SYST-04FAPV128

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### Date:

06-Nov-2020

### Product Category:

8-bit Microcontrollers

### PCN Type:

Silicon Die Revision

### Notification Subject:

ERRATA - ATtiny416/816 Silicon Errata and Data Sheet Clarification

### Affected CPNs:

[SYST-04FAPV128\\_Affected\\_CPN\\_11062020.pdf](#)  
[SYST-04FAPV128\\_Affected\\_CPN\\_11062020.csv](#)

### Notification Text:

SYST-04FAPV128

Microchip has released a new Product Documents for the ATtiny416/816 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [ATtiny416/816 Silicon Errata and Data Sheet Clarification](#).

### Notification Status:

Final

#### Description of Change:

- 1) Added die revision C for ATtiny416
- 2) Updated Affected Silicon Revisions for ADC Interrupt Flags Cleared When Reading RESH
- 3) Added new errata:
  - Device: Writing the OSCLOCK Fuse in FUSE.OSCCFG to '1' Prevents Automatic Loading of Calibration Values
    - CCL: The CCL Must be Disabled to Change the Configuration of a Single LUT
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    - TCA: Restart Will Reset Counter Direction in NORMAL and FRQ Mode
    - TCB: CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode
    - TCD: Asynchronous Input Events Not Working When TCD Counter Prescaler is Used
    - USART:
      - Full Range Duty Cycle Not Supported When Validating LIN Sync Field
      - Open-Drain Mode Does Not Work When TXD is Configured as Output

- Start-of-Frame Detection Can Unintentionally be Enabled in Active Mode when RXCIF is '0'

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Estimated First Ship Date:** 06 Nov 2020

**NOTE:** Please be advised that after the estimated first ship date customers may receive pre and post change parts.

**Markings to Distinguish Revised from Unrevised Devices:** Traceability Code

## **Attachments:**

[ATtiny416/816 Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

## **Terms and Conditions:**

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