



Product Change Notification / SYST-01YTFY734

Date:

05-Apr-2021

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - SAM D21 / DA1 Family Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-01YTFY734_Affected_CPN_04052021.pdf](#)

[SYST-01YTFY734_Affected_CPN_04052021.csv](#)

Notification Text:

SYST-01YTFY734

Microchip has released a new Product Documents for the SAM D21 / DA1 Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [SAM D21 / DA1 Family Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

The following Data Sheet Clarifications were added in this revision:

- 2.1 Voltage Reference Names
- 2.2 DAC Accuracy Characteristics - conversion rate
- 2.3 Decoupling Requirements
- 2.4 Moisture Sensitivity Level

The following Silicon errata were added in this revision:

- ADC: 1.4.2 Offset Correction
- DEVICE: 1.5.7 Standby Entry
- DEVICE: 1.5.8 Standby Wake-up

The SPI, I2S, and I2C standards use the terminology "Master" and "Slave". The equivalent Microchip terminology, "Host" and "Client", is used in this document. These terminologies have been updated throughout this document for this revision.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 05 Apr 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM D21 / DA1 Family Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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