

Title of Change:	Correction of NCV78703 datasheet	
Effective date:	25 Jun 2025	
Contact information:	Contact your local onsemi Sales Office	
Type of notification:	This Product Bulletin is for notification purposes only. onsemi will proceed with implementation of this change upon publication of this Product Bulletin.	
Change Category:	Datasheet/Product Doc change	
Change Sub-Category(s):	Datasheet/Product Doc change	
Sites Affected:		
onsemi Sites		External Foundry/Subcon Sites
None		None
Description and Purpose:		
Correction of the NCV78703 datasheet at the following points:		
<div><div>-</div><div>SPI SDO data in write frame after POR description updated with (page 26):<div><div>o</div><div>After power-on-reset all bits are zero.</div></div></div><div>-</div><div>BSTSYNC pin debouncing ("debounce of two clock cycles on the signal edges") not implemented in reality and its description removed from datasheet (page 16)</div><div>-</div><div>OTP MAP table (Table 27 on page 31) updated with missing registers for 3rd booster phase.</div></div>		
No change has been made to the physical part. It is correction of the datasheet.		
List of Affected Standard Parts:		
<b>Note:</b> Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the <u><b>PCN Customized Portal</b></u> .		
NCV78703MW1AR2G	NCV78703MW0AR2G	