

1 PCN Information

Title	Product Change Notification		
PCN Number	PCN-2003-TCWL-0002	Issue Date (yyyy-mm-dd)	2019-11-15
PCN Revision	0.3	Effective Date (yyyy-mm-dd)	2019-11-15
Refers to Change Order	N/A	Expiration Date (yyyy-mm-dd)	N/A
Refers to Change Request	N/A	Authors	CJR

2 Product Identification

2.1 Product Data

	Pre-Conversion	Post-Conversion
Product Name	conga-TC370	conga-TC370
Product Number P/N	N/A	N/A
Serial Number S/N	N/A	N/A
Lot Number	N/A	N/A
Hardware Revision	N/A	N/A
BIOS Revision	BxWLR122	BxWLR029
congatec Board Controller Revision	N/A	N/A

2.2 Means of Distinguishing Changed Products

		Remark
Product Number	<input type="checkbox"/>	
Hardware Revision	<input type="checkbox"/>	
Lot Code	<input type="checkbox"/>	
Label	<input type="checkbox"/>	
Other	<input checked="" type="checkbox"/>	BIOS revision is displayed in the BIOS setup menu and on the POST screen.

3 Description and Reason of Change

AVL	<input type="checkbox"/>	Product Improvement	<input checked="" type="checkbox"/>
Material	<input type="checkbox"/>	BIOS Release	<input checked="" type="checkbox"/>
Assembly Process	<input type="checkbox"/>	Firmware Release	<input type="checkbox"/>
Assembly BOM	<input type="checkbox"/>	Datasheet/Manual	<input type="checkbox"/>
Testing	<input type="checkbox"/>	DFM	<input type="checkbox"/>
Manufacturing Site	<input type="checkbox"/>	Other	<input type="checkbox"/>
Equipment	<input type="checkbox"/>		

Note

***BIOS releases BxWLR023, BxWLR027 and BxWLR029 are not used for production.
Intermediate BIOS releases do not exist.***

BxWLR027 to BxWLR029

1. Added support for the SMBIOS structure type12 (system configuration).
2. Added the congatec BIOS POST and setup screen license information text.
3. Added code that always programs the LVDS clock enable bits in the LVDS Interface Control 1 register of the PTN3460 LVDS bridge. This is a workaround for the unreliable hardware default configuration of this register.
4. Adapted the _UPC and _PLD ACPI methods to the USB hardware routing.

BxWLR023 to BxWLR027

1. Updated AMI BIOS Tree from label **1AUOK046** (May 2019) to **1AUOK062** (Oct. 2020). This update supports MCU revision DEh, Intel ME FW version 12.0.71.1681 and RAID Driver version Intel(R) RST 17.8.0.4414.
2. Legacy Video BIOS updated to version 1030 and Gop driver updated to version 9.0.1109.
3. Ensured that all setup menus and pages, such as the UEFI network stack and other EDK modules, are included in the congatec menu layout file (MLF) and thus can be handled by the congatec MLF editor and viewer tools.
4. Updated the UefiNetworkStack setup support module to support CGUTIL modifications of these variables.
5. Added GPIO configuration and GPI IRQ support.

Product Change Notification

6. Changed the default value of the *PS Current Threshold1* (in Power & Performance - CPU-Power Management Control – CPU VR Settings – Core/IA VR Settings) from *80* to *0* for better load balancing.
7. Disabled and removed RTD3 support for all PCI Express slots.
8. Fixed the problem that a fixed LVDS panel dataset was not applied on the first boot.
9. Added support to configure the LVDS output voltage swing in BIOS setup.
10. Changed the test mode SATA RX equalization boost magnitude adjustment to be the same value used for normal operation.

BxWLR122 to BxWLR023

1. Fixed an issue with Windows 10 version 1903 whereby the UEFI boot option that is created will be marked as inactive when an existing Windows 10 installation is booted on a new system, resulting in Windows not being able to boot.
2. Updated to the latest Intel Microcode Update (MCU) revision C6h.
3. Added a workaround for certain WWAN modules (M.2 key B) that caused a system hang during POST. The workaround is enabled via the new setup option *WWAN Reset Workaround*.

4 Impact of the change (positive or negative) on fit, form, function & reliability

4.1 Quality and Reliability

The changes listed in section 3 improve the product quality and reliability performance.

4.2 Software Compatibility

Fully software compatible with previous revision.

BVWLR029.BIN must be used for Core i5 and i7 product variants.

BUWLR029.BIN must be used for Core i3 and Celeron product variants.

4.3 Hardware Compatibility

Fully hardware compatible with the previous revision.



Note

The change described within this PCN does not affect the functionality of the modules. congatec is not obligated to perform an update on modules that have already been delivered or on modules that are pending delivery unless explicitly mentioned herein. If you have any questions about this matter please contact your local congatec AG representative.

5 Product/Revision Discontinuation

All previous product revisions are set to EOL status (End-Of-Life).

After the release of this PCN there will be a transition time of 6 months whereby a delivery may consist of products based on previous revisions as well as new revisions. After the completion of the transition time, only products based on the new revision will be delivered. This does not include products that fall under the RMA process.

6 Contact Information

congatec Contact	Technical Solution Center
Fax	+49-991-2700-111
eMail	support@congatec.com

Product Change Notification

7 Customer Acknowledgment of Receipt

congatec AG records indicate that you require written notification of this change. Fill in the acknowledgment below and eMail this document to the eMail address above to request additional information. If congatec AG does not receive acknowledgment within 30 days of this notice it will be assumed that this change is acceptable. congatec AG reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

Customer Name	
Title	
Phone	
Fax	
eMail	

8 Revision History

Revision	Date (yyyy-mm-dd)	Author	Changes
0.1	2019-11-15	CJR	Initial release of document.
0.2	2021-01-13	CJR	Updated for the R027 BIOS release.
0.3	2021-08-13	CJR	Updated for the R029 BIOS release.

Product Change Notification

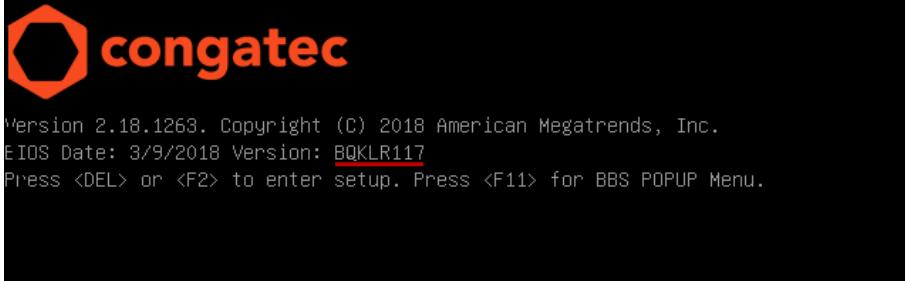
9 Appendix: congatec revision description

The product information can be derived from the congatec product label:



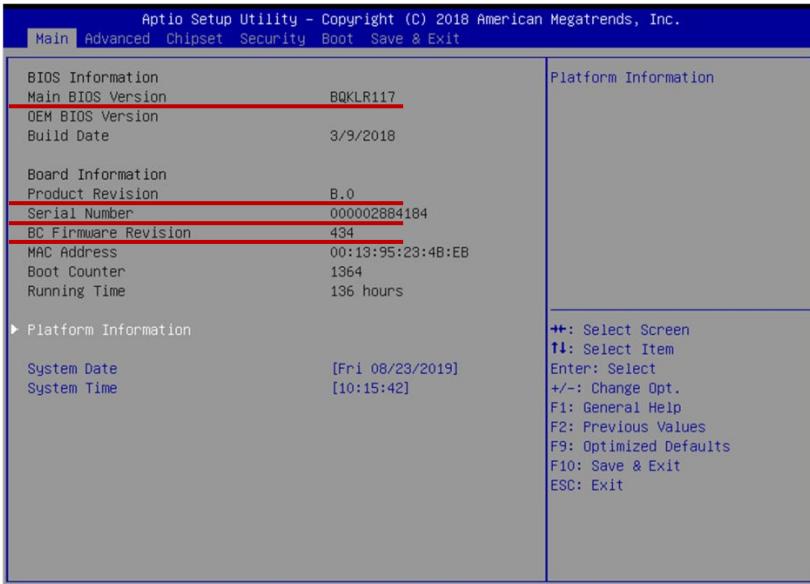
The BIOS can be determined during BIOS POST. The Main BIOS Version, Product Revision, Serial Number and Firmware Revision (Board Controller Firmware) can be determined in the BIOS Setup Utility (Main Menu):

BIOS POST



Version 2.18.1263. Copyright (C) 2018 American Megatrends, Inc.
 BIOS Date: 3/9/2018 Version: BQKLR117
 Press or <F2> to enter setup. Press <F11> for BBS POPUP Menu.

BIOS Setup Utility



Aprio Setup Utility - Copyright (C) 2018 American Megatrends, Inc.

Main		Advanced	Chipset	Security	Boot	Save & Exit
BIOS Information		Platform Information				
Main BIOS Version		BQKLR117				
OEM BIOS Version						
Build Date		3/9/2018				
Board Information						
Product Revision		8.0				
Serial Number		000002884184				
BC Firmware Revision		434				
MAC Address		00:13:95:23:4B:EB				
Boot Counter		1364				
Running Time		136 hours				
► Platform Information						
System Date		[Fri 08/23/2019]				
System Time		[10:15:42]				
++: Select Screen ††: Select Item Enter: Select +/−: Change Opt. F1: General Help F2: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit						

Version 2.18.1263. Copyright (C) 2018 American Megatrends, Inc.