

PCN

AO-PCN-2022-017-A

Introduction of 2nd source for classic InGaAlP chip for Micro SIDELED

01.07.2022

Dear Customer,

please review this **PCN** and provide your feedback in the **Customer approval form** (at the end of this PCN document) to your ams OSRAM sales partner before **05.08.2022** *).

Please take note, that this PCN is published for the introduction of **additional source(s)**.

Your prompt reply will help ams OSRAM to assure a smooth and well executed transition. If ams OSRAM does not hear from your side by the due date, we will assume your (if you are a Distributor: and your customer's) full acceptance to this proposed change and its implementation.

ams OSRAM understands the time requirements your organization needs to approve this PCN. However, if you can provide ams OSRAM an estimated date your organization will have finalized this PCN review, ams OSRAM can use this date to plan continued production to secure your order needs during the expansion with additional source(s).

Your attention and response to this matter is highly appreciated.

Please direct your inquiries to your local Sales office.

*) ams OSRAM aligns with the widely recognized JEDEC/ECIA/IPC Joint Standard No. 46, which stipulates:

- Customers should acknowledge receipt of the PCN within 30 days of delivery of the PCN.
- Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.
- After acknowledgement, lack of additional response within the 90 day period constitutes acceptance of the change. If the customer requires additional time to perform sample testing, beyond the 90 day review period, an extension must be negotiated with the supplier.

Subject of change:	Introduction of 2 nd source for classic InGaAlP chip for Micro SIDELED	
Affected products:	LS Y876, LY Y876, LO Y876, LG Y876	
Reason for change:	<ul style="list-style-type: none"> • Secure continuous supply • Introduction of additional supplier chips 	
Description of change:	<u>Current status</u> Inhouse chips	<u>New status</u> Inhouse chips + 2 nd (and 3 rd) source chips provided by supplier(s)
	For details refer to file 2_cip_AO-PCN-2022-017-A	
Time schedule for PCN material: (after implementation of change):	Final qualification report:	01.08.2022
	Samples available:	01.08.2022 ^{*)} *) For details refer to file 2_cip_AO-PCN-2022-017-A
	Intended Start of delivery:	01.12.2022 ^{**)} ^{**)} or earlier if released by customer and upon mutual agreement
	Customer Review Finalization:	01.07.2023 ^{***)} ^{***)} Expected final feedback of customer. Released order volume is related to deliveries of material from both previous and additional source(s).
Assessment:	No change in fit, form and reliability → no change in Datasheets	
Documentation:	Customer information package 2_cip_AO-PCN-2022-017-A 3_cip_AO-PCN-2022-017-A_Qual	

Note:

Pre-PCN material: Products of current status, means before implementation of the changes as described in the PCN.

PCN material: Products with implementation of the changes as described in the PCN.

Customer approval form AO-PCN-2022-017-A

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Please list product(s) affected in your application(s):

Please check the appropriate box below:

- | | |
|--|---|
| <input type="radio"/> Approval:
We agree with the proposed change and accept start of the shipment upon availability of PCN material | <input type="radio"/> Not relevant:
Change is not relevant for products in use. |
|--|---|

Change cannot be accepted:

- We have objections:**
- We request following Information:**
- We request following Samples:**
- Expected approval date:**
- Volume requirements for Pre-PCN material:**

Remarks:

Sender:

Company:

Address / Location:

Signature:

Date:

Please return this approval form to your Sales partner.

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PCN

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SIDELED

Customer information package

S&MK EM FQE/OS Q CQM A ITR
2022-07-01

Agenda

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Reason for change

Item	Description
1.	Secure continuous supply
2.	Introduction of additional supplier chips

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Description of change for standard chips

Item	Current status	2 nd source chip A	2 nd source chip B	2 nd source chip C
Wafer size [mm]	100	100		
Wafer substrate	GaAs	GaAs		
Height [μm]	220	180		
Chip dicing process	Sawing	Sawing		
Picture (schematic)				
Chip size [μm]	200 x 200	200 x 200	180 x 180	180 x 180
Front metal type	Al	Au		
Front metal thickness [μm]	1.5	2.25 - 2.9		
Back metal type	Au	Au		
Back metal thickness [μm]	0.25	0.05 - 0.50		
Bond pad size [μm]	100	100		

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Changes in the datasheets

No change in fit, form and function of affected devices → no change in Datasheets

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List of affected products

Brand	Standard
Micro SIDELED	LS Y876
	LO Y876
	LY Y876
	LG Y876

Due to complexity not all device/chip source combinations will be available at start of series production.

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PCN Samples

Standard
LS Y876
LO Y876
LY Y876
LG Y876

Color code:  available on 01.08.2022  on request

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Time schedule

for PCN material (<u>after</u> implementation of change):		
Final qualification report	01.08.2022	
Samples available	01.08.2022	
Intended Start of delivery	01.12.2022*)	*) or earlier if released by customer and upon mutual agreement
Customer Review Finalization:	01.07.2023 **)	***) Expected final feedback of customer. Released order volume is related to deliveries of material from both previous and additional source(s).

Note:

PCN material: Products with implementation of the changes as described in the PCN.

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