



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

PCN# 20250730007.2

**Qualification of RFAB using qualified Process Technology, additional Assembly Site & BOM options for select devices
Change Notification / Sample Request**

Date: July 30, 2025

To: MOUSER PCN

Dear Customer:

This is an announcement of a change to a device that is currently offered by Texas Instruments (TI). The details of this change are on the following pages, and are in alignment with our standard product change notification (PCN) [process](#).

TI requires acknowledgement of receipt of this notification within 60 days of the date of this notice. Lack of acknowledgement of this notice within 60 days constitutes acceptance and approval of this change. If samples or additional data are required, requests must be received within 60 days of this notification, given that samples are not built ahead of the change.

The Proposed First Ship date in this PCN letter is the earliest possible date that customers could receive the changed material. It is our commitment that the changed device will not ship before that date. If samples are requested within the 60 day sample request window, customers will still have 30-days to complete their evaluation regardless of the proposed 1st ship date.

Changes outlined in this notification underscore our commitment to product longevity and supply continuity, as well as our continued efforts to transition to newer, more efficient manufacturing processes and technologies. Specifically, this particular notification is related to TI's multiyear transition plan for our two remaining 150-millimeter production lines (DFAB in Dallas, Texas, and SFAB in Sherman, Texas). SFAB closure activities are expected to begin by the end of 2025. DFAB will remain open with a smaller set of 200mm technologies and GaN.

For questions regarding this notice or to provide acknowledgement of this PCN, you may contact your local Field Sales Representative or the Change Management team. For sample requests or sample related questions, contact your local Field Sales Representative. As always, we thank you for your continued business.

TI values customer engagement and feedback related to TI changes. Customers should contact TI if there are questions or concerns regarding a change notification.

Change Management Team
SC Business Services

20250730007.2
Attachment: 1

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
LMV341QDCKRQ1G4	NULL

Technical details of this Product Change follow on the next page(s).

PCN Number:	20250730007.2	PCN Date:	July 30, 2025
Title:	Qualification of RFAB using qualified Process Technology, Die Revision and additional Assembly Site & BOM options for select devices		
Customer Contact:	Change Management team	Dept:	Quality Services
Proposed 1st Ship Date:	January 26, 2026	Sample requests accepted until:	September 28, 2025*
*Sample requests received after September 28, 2025 will not be supported.			
Change Type:			
<input checked="" type="checkbox"/> Assembly Site	<input checked="" type="checkbox"/> Design	<input type="checkbox"/>	Wafer Bump Material
<input checked="" type="checkbox"/> Assembly Process	<input type="checkbox"/> Data Sheet	<input type="checkbox"/>	Wafer Bump Process
<input checked="" type="checkbox"/> Assembly Materials	<input type="checkbox"/> Part number change	<input checked="" type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/> Mechanical Specification	<input checked="" type="checkbox"/> Test Site	<input checked="" type="checkbox"/>	Wafer Fab Materials
<input checked="" type="checkbox"/> Packing/Shipping/Labeling	<input checked="" type="checkbox"/> Test Process	<input checked="" type="checkbox"/>	Wafer Fab Process

PCN Details

Description of Change:

Texas Instruments is pleased to announce the addition of RFAB using the LBC9 qualified process technology and additional Assembly/Test Site) and BOM options for the devices listed below.

Current Fab Site			Additional Fab Site		
Current Fab Site	Process	Wafer Diameter	Additional Fab Site	Process	Wafer Diameter
DFAB	A21	150 mm	RFAB	LBC9	300 mm

The die was also changed as a result of the process change.

	Current	New
Probe Site	DL-LIN	None

Construction differences are as follows:

Group 1

	Current Site	Additional Site
Assembly Site	UTL2	PHI
Wire diam/type	Au 1.0 mil	Cu 0.8 mil
Mold Compound	SID#CZ0096	4222198
Mount Compound	SID#PZ0001	8095733

Group 2

	Current Site	Additional Site
Assembly Site	UTL2	HFTFAT
Wire diam/type	Au 1.0 mil	Cu 0.8 mil
Mold Compound	SID#CZ0096	SID#R-27
Mount Compound	SID#PZ0037	SID#A-18
Final Wafer Thickness	8 mils	7.5 mils

The datasheets will be changing as a result of the above mentioned changes. The datasheet change details can be reviewed in the datasheet revision history. The links to the revised datasheets are available in the table below.

Changes from Revision C (June 2009) to Revision D (July 2025)		Page
• Updated the numbering format for tables, figures, and cross-references throughout the document.....		1
• Changed supply current per channel specification at V+ = 2.7V from 100µA to 150µA (typical) and from 170µA to 200µA (maximum at 25°C).....		6
• Changed recommended shutdown pin voltage range, shutdown mode specification at V+ = 2.7V from 0 to 0.8V to 0 to 0.2V (maximum).....		7
• Changed supply current per channel specification at V+ = 5V from 100µA to 150µA (typical).....		8
• Changed recommended shutdown pin voltage range, shutdown mode specification at V+ = 5V from 0 to 0.8V to 0 to 0.2V (maximum).....		9

Product Folder	Current Datasheet Number	New Datasheet Number	Link to full datasheet
LMV341-Q1	SGLS342C	SGLS342D	http://www.ti.com/product/LMV341-Q1

Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ.

Qual details are provided in the Qual Data Section.

Reason for Change:

These changes are part of our multiyear plan to transition products from our 150-millimeter and 200-millimeter factories to newer, more efficient manufacturing processes and technologies, underscoring our commitment to product longevity and supply continuity.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings:

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
<input checked="" type="checkbox"/> No Change			

Changes to product identification resulting from this PCN:

Fab Site Information:

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
DL-LIN	DLN	USA	Dallas
RFAB	RFB	USA	Richardson

Die Rev:

Current

New

Die Rev [2P]	Die Rev [2P]
A	A

Assembly/Test Site Information:

Group 1

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
UTL2	NS2	THA	Bangpakong, Chachoengsao
PHI	PHI	PHL	Baguio City

Group 2

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
UTL2	NS2	THA	Bangpakong, Chachoengsao
HFTF	HFT	CHN	Hefei

Sample product shipping label (not actual product label)

**Product Affected- Group 1:**

LMV341QDBVRQ1

Product Affected- Group 2:

LMV341QDCKRQ1G4

For alternate parts with similar or improved performance, please visit the product page on TI.com

Automotive Qualification Summary
(As per AEC and JEDEC Guidelines)Q006 {SOT} at {HFTFAT}
Approve Date 16-July-2024

Attributes		Qual Device: <u>LMV341QDCKRQ1</u>	QBS Process Reference: <u>TLV7031QDCKRQ1</u>	QBS Package Reference: <u>SN74AXC1T45QDCKRQ1</u>	QBS Package Reference: <u>TPS22919QDCKRQ1</u>
Automotive Grade Level		Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)		-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function		Signal Chain	Signal Chain	Logic	Power Management
Wafer Fab Supplier		RFAB	RFAB	MH8	RFAB
Assembly Site		HFTFAT	HFTFAT	HFTFAT	HFTFAT
Package Group		SOT	SOT	SOT	SOT
Package Designator		DCK	DCK	DCK	DCK
Pin Count		6	5	6	6

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>LMV341QDCKRQ1</u>	QBS Reference: <u>TLV7031QDCKRQ1</u>	QBS Reference: <u>SN74AXC1T45QDCKRQ1</u>	QBS Reference: <u>TPS22919QDCKRQ1</u>
Test Group A - Accelerated Environment Stress Tests											
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	-	3/Pass	3/Pass	3/Pass
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	-	3/66/0	3/66/0	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	-	3/66/0	3/66/0	3/66/0
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	110C/85%RH	264 Hours	-	-	3/231/0	-
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	3/231/0	-	3/231/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	-	3/3/0	3/3/0	3/3/0
HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	-	-	3/9/0	3/9/0	3/9/0
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	-	-	3/9/0	3/9/0	3/9/0
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	-	-	3/9/0	3/9/0	3/9/0
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	110C/85%RH	528 Hours	-	-	3/231/0	-
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C/85%RH	192 Hours	-	3/210/0	-	3/210/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	-	3/66/0	3/66/0	3/22/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	-	3/3/0	3/3/0	3/3/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: LMV341QDCKRQ1	QBS Reference: TLV7031QDCKRQ1	QBS Reference: SN74AXC1T45QDCKRQ1	QBS Reference: TPS22919QDCKRQ1
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	-	3/9/0	3/9/0	3/9/0
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	-	3/9/0	3/9/0	3/9/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	-	3/9/0	3/9/0	3/9/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	3/231/0	3/231/0	3/231/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	-	3/66/0	3/66/0	3/66/0
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	-	3/3/0	3/3/0	3/3/0
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	-	3/9/0	3/9/0	3/9/0
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	-	3/9/0	3/9/0	3/9/0
TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	-	3/9/0	3/9/0	3/9/0
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	-	3/210/0	3/210/0	3/210/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	-	3/66/0	3/66/0	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	-	3/3/0	3/3/0	3/3/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: LMV341QDCKRQ1	QBS Reference: TLV7031QDCKRQ1	QBS Reference: SN74AXC1T45QDCKRQ1	QBS Reference: TPS22919QDCKRQ1
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	-	3/9/0	3/9/0	3/9/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	-	3/9/0	3/9/0	3/9/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	-	3/9/0	3/9/0	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	-	3/231/0	3/135/0	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	-	3/3/0	3/3/0	3/3/0
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	-	3/228/0	3/132/0	3/132/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	-	3/3/0	3/3/0	3/3/0

Test Group C - Package Assembly Integrity Tests

WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0	3/90/0

- QBS: Qual By Similarity
- Qual Device LMV341QDCKRQ1 is qualified at MSL1 260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2305-005

TI Information
Selective Disclosure

**Automotive Qualification Summary
(As per AEC-Q100 Rev. H and JEDEC Guidelines)**

**LMV341QDBVRQ1 [ROLO] AECQ100 Qual
Approve Date 24-April-2025**

Product Attributes

Attributes	Qual Device: LMV341QDBVRQ1	QBS Process Reference: PCM6260QRTVRQ1	QBS Package, Package Reference: PTPS3840PHXXDBVR	QBS Package, Product Reference: OPA310SQDBVRQ1	QBS Product Reference: OPA310SQDCKRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Signal Chain	Signal Chain	Power Management	Signal Chain	Signal Chain
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB	RFAB
Assembly Site	PHI	CDAT	PHI	PHI	HFTFAT
Package Group	SOT	QFN	SOT	SOT	SOT
Package Designator	DBV	RTV	DBV	DBV	DCK
Pin Count	6	32	5	6	6

- QBS: Qual By Similarity, also known as Generic Data
- Qual Device LMV341QDBVRQ1 is qualified at MSL1 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: LMV341QDBVRQ1	QBS Process Reference: PCM6260QRTVRQ1	QBS Package, Package Reference: PTPS3840PHXXDBVR	QBS Package, Product Reference: OPA310SQDBVRQ1	QBS Product Reference: OPA310SQDCKRQ1
Test Group A - Accelerated Environment Stress Tests												

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: LMV341QDBVRQ1	QBS Process Reference: PCM6260QRTVRQ1	QBS Package, Package Reference: PTPS3840PHXXDBVR	QBS Package, Product Reference: OPA310SQDBVRQ1	QBS Product Reference: OPA310SQDCRKQ1
PC	A1	JEDEC J-STD-020 JEDEC22-A113	3	77	Preconditioning	MSL1 260C	-	-	-	3/Pass	1/0/2 ¹	1/Pass
PC	A1	JEDEC J-STD-020 JEDEC22-A113	3	77	Preconditioning	MSL2 260C	-	-	3/Pass	-	-	-
HAST	A2	JEDEC JEDEC22-A110	3	77	Biased HAST	110C/85%RH	264 Hours	-	-	-	-	1/77/0
HAST	A2	JEDEC JEDEC22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	3/231/0	3/231/0	1/77/0	-
AC/UHAST	A3	JEDEC JEDEC22-A102/JEDEC JEDEC22-A118	3	77	Unbiased HAST	110C/85%RH	264 Hours	-	-	-	-	1/77/0
AC/UHAST	A3	JEDEC JEDEC22-A102/JEDEC JEDEC22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	-	3/231/0	3/231/0	1/77/0	-
TC	A4	JEDEC JEDEC22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	3/231/0	3/231/0	1/77/2 ¹	1/77/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	-	-	-	1/5/0	1/5/0
HTSL	A6	JEDEC JEDEC22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	3/135/0	3/135/0	-	-
HTSL	A6	JEDEC JEDEC22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	1/45/0	1/45/0
Test Group B - Accelerated Lifetime Simulation Tests												
HTOL	B1	JEDEC JEDEC22-A108	3	77	Life Test	125C	1000 Hours	-	3/231/0	3/231/0	-	-
HTOL	B1	JEDEC JEDEC22-A108	3	77	Life Test	150C	300 Hours	-	-	-	1/77/2 ¹	1/77/0
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	3/2400/0	-	-	-
Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: LMV341QDBVRQ1	QBS Process Reference: PCM6260QRTVRQ1	QBS Package, Package Reference: PTPS3840PHXXDBVR	QBS Package, Product Reference: OPA310SQDBVRQ1	QBS Product Reference: OPA310SQDCRKQ1
Test Group C - Package Assembly Integrity Tests												
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0	1/30/0	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0	1/30/0	1/30/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	1/15/0	1/15/0	-	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	1/15/0	1/15/0	-	-
PD	C4	JEDEC JEDEC22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	1/10/0	3/30/0	3/30/0	1/10/0	1/10/0
Test Group D - Die Fabrication Reliability Tests												
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests												
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	-	1/3/0	1/3/0	1/3/0	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	-	1/3/0	1/3/0	1/3/0	1/3/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: LMV341QDBVRQ1	QBS Process Reference: PCM6260QRTVRQ1	QBS Package, Package Reference: PTPS3840PHXXDBVR	QBS Package, Product Reference: OPA310SQDBVRQ1	QBS Product Reference: OPA310SQDCKRQ1
LU	E4	AEC Q100-004	1	3	Latch-Up	Per AEC Q100-004	-	-	1/6/0	1/6/0	-	1/3/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	-	3/90/0	3/90/0	3/90/0	3/90/0
Additional Tests												
BLR	T1	-	-	-	Board Level Reliability - Temp Cycle	-40/125C	1000 Cycles	-	1/32/0	-	-	-

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2210-023

[1]-Spike check not performed. Discount.



TI Information
Selective Disclosure

Automotive New Product Qualification Summary (As per AEC-Q100, AECQ006, and JEDEC Guidelines) 0.8 mil Cu wire in TIPI 6-DBV/3-DBZ Package for TIB/LBC9 Al Pad Approved 12-Jun-2019

Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: TLV1805QDBV
Test Group A- Accelerated Environment Stress Tests							
PC	A1	-	3	22	SAM Analysis, Pre Stress	Completed	3/66/0
PC	A1	JEDEC J-STD-020 JESD22-A113	3	276	Preconditioning	Level 1-260C	3/828/0
PC	A1	-	3	22	SAM Analysis, Post Stress	Completed	3/66/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST, 130C/85%RH	96 Hours	3/231/0
HAST	A2	-	3	1	Cross Section, Post bHAST 96 Hours	Completed	3/3/0
HAST	A2	-	3	3	Wire Bond Shear, Post bHast, 96 Hours	Units	3/9/0
HAST	A2	-	3	3	Bond Pull over Stitch, post bHAST, 96 Hours	Units	3/9/0
HAST	A2	-	3	3	Bond Pull over Ball, Post bHAST, 96 Hours	Units	3/9/0
HAST	A2	JEDEC JESD22-A110	3	70	Biased HAST, 130C/85%RH	192 Hours	3/210/0
HAST	A2	-	3	1	Cross Section, Post bHAST 192 Hours	Completed	3/3/0
HAST	A2	-	3	22	SAM Analysis, Post bHAST, 192 Hours	Completed	3/66/0

Type	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: TLV1805QDBV
HAST	A2	-	3	3	Wire Bond Shear, Post bHast, 192 Hours	Units	3/9/0
HAST	A2	-	3	3	Bond Pull over Stitch, post bHAST, 192 Hours	Units	3/9/0
HAST	A2	-	3	3	Bond Pull over Ball, Post bHAST, 192 Hours	Units	3/9/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle, -65/150C	500 Cycles	3/23/0
TC	A4	-	3	1	Cross Section, Post T/C 500 Cycles	Completed	3/3/0
TC	A4	-	3	22	SAM Analysis, Post T/C, 500 Cycles	Completed	3/66/0
TC	A4	-	3	3	Wire Bond Shear, Post T/C 500 Cycles	Units	3/9/0
TC	A4	-	3	3	Bond Pull over Stitch Post T/C 500 Cycles	Units	3/9/0
TC	A4	-	3	3	Bond Pull over Ball Post T/C 500 Cycles	Units	3/9/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle, -65/150C	1000 Cycles	3/21/0
TC	A4	-	3	1	Cross Section, Post T/C 1000 Cycles	Completed	3/3/0
TC	A4	-	3	22	SAM Analysis, Post T/C, 1000 Cycles	Completed	3/66/0
TC	A4	-	3	3	Wire Bond Shear, Post T/C 1000 Cycles	Units	3/9/0
TC	A4	-	3	3	Bond Pull over Stitch, Post T/C, 1000 Cycles	Units	3/9/0
TC	A4	-	3	3	Bond Pull over Ball, Post T/C, 1000 Cycles	Units	3/9/0
PTC	A5	JEDEC JESD22-A105	1	45	Power Temperature Cycle -40/125C	1000 Cycles	-
PTC	A5	JEDEC JESD22-A105	1	45	Power Temperature Cycle -40/125C	2000 Cycles	-
HTSL	A6	JEDEC JESD22-A103	3	45	High Temp Storage Bake 150C	1000 Hours	3/135/0
HTSL	A6	-	3	1	Cross Section, Post HTSL 1000 Hours	Completed	3/3/0
HTSL	A6	JEDEC JESD22-A103	3	44	High Temp Storage Bake 150C	2000 Hours	3/132/0
HTSL	A6	-	3	1	Cross Section, Post HTSL 2000 Hours	Completed	3/3/0
Test Group C – Package Assembly Integrity Tests							
WBS	C1	AEC Q100-001	3	30	Wire Bond Shear, Cpk>1.67	Wires	3/90/0
WBP	C2	MIL-STD883 Method 2011	3	30	Bond Pull over Ball, Cpk >1.67	Wires	3/90/0

A1 (PC): Preconditioning:

Performed for THB, Biased HAST, AC, uHAST & TC samples, as applicable.

Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Green/Pb-free Status:

Qualified Pb-Free(SMT) and Green

TI Qualification ID: 20171014-123670

ZVEI ID's: SEM-DE-01, SEM-DE-02, SEM-DE-03, SEM-PW-02, SEM-PW-09, SEM-PW-13, SEM-PA-08, SEM-PA-11, SEM-PA-07, SEM-PA-13, SEM-QG-01

In performing change qualifications, Texas Instruments follows integrated circuit industry standards in performing defect mechanism analysis and failure mechanism-based accelerated environmental testing to ensure wafer fab process, assembly process and product quality and reliability. As encouraged by these standards, TI uses both product-specific and generic (family) data in qualifying its changes. For devices to be categorized as a 'product qualification family' for generic data purposes, they must share similar product, wafer fab process and assembly process elements. The applicability of generic data (also known at TI as Qualification by Similarity (QBS)) is determined by the Reliability Engineering function following these industry standards. Generic data is shown in the qualification report in columns titled "QBS

Process" (for wafer fab process), "QBS Package" (for assembly process) and "QBS Product" (for product family).

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.