



**12500 TI Boulevard, MS 8640, Dallas, Texas 75243**

**PCN#20250806000.2**

**Qualification of CLARK-PR as additional wafer probe site &  
TI Malaysia as additional Assembly & Test site options for select devices  
Change Notification / Sample Request**

**Date:** August 06, 2025  
**To:** Mouser PCN

Dear Customer:

This is an announcement of a change to a device that is currently offered by Texas Instruments. The details of this change are on the following pages.

Texas Instruments requires acknowledgement of receipt of this notification within 60 days of the date of this notice. Lack of acknowledgement of this notice within 60 days constitutes acceptance and approval of this change. If samples or additional data are required, requests must be received within 60 days of this notification.

The changes discussed within this PCN will not take effect any earlier than the proposed first ship date on Page 3 of this notification, unless customer agreement has been reached on an earlier implementation of the change.

This notice does not change the end-of-life status of any product. Should product affected be on a previously issued product withdrawal/discontinuance notice, this notification does not extend the life of that product or change the life time buy offering/discontinuance plan.

For questions regarding this notice or to provide acknowledgement of this PCN, you may contact your local Field Sales Representative or the change management team.

For sample requests or sample related questions, contact your local Field Sales Representative.

TI values customer engagement and feedback related to TI changes. Customers should contact TI if there are questions or concerns regarding a change notification.

Sincerely,

Change Management Team  
SC Business Services

**20250806000.2**  
**Attachment: 1**

**Products Affected:**

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

<b>DEVICE</b>	<b>CUSTOMER PART NUMBER</b>
TPS40210QDGQRQ1	TPS40210QDGQRQ1

Technical details of this Product Change follow on the next page(s).

<b>PCN Number:</b>	20250806000.2	<b>PCN Date:</b>	August 06, 2025
<b>Title:</b>	Qualification of CLARK-PR as additional wafer probe site & TI Malaysia as additional Assembly & Test site options for select devices		
<b>Customer Contact:</b>	Change Management Team	<b>Dept:</b>	Quality Services
<b>Proposed 1<sup>st</sup> Ship Date:</b>	February 02, 2026	<b>Sample requests accepted until:</b>	October 05, 2025*
*Sample requests received after October 05, 2025 will not be supported.			
<b>Change Type:</b>			
<input checked="" type="checkbox"/> Assembly Site	<input type="checkbox"/> Design	<input type="checkbox"/> Wafer Bump Material	
<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Data Sheet	<input type="checkbox"/> Wafer Bump Process	
<input checked="" type="checkbox"/> Assembly Materials	<input type="checkbox"/> Part number change	<input type="checkbox"/> Wafer Fab Site	
<input type="checkbox"/> Mechanical Specification	<input checked="" type="checkbox"/> Test Site	<input type="checkbox"/> Wafer Fab Material	
<input checked="" type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Process	<input type="checkbox"/> Wafer Fab Process	

### PCN Details

#### Description of Change:

Texas Instruments is pleased to announce the qualification of CLARK-PR as additional wafer probe site & TI Malaysia as additional Assembly & Test site options for the devices listed below.

#### Wafer Probe Test:

	<b>Current</b>	<b>Proposed</b>
Wafer probe test	CD-PR (TI Chengdu)	CLARK-PR (TI Clark)

Construction differences are as follows:

	<b>Current site</b>	<b>Additional site</b>
Assembly & Test site	<b>HFTF</b>	<b>TI Malaysia</b>
Mount compound	SID#A-24	4211470
Mold compound	SID#R-32	4211880
Device marking	TI logo, Pin 1 dimple 4210Q	TI letter, Pin1 dot, Mold cavity ID 3PLS

	<b>Current Site</b>	<b>Additional Site</b>
Assembly & Test site	<b>HFTF</b>	<b>MLA</b>
ePOD drawing		
Exposed Thermal Pad (mm)	2.22 x 1.75 (Max) 1.92 x 1.45 (Min)	1.84 x 1.65 (Max) 1.18 x 0.99 (Min)

Qual details are provided in the Qual Data Section.

Test coverage, insertions, conditions will remain consistent with current testing.

#### Reason for Change:

Supply Continuity

#### Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

## Impact on Environmental Ratings

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

### RoHS

No Change

### REACH

No Change

### Green Status

No Change

### IEC 62474

No Change

## Changes to product identification resulting from this PCN:

### Assembly Site Information:

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
HFTF	HFT	CHN	Hefei
<b>TI Malaysia</b>	<b>MLA</b>	<b>MYS</b>	<b>Kuala Lumpur</b>

Sample product shipping label (not actual product label):



### Product Affected:

TPS40210QDGQRCT TPS40210QDGQRQ1

## Qualification Report Automotive Qualification Summary (As per AEC-Q100 Rev. J and JEDEC Guidelines)

Approve Date 03-April-2025

### Product Attributes

Attributes	Qual Device: TPS40210QDGQRQ1	QBS Package, Process Reference: TPS92622QDGQRQ1	QBS Package, Product Reference: TPS92515QDGQRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125
Product Function	Power Management	Power Management	Power Management
Wafer Fab Supplier	RFAB	RFAB	RFAB
Assembly Site	MLA	MLA	MLA
Package Group	VSSOP	VSSOP	VSSOP
Package Designator	DGQ	DGN	DGQ
Pin Count	10	12	10

QBS: Qual By Similarity, also known as Generic Data

Qual Device TPS40210QDGQRQ1 is qualified at MSL1 260C

**Qualification Results**  
 Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <a href="#">TPS40210QDGQRQ1</a>	QBS Package, Process Reference: <a href="#">TPS92622QDGQRQ1</a>	QBS Package, Product Reference: <a href="#">TPS92515QDGQRQ1</a>
<b>Test Group A - Accelerated Environment Stress Tests</b>										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	-	3/0/0	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	-	3/0/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	1/77/0	3/231/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	-	3/231/0	3/231/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	3/231/0	3/231/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	-	-	1/5/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	1/45/0	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	2/90/0	3/135/0
<b>Test Group B - Accelerated Lifetime Simulation Tests</b>										
<b>Test Group C - Package Assembly Integrity Tests</b>										
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	1/15/0	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	1/15/0	-
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	1/10/0	3/30/0	3/30/0
<b>Test Group D - Die Fabrication Reliability Tests</b>										
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
<b>Test Group E - Electrical Verification Tests</b>										

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <a href="#">TPS40210QDGQRQ1</a>	QBS Package, Process Reference: <a href="#">TPS92622QDGQRQ1</a>	QBS Package, Product Reference: <a href="#">TPS92515QDGQRQ1</a>
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	-	1/3/0	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	-	1/3/0	-
LU	E4	AEC Q100-004	1	3	Latch-Up	Per AEC Q100-004	-	-	1/6/0	-
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	-	1/30/0	1/30/0

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

#### Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

#### E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2411-007

## Qualification Report Automotive Qualification Summary (As per AEC and JEDEC Guidelines)

### Q006 VSSOP at MLA

Approve Date 21-June-2024

#### Product Attributes

Attributes	Qual Device: <a href="#">M0G3507QDGS28RQ1</a>	Qual Device: <a href="#">M0L1306QDGS28RQ1</a>
<b>Die Attributes</b>		
Wafer Fab Supplier	UMCI	UMCI
Wafer Process	12F65M	12F65M
Die Size (L,W) (um)	1900 x 2300	1191 x 2115
<b>Package Attributes</b>		
Assembly Site	MLA	MLA
Package Group	VSSOP	VSSOP
Package Designator	DGS	DGS
Package Size (mm)	7.1 x 3	7.1 x 3
Body Thickness (mm)	0.95	0.95
Pin Count	28	28
Lead Finish	NIPDAU	NIPDAU

QBS: Qual By Similarity, based on generic data for the technology, product, and/or package. Additional supporting data are provided for as general reference data.

VSSOP packages referenced here are qualified at MSL2 260C

#### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: M0G3507QDGS28RQ1	Qual Device: M0L1306QDGS28RQ1
<b>Test Group A - Accelerated Environment Stress Tests</b>									
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	3/510/0	3/0/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	3/66/0	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	3/66	3/66/0
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231/0	3/231/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	3/3/0	3/3/0
HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	-	3/9/0	3/9/0
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	-	3/9/0	3/9/0
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	-	3/9/0	3/9/0
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C/85%RH	192 Hours	3/231/0	3/231/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	3/66/0	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	3/3/0	3/3/0
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	3/9/0	3/9/0
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	3/9/0	3/9/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	3/9/0	3/9/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	3/66/0	3/66/0
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	3/3/0	3/3/0
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	3/9/0	3/9/0
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	3/9/0	3/9/0
TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	3/9/0	3/9/0
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	3/231/0	3/231/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66/0	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3/0	3/3/0
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	3/9/0	3/9/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	3/9/0	3/9/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	3/9/0	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	1/45/0	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	1/1/0	3/3/0
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	1000 Hours	1/1/0	3/3/0
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	1/45/0	3/135/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	1/1/0	3/3/0
<b>Test Group C - Package Assembly Integrity Tests</b>									
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-NPD-2306-084

In performing change qualifications, Texas Instruments follows integrated circuit industry standards in performing defect mechanism analysis and failure mechanism-based accelerated environmental testing to ensure wafer fab process, assembly process and product quality and reliability. As encouraged by these standards, TI uses both product-specific and generic (family) data in qualifying its changes. For devices to be categorized as a 'product qualification family' for generic data purposes, they must share similar product, wafer fab process and assembly process elements. The applicability of generic data (also known at TI as Qualification by Similarity (QBS)) is determined by the Reliability Engineering function following these industry standards. Generic data is shown in the qualification report in columns titled "QBS Process" (for wafer fab process), "QBS Package" (for assembly process) and "QBS Product" (for product family).

ZVEI ID: SEM-PA-01, SEM-PA-18 SEM-PA-11 SEM-PA-07 SEM-PA-13 SEM-TF-01

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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