



**12500 TI Boulevard, MS 8640, Dallas, Texas 75243**

**PCN#20250710001.2**  
**Qualification of RFAB as an additional Fab site option,  
Die Revision, Datasheet & BOM options for select devices**  
**Change Notification / Sample Request**

**Date:** July 11, 2025  
**To:** Mouser PCN

Dear Customer:

This is an announcement of a change to a device that is currently offered by Texas Instruments. The details of this change are on the following pages.

Texas Instruments requires acknowledgement of receipt of this notification within 60 days of the date of this notice. Lack of acknowledgement of this notice within 60 days constitutes acceptance and approval of this change. If samples or additional data are required, requests must be received within 60 days of this notification.

The changes discussed within this PCN will not take effect any earlier than the proposed first ship date on Page 3 of this notification, unless customer agreement has been reached on an earlier implementation of the change.

This notice does not change the end-of-life status of any product. Should product affected be on a previously issued product withdrawal/discontinuance notice, this notification does not extend the life of that product or change the life time buy offering/discontinuance plan.

For questions regarding this notice or to provide acknowledgement of this PCN, you may contact your local Field Sales Representative or the change management team.

For sample requests or sample related questions, contact your local Field Sales Representative.

TI values customer engagement and feedback related to TI changes. Customers should contact TI if there are questions or concerns regarding a change notification.

Sincerely,

Change Management Team  
SC Business Services

**20250710001.2**  
**Attachment: 1**

**Products Affected:**

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

<b>DEVICE</b>	<b>CUSTOMER PART NUMBER</b>
CLVC125AQPWRG4Q1	CLVC125AQPWRG4Q1
SN74LVC32AQPWRG4Q1	NULL

Technical details of this Product Change follow on the next page(s).

<b>PCN Number:</b>	20250710001.2	<b>PCN Date:</b>	July 11, 2025
<b>Title:</b>	Qualification of RFAB as an additional Fab site option, Die Revision, Datasheet & BOM options for select devices		
<b>Customer Contact:</b>	Change Management Team	<b>Dept:</b>	Quality Services
<b>Proposed 1<sup>st</sup> Ship Date:</b>	January 07, 2026	<b>Sample requests accepted until:</b>	September 09, 2025*

\*Sample requests received after September 09, 2025 will not be supported.

#### Change Type:

<input checked="" type="checkbox"/>	Assembly Site	<input checked="" type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material
<input checked="" type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input checked="" type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site	<input checked="" type="checkbox"/>	Wafer Fab Material
<input checked="" type="checkbox"/>	Packing/Shipping/Labeling	<input checked="" type="checkbox"/>	Test Process	<input checked="" type="checkbox"/>	Wafer Fab Process

#### PCN Details

#### Description of Change:

Texas Instruments is pleased to announce the qualification of RFAB as an additional Fab site option & BOM options for the devices listed below.

Current Fab Site			Additional Fab site		
Current Fab Site	Process	Wafer Diameter	Additional Fab site	Process	Wafer Diameter
FFAB	ASLNONC 10	200mm	RFAB	LBC7	300mm

The die was also changed as a result of the process change.

#### Wafer Probe Test:

	Current	Proposed
Wafer probe test	With Probe	No Probe

Construction differences are as follows:

#### Group 1 device

	Current BOM	Additional BOM
Wire bond diam/type	0.96mil Au	0.8mil Cu

#### Group 2 device

	Current BOM	Additional BOM
Wire bond diam/type	0.96mil Au, 1.0mil Cu	0.8mil Cu
Mount compound	4147858, 4042500	4147858
Mold compound	4211471, 4206193	4211471

The datasheets will be changing as a result of the above mentioned changes. The datasheet change details can be reviewed in the datasheet revision history. The links to the revised datasheets are available in the table below.



SN74LVC02A-Q1

SCES465F – JULY 2003 – REVISED DECEMBER 2024

#### Changes from Revision E (May 2024) to Revision F (December 2024)

Page

- Updated R<sub>θJA</sub> values: PW = 86 to 127.8, all values in °C/W ..... 4



SN74LVC08A-Q1

SCES480E – AUGUST 2003 – REVISED MAY 2024

<b>Changes from Revision D (March 2024) to Revision E (May 2024)</b>		<b>Page</b>	
• Updated R <sub>0JA</sub> values: PW = 113 to 150.8, all values in °C/W .....		4	
 <b>TEXAS INSTRUMENTS</b>		<b>SN74LVC00A-Q1</b> SCAS703D – SEPTEMBER 2003 – REVISED MAY 2024	
<b>Changes from Revision C (March 2024) to Revision D (May 2024)</b>		<b>Page</b>	
• Updated R <sub>0JA</sub> values: PW = 113 to 150.8, all values in °C/W .....		4	
 <b>TEXAS INSTRUMENTS</b>		<b>SN74LVC32A-Q1</b> SCAS706E – SEPTEMBER 2003 – REVISED DECEMBER 2024	
<b>Changes from Revision D (May 2024) to Revision E (December 2024)</b>		<b>Page</b>	
• Updated R <sub>0JA</sub> value: D = 86 to 127.8, all values in °C/W .....		4	
 <b>TEXAS INSTRUMENTS</b>		<b>SN74LVC74A-Q1</b> SCES481E – AUGUST 2003 – REVISED AUGUST 2024	
<b>Changes from Revision D (May 2024) to Revision E (August 2024)</b>		<b>Page</b>	
• Updated thermal values for PW package from R <sub>0JA</sub> = 113 to 150.8, R <sub>0JC</sub> (top) = 50.3 to 78.3, R <sub>0JB</sub> = 63.4 to 93.8, Ψ <sub>JT</sub> = 6.2 to 24.7, Ψ <sub>JB</sub> = 62.8 to 93.2, all values in °C/W .....		4	
 <b>TEXAS INSTRUMENTS</b>		<b>SN74LVC86A-Q1</b> SCAS707E – SEPTEMBER 2003 – REVISED DECEMBER 2024	
<b>Changes from Revision D (August 2024) to Revision E (December 2024)</b>		<b>Page</b>	
• Updated R <sub>0JA</sub> values: D = 86 to 127.8, all values in °C/W .....		4	
 <b>TEXAS INSTRUMENTS</b>		<b>SN74LVC125A-Q1</b> SCAS762E – FEBRUARY 2004 – REVISED DECEMBER 2024	
<b>Changes from Revision D (May 2024) to Revision E (December 2024)</b>		<b>Page</b>	
• Updated R <sub>0JA</sub> values: D = 86 to 127.8, all values in °C/W .....		5	
 <b>TEXAS INSTRUMENTS</b>		<b>SN74LVC126A-Q1</b> SCAS763D – FEBRUARY 2004 – REVISED OCTOBER 2024	
<b>Changes from Revision C (May 2024) to Revision D (October 2024)</b>		<b>Page</b>	
• Updated R <sub>0JA</sub> values: D = 86 to 127.8, all values in °C/W .....		5	
<b>Product Folder</b>	<b>Current Datasheet Number</b>	<b>New Datasheet Number</b>	<b>Link to full datasheet</b>
SN74LVC02A-Q1	SCES465E	<b>SCES465F</b>	<a href="http://www.ti.com/product/SN74LVC02A-Q1">http://www.ti.com/product/SN74LVC02A-Q1</a>
SN74LVC08A-Q1	SCES480D	<b>SCAS480E</b>	<a href="http://www.ti.com/product/SN74LVC08A-Q1">http://www.ti.com/product/SN74LVC08A-Q1</a>
SN74LVC00A-Q1	SCAS703C	<b>SCAS703D</b>	<a href="http://www.ti.com/product/SN74LVC00A-Q1">http://www.ti.com/product/SN74LVC00A-Q1</a>
SN74LVC32A-Q1	SCAS706E	<b>SCAS706D</b>	<a href="http://www.ti.com/product/SN74LVC32A-Q1">http://www.ti.com/product/SN74LVC32A-Q1</a>
SN74LVC74A-Q1	SCES481D	<b>SCES481E</b>	<a href="http://www.ti.com/product/SN74LVC74A-Q1">http://www.ti.com/product/SN74LVC74A-Q1</a>
SN74LVC86A-Q1	SCAS707D	<b>SCAS707E</b>	<a href="http://www.ti.com/product/SN74LVC86A-Q1">http://www.ti.com/product/SN74LVC86A-Q1</a>
SN74LVC125A-Q1	SCAS762D	<b>SCAS762E</b>	<a href="http://www.ti.com/product/SN74LVC125A-Q1">http://www.ti.com/product/SN74LVC125A-Q1</a>
SN74LVC126A-Q1	SCAS763C	<b>SCAS763D</b>	<a href="http://www.ti.com/product/SN74LVC126A-Q1">http://www.ti.com/product/SN74LVC126A-Q1</a>

  

Qual details are provided in the Qual Data Section. Test coverage, insertions, conditions will remain consistent with current testing.	
<b>Reason for Change:</b>	
Supply Continuity	
<b>Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):</b>	
None	
<b>Impact on Environmental Ratings</b>	
Checked boxes indicate the status of environmental ratings following implementation of this	

change. If below boxes are checked, there are no changes to the associated environmental ratings.

**RoHS**

**REACH**

**Green Status**

**IEC 62474**

<input type="checkbox"/> No Change			
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**Changes to product identification resulting from this PCN:**

**Fab Site Information:**

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
FFAB	TID	DEU	Freising
<b>RFAB</b>	<b>RFB</b>	<b>USA</b>	<b>Richardson</b>

**Die Rev:**

**Current** New

Die Rev [2P]	<b>Die Rev [2P]</b>
B, K	<b>A</b>

Sample product shipping label (not actual product label):



**Group 1 Product Affected: (Fab, Die rev, BOM)**

SN74LVC86AQDRG4Q1.A	SN74LVC125AQDRQ1.B	SN74LVC32AQDRQ1.B
SN74LVC86AQDRG4Q1.B	SN74LVC126AQDRG4Q1	SN74LVC74AQDRG4Q1
SN74LVC02AQDRG4Q1.B	SN74LVC126AQDRG4Q1.B	SN74LVC74AQDRG4Q1.A
SN74LVC02AQDRQ1	SN74LVC126AQDRQ1	SN74LVC74AQDRG4Q1.B
SN74LVC02AQDRQ1.A	SN74LVC126AQDRQ1.A	SN74LVC86AQDRG4Q1
SN74LVC02AQDRQ1.B	SN74LVC126AQDRQ1.B	SN74LVC02AQDRG4Q1
SN74LVC125AQDRQ1	SN74LVC32AQDRQ1	
SN74LVC125AQDRQ1.A	SN74LVC32AQDRQ1.A	

**Group 2 Product Affected: (Fab, Die rev, BOM)**

SN74LVC86AQPWRG4Q1	SN74LVC32AQPWRG4Q1.B	SN74LVC00AQPWRG4Q1
CLVC125AQPWRG4Q1.B	SN74LVC74AQPWRG4Q1	SN74LVC00AQPWRG4Q1.B
SN74LVC02AQPWRG4Q1	SN74LVC74AQPWRG4Q1.B	SN74LVC08AQPWRG4Q1.B
SN74LVC02AQPWRG4Q1.B	SN74LVC86AQPWRG4Q1.B	SN74LVC32AQPWRG4Q1
SN74LVC08AQPWRG4Q1	CLVC125AQPWRG4Q1	

**Group 1**  
**Qualification Report**  
**Automotive Qualification Summary**  
**(As per AEC-Q100 Rev. J and JEDEC Guidelines)**

Approve Date 20-DECEMBER -2024

**Product Attributes**

Attributes		Qual Device: SN74LVC74AQDRG4Q1	Qual Device: SN74LVC125AQDRQ1	Qual Device: SN74LVC02AQDRQ1	QBS Process Reference: SN3257QDYYRQ1	QBS Package, Product Reference: SN74LVC11ADRQ1	QBS Package, Product Reference: SN74LVC132ADRQ1	QBS Package, Product Reference: SN74LVC74ADRQ1
Automotive Grade Level		Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)		-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function		Logic	Logic	Logic	Logic	Logic	Logic	Logic
Wafer Fab Supplier		RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB
Assembly Site		FMX	FMX	FMX	PHI	FMX	FMX	FMX
Package Group		SOIC	SOIC	SOIC	SOT	SOIC	SOIC	SOIC
Package Designator		D	D	D	DYY	D	D	D
Pin Count		14	14	14	16	14	14	14

QBS: Qual By Similarity, also known as Generic Data  
 Qual Device SN74LVC74AQDRG4Q1 is qualified at MSL1 260C  
 Qual Device SN74LVC125AQDRQ1 is qualified at MSL1 260C  
 Qual Device SN74LVC02AQDRQ1 is qualified at MSL1 260C

**Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74LVC74AQDRG4Q1	Qual Device: SN74LVC125AQDRQ1	Qual Device: SN74LVC02AQDRQ1	QBS Process Reference: SN3257QDYYRQ1	QBS Package, Product Reference: SN74LVC11ADRQ1	QBS Package, Product Reference: SN74LVC132ADRQ1	QBS Package, Product Reference: SN74LVC74ADRQ1
<b>Test Group A - Accelerated Environment Stress Tests</b>														
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	-	-	-	3/0/0	1/0/0	1/0/0	1/0/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	-	3/231/0	1/77/0	1/77/0	1/77/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	-	3/231/0	1/77/0	1/77/0	1/77/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	-	3/231/0	1/77/0	1/77/0	1/77/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	-	-	3/135/0	-	-	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	1/45/0	1/45/0	1/45/0	1/45/0
<b>Test Group B - Accelerated Lifetime Simulation Tests</b>														
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	150C	300 Hours	-	-	-	3/231/0	1/77/0	-	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	150C	24 Hours	-	-	-	3/240/0	-	-	-
<b>Test Group C - Package Assembly Integrity Tests</b>														
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	-	-	3/90/0	1/30/0	1/30/0	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	-	-	3/90/0	1/30/0	1/30/0	1/30/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	-	1/15/0	-	-	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	-	1/15/0	1/15/0	-	-
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	-	-	-	3/30/0	1/10/0	1/10/0	1/10/0
<b>Test Group D - Die Fabrication Reliability Tests</b>														
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
TDD	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements				

BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements					
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements					
<b>Test Group E - Electrical Verification Tests</b>													
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	-	-	-	1/3/0	-	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	1/3/0	1/3/0	1/3/0	-	1/3/0	1/3/0
LU	E4	AEC Q100-004	1	3	Latch-Up	Per AEC Q100-004	-	1/3/0	1/3/0	1/3/0	1/6/0	1/3/0	-
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpl>1.67 Room, hot, and cold	-	1/30/0	1/30/0	1/30/0	3/90/0	2/60/0	1/30/0
Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable													

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

#### **Ambient Operating Temperature by Automotive Grade Level:**

Grade 0 (or E) : -40C to +150C

Grade 1 (or Q) : -40C to +125C

Grade 2 (or T) : -40C to +105C

Grade 3 (or I) : -40C to +85C

#### **E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):**

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2408-074

**Group 2**  
**Qualification Report**  
**Automotive Qualification Summary**  
**(As per AEC-Q100 Rev. J and JEDEC Guidelines)**  
Approve Date 07-MAY -2024

**Product Attributes**

Attributes		Qual Device: SN74LVC125AOPWRO1	Qual Device: SN74LVC02AOPWRO1	QBS Process Reference: SN3257QDYYR01	QBS Package Reference: SN74HCS74QOPWRO1	QBS Product Reference: SN74LVC11AWBQAR01	QBS Product Reference: SN74LVC115AWBQAR01	QBS Product Reference: SN74LVC11APWRO1	QBS Product Reference: SN74LVC132APWRO1	QBS Product Reference: SN74LVC02AWBQAR01
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Logic	Logic	Logic	Logic	Logic	Logic	Logic	Logic	Logic	Logic
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB
Assembly Site	MLA	MLA	PHI	MLA	CDAT	CDAT	MLA	MLA	CDAT	CDAT
Package Group	TSSOP	TSSOP	SOT	TSSOP	QFN	QFN	TSSOP	TSSOP	QFN	QFN
Package Designator	PW	PW	DY	PW	BQA	BQA	PW	PW	BQA	BQA
Pin Count	14	14	16	14	14	14	14	14	14	14

QBS: Qual By Similarity

Qual Device SN74LVC125AOPWRO1 is qualified at MSL1 260C

Qual Device SN74LVC02AOPWRO1 is qualified at MSL1 260C

**Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74LVC125AOPWRO1	Qual Device: SN74LVC02AOPWRO1	QBS Process Reference: SN3257QDYYR01	QBS Package Reference: SN74HCS74QOPWRO1	QBS Product Reference: SN74LVC11AWBQAR01	QBS Product Reference: SN74LVC115AWBQAR01	QBS Product Reference: SN74LVC11APWRO1	QBS Product Reference: SN74LVC132APWRO1	QBS Product Reference: SN74LVC02AWBQAR01	
<b>Test Group A - Accelerated Environment Stress Tests</b>																	
PC	A1	JEDEC J-STD-022-A113	3	77	Preconditioning	MSL1 260C	-	-	-	3/0/0	3/0/0	1/0/0	-	1/0/0	-	-	
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	3/231/0	3/231/0	1/77/0	-	1/77/0	-	-	
AC/UAHAST	A3	JEDEC JESD22-A102/UEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	3/231/0	3/231/0	-	-	-	-	-	
AC/UAHAST	A3	JEDEC JESD22-A102/UEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	-	-	-	-	1/77/0	-	1/77/0	-	-	
TC	A4	JEDEC JESD22-A102 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	3/231/0	3/231/0	1/77/0	-	1/77/0	-	-	
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	-	-	-	-	1/5/0	-	1/5/0	-	-	
TC-SAM	A4	-	3	3	Post TC SAM	<50% delamination	-	-	-	-	1/12/0	-	-	-	-	-	
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	-	3/135/0	3/135/0	1/45/0	-	1/45/0	-	-	
<b>Test Group B - Accelerated Lifetime Simulation Tests</b>																	
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	-	-	-	3/231/0	1/77/0	-	-	-	-	-
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	150C	300 Hours	-	-	3/231/0	-	-	-	-	-	-	
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	-	-	3/2400/0	-	-	-	-	-	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	150C	24 Hours	-	-	3/2400/0	-	-	-	-	-	-	
<b>Test Group C - Package Assembly Integrity Tests</b>																	
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	-	3/60/0	3/60/0	1/60/0	-	1/60/0	-	-	
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	-	3/60/0	3/60/0	1/60/0	-	1/60/0	-	-	
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0	1/15/0	-	-	-	-	-	
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0	1/15/0	1/15/0	-	-	-	-	
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	-	-	3/60/0	3/60/0	1/60/0	-	1/60/0	-	-	
<b>Test Group D - Die Fabrication Reliability Tests</b>																	
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements									
TDD8	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements									
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements									
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements									
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements									
<b>Test Group E - Electrical Verification Tests</b>																	
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	-	1/3/0	
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	-	-	1/3/0	-	-	-	-	-	-	
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	1/3/0	1/3/0	-	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	-	1/6/0	
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Production and cold	-	1/30/0	1/30/0	3/90/0	3/90/0	1/30/0	1/30/0	1/30/0	1/30/0	1/30/0	

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

**Ambient Operating Temperature by Automotive Grade Level:**

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

**E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):**

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

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TI Qualification ID: R-CHG-2403-036

**Qualification Report**  
**Automotive Qualification Summary**  
**(As per AEC Q006 and JEDEC Guidelines)**

Approve Date 20-DECEMBER -2024

**Product Attributes**

Attributes		Qual Device:	Qual Device:	Qual Device:	QBS Package, Product Reference:	QBS Package, Product Reference:	QBS Package, Product Reference:
		SN74LVC74AQDRG4Q1	SN74LVC125AQDRQ1	SN74LVC02AQDRQ1	SN74LVC11ADRQ1	SN74LVC132ADRQ1	SN74LVC74ADRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Logic	Logic	Logic	Logic	Logic	Logic	Logic
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB
Assembly Site	FMX	FMX	FMX	FMX	FMX	FMX	FMX
Package Group	SOIC	SOIC	SOIC	SOIC	SOIC	SOIC	SOIC
Package Designator	D	D	D	D	D	D	D
Pin Count	14	14	14	14	14	14	14

QBS: Qual By Similarity, also known as Generic Data

Qual Device SN74LVC74AQDRG4Q1 is qualified at MSL1 260C

Qual Device SN74LVC125AQDRQ1 is qualified at MSL1 260C

Qual Device SN74LVC02AQDRQ1 is qualified at MSL1 260C

**Qualification Results**

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74LVC74AQDRG4Q1	Qual Device: SN74LVC125AQDRQ1	Qual Device: SN74LVC02AQDRQ1	QBS Reference: SN74LVC11ADRQ1	QBS Reference: SN74LVC132ADRQ1	QBS Reference: SN74LVC74ADRQ1
<b>Test Group A - Accelerated Environment Stress Tests</b>													
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	-	-	-	1/0/0	1/0/0	1/0/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	-	-	-	1/22/0	1/22/0	1/22/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	-	-	-	1/22/0	1/22/0	1/22/0

HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	-	1/77/0	1/77/0	1/77/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	-	-	-	1/1/0	1/1/0	1/1/0
HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C/85%RH	192 Hours	-	-	-	1/77/0	1/77/0	1/77/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST, 2X	Review for delamination	Completed	-	-	-	1/22/0	1/22/0	1/22/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	-	-	-	1/1/0	1/1/0	1/1/0
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	-	1/77/0	1/77/0	1/77/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC, 1X	Review for delamination	Completed	-	-	-	1/22/0	1/22/0	1/22/0
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	-	-	-	1/1/0	1/1/0	1/1/0
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	-	-	-	1/77/0	1/77/0	1/77/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	-	-	-	1/22/0	1/22/0	1/22/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	-	-	-	1/1/0	1/1/0	1/1/0
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	-	-	-	1/3/0	1/3/0	1/3/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	-	-	-	-	-	-
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	175C	500 Hours	-	-	-	1/45/0	1/45/0	1/45/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	-	-	-	1/1/0	1/1/0	1/1/0
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	-	-	-	-	-	-
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	175C	1000 Hours	-	-	-	1/45/0	1/45/0	1/45/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	-	-	-	1/1/0	1/1/0	1/1/0
<b>Test Group C - Package Assembly Integrity Tests</b>													
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	-	-	1/30/0	1/30/0	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	-	-	1/30/0	1/30/0	1/30/0

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TI Qualification ID: R-CHG-2408-074

In performing change qualifications, Texas Instruments follows integrated circuit industry standards in performing defect mechanism analysis and failure mechanism-based accelerated environmental testing to ensure wafer fab process, assembly process and product quality and reliability. As encouraged by these standards, TI uses both product-specific and generic (family) data in qualifying its changes. For devices to be categorized as a 'product qualification family' for generic data purposes, they must share similar product, wafer fab process and assembly process elements. The applicability of generic data (also known at TI as Qualification by Similarity (QBS)) is determined by the Reliability Engineering function following these industry standards. Generic data is shown in the qualification report in columns titled "QBS Process" (for wafer fab process), "QBS Package" (for assembly process) and "QBS Product" (for product family).

ZVEI ID: SEM-DS-02, SEM-DE-03, SEM-PW-02, SEM-PW-09, SEM-PW-13, SEM-PA-07, SEM-PA-08, SEM-PA-11, SEM-QG-01

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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