



**12500 TI Boulevard, MS 8640, Dallas, Texas 75243**

**PCN# 20250417003.2**  
**Qualify additional BOM material for select package devices**  
**Change Notification / Sample Request**

**Date:** April 28, 2025  
**To:** MOUSER PCN

Dear Customer:

This is an announcement of a change to a device that is currently offered by Texas Instruments. The details of this change are on the following pages.

Texas Instruments requires acknowledgement of receipt of this notification within 60 days of the date of this notice. Lack of acknowledgement of this notice within 60 days constitutes acceptance and approval of this change. If samples or additional data are required, requests must be received within 60 days of this notification.

The changes discussed within this PCN will not take effect any earlier than the proposed first ship date on Page 3 of this notification, unless customer agreement has been reached on an earlier implementation of the change.

This notice does not change the end-of-life status of any product. Should product affected be on a previously issued product withdrawal/discontinuance notice, this notification does not extend the life of that product or change the life time buy offering/discontinuance plan.

For questions regarding this notice or to provide acknowledgement of this PCN, you may contact your local Field Sales Representative or the change management team.

For sample requests or sample related questions, contact your local Field Sales Representative.

TI values customer engagement and feedback related to TI changes. Customers should contact TI if there are questions or concerns regarding a change notification.

Sincerely,

Change Management Team  
SC Business Services

**20250417003.2**  
**Change Notification / Sample Request**  
**Attachments**

**Products Affected:**

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
ISO6740FQDWRQ1	NULL
ISO6741FQDWRQ1	NULL
ISO6763FQDWRQ1	NULL
ISO7730QDWRQ1	595-ISO7730QDWRQ1
ISO7741FQDWRQ1	ISO7741FQDWRQ1
ISO6762FQDWRQ1	NULL
ISO7740QDWRQ1	NULL
ISO6742QDWRQ1	NULL
ISO6762QDWRQ1	NULL
ISO6731FQDWRQ1	NULL
ISO6742FQDWRQ1	NULL
ISO7760FQDWRQ1	NULL
ISO7761FQDWRQ1	NULL
ISO6741QDWRQ1	NULL
ISO7730FQDWRQ1	NULL
ISO7762FQDWRQ1	NULL
ISO7720QDWRQ1	NULL
ISO7721FQDWRQ1	NULL
ISO7740FQDWRQ1	NULL
ISO6731QDWRQ1	NULL
ISO7721FQDWRQ1	NULL
ISO7763FQDWRQ1	NULL
ISO6761QDWRQ1	NULL
ISO7720FQDWRQ1	NULL
ISO7731QDWRQ1	ISO7731QDWRQ1
ISO7710FQDWRQ1	NULL
ISO7742FQDWRQ1	NULL
ISO7742QDWRQ1	NULL
ISO6720QDWRQ1	NULL
ISO6740QDWRQ1	NULL
ISO6721QDWRQ1	NULL
ISO7721QDWRQ1	NULL
ISO7731FQDWRQ1	ISO7731FQDWRQ1
UCC21550BQDWRQ1	NULL
ISO6720FQDWRQ1	NULL
ISO6721FQDWRQ1	NULL
ISO7720QDWRQ1	ISO7720QDWRQ1
ISO7721QDWRQ1	ISO7721QDWRQ1
UCC21550AQDWRQ1	NULL
ISO7741QDWRQ1	ISO7741QDWRQ1

Technical details of this Product Change follow on the next page(s).

<b>PCN Number:</b>	20250417003.2			<b>PCN Date:</b>	April 28, 2025
<b>Title:</b>	Qualify additional BOM material for select package devices				
<b>Customer Contact:</b>	Change Management team		<b>Dept:</b>	Quality Services	
<b>Proposed 1<sup>st</sup> Ship Date:</b>	October 25, 2025		<b>Estimated Sample Availability:</b>	June 27, 2025	
<b>*Sample requests received after June 27, 2025 will not be supported.</b>					
<b>Change Type:</b>					
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Material
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Process
<b>PCN Details</b>					
<b>Description of Change:</b>					
Texas Instruments is pleased to announce the qualification of additional BOM material for select devices in the SOIC package. Device affected will remain on current Assembly & Test sites. Material differences as follows.					
		<b>Current</b>	<b>Additional</b>		
Mold compound		4221499-0008	4221499-1000		
<b>Reason for Change:</b>					
Continuity of supply.					
<b>Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):</b>					
None					
<b>Impact on Environmental Ratings:</b>					
Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.					
<b>RoHS</b>	<b>REACH</b>	<b>Green Status</b>	<b>IEC 62474</b>		
<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change		
<b>Changes to product identification resulting from this PCN:</b>					
None					
<b>Product Affected:</b>					
ISO6720FQDWVRQ1	ISO6762QDWRQ1	ISO7740FQDWVRQ1			
ISO6720QDWVRQ1	ISO6763FQDWVRQ1	ISO7740QDWRQ1			
ISO6721FQDWVRQ1	ISO6763QDWRQ1	ISO7741FQDWVRQ1			
ISO6721QDWVRQ1	ISO7710FQDWVRQ1	ISO7741QDWRQ1			
ISO6731FQDWVRQ1	ISO7710QDWRQ1	ISO7742FQDWVRQ1			
ISO6731QDWRQ1	ISO7720FQDWVRQ1	ISO7742QDWRQ1			
ISO6740FQDWVRQ1	ISO7720FQDWVRQ1	ISO7760FQDWVRQ1			
ISO6740QDWRQ1	ISO7720QDWRQ1	ISO7760QDWRQ1			
ISO6741FQDWVRQ1	ISO7720QDWVRQ1	ISO7761FQDWVRQ1			
ISO6741QDWRQ1	ISO7721FQDWVRQ1	ISO7761QDWRQ1			
ISO6742FQDWVRQ1	ISO7721FQDWVRQ1	ISO7762FQDWVRQ1			
ISO6742QDWRQ1	ISO7721QDWRQ1	ISO7762QDWRQ1			

ISO6760FQDWRQ1	ISO7721QDWVRQ1	ISO7763FQDWRQ1
ISO6760QDWRQ1	ISO7730FQDWRQ1	ISO7763QDWRQ1
ISO6761FQDWRQ1	ISO7730QDWRQ1	UCC21550AQDWRQ1
ISO6761QDWRQ1	ISO7731FQDWRQ1	UCC21550BQDWRQ1
ISO6762FQDWRQ1	ISO7731QDWRQ1	

## Qualification Data

Automotive Qualification Summary  
(As per AEC-Q100 Rev. J and JEDEC Guidelines)  
Approve Date 25-March-2025

### Product Attributes

Attributes	Qual Device: <u>ISO7763FQDWRQ1</u>	QBS Process Reference: <u>UCC23513QDWYQ1</u>	QBS Product Reference: <u>ISO7763QDWRQ1</u>	QBS Package Reference: <u>ISO7241CQDWRQ1</u>	QBS Package Reference: <u>ISO7241CQDWRQ1</u>
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Power Management	Power Management	Interface	Interface	Interface
Wafer Fab Supplier	RFAB, RFAB	RFAB, RFAB	RFAB, RFAB	RFAB, MH8, RFAB	RFAB, MH8, RFAB
Assembly Site	MLA	TAI	MLA	MLA	MLA
Package Group	SOIC	SOIC	SOIC	SOIC	SOIC
Package Designator	DW	DWY	DW	DW	DW
Pin Count	16	6	16	16	16

QBS: Qual By Similarity, also known as Generic Data  
Qual Device ISO7763FQDWRQ1 is qualified at MSL2 260C

### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>ISO7763FQDWRQ1</u>	QBS Process Reference: <u>UCC23513QDWYQ1</u>	QBS Product Reference: <u>ISO7763QDWRQ1</u>	QBS Package Reference: <u>ISO7241CQDWRQ1</u>	QBS Package Reference: <u>ISO7241CQDWRQ1</u>
Test Group A - Accelerated Environment Stress Tests												
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	-	-	No Fails	No Fails
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	-	1/77/0	3/231/0
AC/HAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	-	1/77/0	-
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	-	1/77/0	3/231/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	-	-	-	1/5/0	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	-	-	1/45/0	3/135/0
Test Group B - Accelerated Lifetime Simulation Tests												
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	-	3/231/0	-	-	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	3/2400/0	-	-	-

Test Group C - Package Assembly Integrity Tests												
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	-	1/30/0	3/90/0	-
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	-	1/30/0	3/90/0	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	-	1/15/0	-
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	3/30/0	-	1/10/0	3/30/0	-
Test Group D - Die Fabrication Reliability Tests												
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDD	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests												
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	Device specific data [1]	1/3/0	1/3/0	1/3/0	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	Device specific data [1]	1/3/0	1/3/0	1/3/0	-
LU	E4	AEC Q100-004	1	3	Latch-Up	Per AEC Q100-004	-	Device specific data [1]	1/6/0	1/6/0	1/3/0	-
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	Device specific data [1]	3/90/0	1/30/0	3/90/0	-

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

#### Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

#### E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2503-066

[1] Qual Device: ISO7763FQDWRQ1 and QBS Product Reference: ISO7763QDWRQ1 use the same silicon die.

**ZVEI ID:** SEM-PA-11

In performing change qualifications, Texas Instruments follows integrated circuit industry standards in performing defect mechanism analysis and failure mechanism-based accelerated environmental testing to ensure wafer fab process, assembly process and product quality and reliability. As encouraged by these standards, TI uses both product-specific and generic (family) data in qualifying its changes. For devices to be categorized as a 'product qualification family' for generic data purposes, they must share similar product, wafer fab process and assembly process elements. The applicability of generic data (also known at TI as Qualification by Similarity (QBS)) is determined by the Reliability Engineering function following these industry standards. Generic data is shown in the qualification report in columns titled "QBS Process" (for wafer fab process), "QBS Package" (for assembly process) and "QBS Product" (for product family).

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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