

PCN# 20241119001.2
**Qualification of FFAB using qualified Process Technology, Die Revision, and
additional Assembly/Test site & BOM options for select devices**
Change Notification / Sample Request

Date: November 20, 2024

To: MOUSER PCN

Dear Customer:

This is an announcement of a change to a device that is currently offered by Texas Instruments (TI). The details of this change are on the following pages, and are in alignment with our standard product change notification (PCN) [process](#).

TI requires acknowledgement of receipt of this notification within 30 days of the date of this notice. Lack of acknowledgement of this notice within 30 days constitutes acceptance and approval of this change. If samples or additional data are required, requests must be received within 30 days of this notification, given that samples are not built ahead of the change.

The Proposed First Ship date in this PCN letter is the earliest possible date that customers could receive the changed material. It is our commitment that the changed device will not ship before that date. If samples are requested within the 30 day sample request window, customers will still have 30-days to complete their evaluation regardless of the proposed 1st ship date.

Changes outlined in this notification underscore our commitment to product longevity and supply continuity, as well as our continued efforts to transition to newer, more efficient manufacturing processes and technologies. Specifically, this particular notification is related to TI's multiyear transition plan for our two remaining 150-millimeter production lines (DFAB in Dallas, Texas, and SFAB in Sherman, Texas). SFAB closure activities are expected to begin by the end of 2025. DFAB will remain open with a smaller set of 200mm technologies and GaN.

For questions regarding this notice or to provide acknowledgement of this PCN, you may contact your local Field Sales Representative or the Change Management team. For sample requests or sample related questions, contact your local Field Sales Representative. As always, we thank you for your continued business.

Change Management Team
SC Business Services

20241119001.2
Attachment: 1

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
LM7321QMFY/NOPB	NULL
LM7322QMAX/NOPB	LM7322QMAX/NOPB

Technical details of this Product Change follow on the next page(s).

PCN Number:	20241119001.2	PCN Date:	November 20, 2024
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Title: Qualification of FFAB using qualified Process Technology, Die Revision, and additional Assembly/Test site & BOM options for select devices

Customer Contact: Change Management team **Dept:** Quality Services

Proposed 1st Ship Date: May 19, 2025 **Sample requests accepted until:** December 20, 2024*

***Sample requests received after December 20, 2024 will not be supported.**

Change Type:					
<input checked="" type="checkbox"/>	Assembly Site	<input checked="" type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material
<input checked="" type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input checked="" type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>	Mechanical Specification	<input checked="" type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Materials
<input checked="" type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input checked="" type="checkbox"/>	Wafer Fab Process

PCN Details

Description of Change:

Texas Instruments is pleased to announce the addition of FFAB using the BICOMHD qualified process technology, die change and additional Assembly/Test site (MLA and TI-CDAT), and BOM options for the devices listed below.

Current Fab Site			Additional Fab Site		
Current Fab Site	Process	Wafer Diameter	Additional Fab Site	Process	Wafer Diameter
DFAB	VIP	200 mm	FFAB	BICOMHD	200 mm

The die was also changed as a result of the process change.

Construction differences are as follows:

Group 1:

	Current	Proposed
Probe Test	TIEM-PR	None
Assembly/Test site	TIEMA	TI CDAT
Wire diam/type	1.0mil Au	0.8mil Cu
Mount compound	8075531	4207123
Mold compound	8097131	4222198

Group 2:

	Current	Proposed
Probe Test	TIEM-PR	FR-BIP-1
Assembly/Test site	TIEMA	MLA
Wire diam/type	1.0mil Au	1.0mil Cu
Mount compound	8075531	4147858
Mold compound	8095179	4211880
Marking differences	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: auto;"> NS/ YMLL LM732 2QMA ○ </div>	<div style="border: 1px solid black; padding: 5px; width: fit-content; margin: auto;"> 7322Q TI YMS LLLL ○ </div>

Test coverage, insertions, conditions will remain consistent with current testing.

Qual details are provided in the Qual Data Section.

Reason for Change:

These changes are part of our multiyear plan to transition products from our 150-millimeter and 200-millimeter factories to newer, more efficient manufacturing processes and technologies, underscoring our commitment to product longevity and supply continuity.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings:

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
<input checked="" type="checkbox"/> No Change			

Changes to product identification resulting from this PCN:

Fab Site Information:

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
DL-LIN	DLN	USA	Dallas
FR-BIP-1	TID	DEU	Freising

Die Rev:

Current	New
Die Rev [2P] A, B	Die Rev [2P] A

Assembly/Test Site Information:

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
TIEMA	CU6	MYS	Melaka
TI CDAT	CDA	CHN	Chengdu
MLA	MLA	MYS	Kuala Lumpur

Sample product shipping label (not actual product label)

TEXAS INSTRUMENTS
 MADE IN: Malaysia
 2DC: 20:
 MSL 2 /260C/1 YEAR SEAL DT
 MSL 1 /235C/UNLIM 03/29/04
 OPT:
 ITEM: 39
 LBL: 5A (L)T0:1750

(1P) SN74LS07NSR
 (Q) 2000 (D) 0336
 (31T) LOT: 3959047MLA
 (4W) TKY (1T) 7523483SI2
 (P)
 (2P) REV: 0033317
 (20L) CSO: SHE (21L) CCO: USA
 (22L) ASO: MLA (23L) ACO: MYS

Group 1 Product Affected:

LM7321QMFN/NOPB

Group 2 Product Affected:

LM7322QMAX/NOPB

For alternate parts with similar or improved performance, please visit the product page on TI.com

**Automotive Qualification Summary
(As per AEC-Q100 Rev. J and JEDEC Guidelines)**

**OPA2994QDR - Q100 Q006 Grade 1 BiComHD FFAB - MLA D
Approve Date 07-October-2024**

Product Attributes

Attributes	Qual Device:	QBS Package Reference:	QBS Package, Process, Product Reference:
	<u>OPA2994QDRQ1</u>	<u>TLV9022QDRQ1</u>	<u>OPA2863QDRQ1</u>
Automotive Grade Level	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125
Product Function	Signal Chain	Signal Chain	Signal Chain
Wafer Fab Supplier	FR-BIP-1	RFAB	FR-BIP-1, FR-BIP-1
Assembly Site	MLA	MLA	MLA
Package Group	SOIC	-	SOIC
Package Designator	D	D	D
Pin Count	8	8	8

- QBS: Qual By Similarity, also known as Generic Data
- Qual Device OPA2994QDRQ1 is qualified at MSL1 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>OPA2994QDRQ1</u>	QBS Package Reference: <u>TLV9022QDRQ1</u>	QBS Package, Process, Product Reference: <u>OPA2863QDRQ1</u>
Test Group A - Accelerated Environment Stress Tests										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	1/Pass	3/Pass	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	-	3/915/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	1/77/0	3/231/0	3/231/0
ACUHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	1/77/0	3/231/0	3/231/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	3/231/0	3/231/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	1/45/0	-	-
Test Group B - Accelerated Lifetime Simulation Tests										
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	-	3/231/0	-
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	150C	300 Hours	1/77/0 ¹	-	3/231/0
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	-	3/2400/0
Test Group C - Package Assembly Integrity Tests										
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>OPA2994QDRQ1</u>	QBS Package Reference: <u>TLV9022QDRQ1</u>	QBS Package, Process, Product Reference: <u>OPA2863QDRQ1</u>
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	1/10/0	3/30/0	3/30/0
Test Group D - Die Fabrication Reliability Tests										
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Tddb	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests										
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	1/3/0	1/3/0
Additional Tests										
Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>OPA2994QDRQ1</u>	QBS Package Reference: <u>TLV9022QDRQ1</u>	QBS Package, Process, Product Reference: <u>OPA2863QDRQ1</u>
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	1/3/0	1/3/0	1/3/0
LU	E4	AEC Q100-004	1	3	Latch-Up	Per AEC Q100-004	-	1/3/0	1/6/0	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	2/60/0	3/90/0	3/90/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-NPD-2401-051

[1]-broken lead discounted

**Automotive Qualification Summary
(As per AEC and JEDEC Guidelines)**

**Q006 SOIC at MLA
Approve Date 07-October-2024**

Attributes	Qual Device: <u>OPA2994QDRQ1</u>	QBS Package Reference: <u>TLV9022QDRQ1</u>	QBS Package, Process, Product Reference: <u>OPA2863QDRQ1</u>
Automotive Grade Level	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125
Product Function	Signal Chain	Signal Chain	Signal Chain
Wafer Fab Supplier	FR-BIP-1	RFAB	FR-BIP-1, FR-BIP-1
Assembly Site	MLA	MLA	MLA
Package Group	SOIC	-	SOIC
Package Designator	D	D	D
Pin Count	8	8	8

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>OPA2994QDRQ1</u>	QBS Reference: <u>TLV9022QDRQ1</u>	QBS Reference: <u>OPA2863QDRQ1</u>
Test Group A - Accelerated Environment Stress Tests										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	1/0/0	3/0/0	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	-	3/915/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	1/22/0	3/66/0	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	1/22/0	3/66/0	3/66/0
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	3/231/0	3/231/0
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C/85%RH	192 Hours	-	3/231/0	3/231/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	-	3/66/0	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	-	3/3/0	3/2/0
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	-	3/9/0	3/9/0
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	-	3/9/0	3/9/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	-	3/9/0	3/9/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	3/231/0	3/231/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	1/22/0 ¹	-	-
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	1/1/0 ¹	-	-

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: OPA2994QDRQ1	QBS Reference: TLV9022QDRQ1	QBS Reference: OPA2863QDRQ1
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	1/3/0 ¹	-	-
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	1/3/0 ¹	-	-
TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	1/3/0 ¹	-	-
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	1/77/0	3/231/0	3/231/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	1/22/0	3/66/0	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	1/1/0	3/3/0	3/3/0
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	1/3/0	3/9/0	3/9/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	1/3/0	3/9/0	3/9/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	1/3/0	3/9/0	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	175C	500 Hours	1/45/0	-	-
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	1/1/0	-	-
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	-	3/135/0	3/135/0
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	175C	1000 Hours	1/45/0	-	-
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	1/1/0	3/3/0	3/3/0

Test Group C - Package Assembly Integrity Tests

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: OPA2994QDRQ1	QBS Reference: TLV9022QDRQ1	QBS Reference: OPA2863QDRQ1
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0

- QBS: Qual By Similarity, also known as Generic Data
- Qual Device OPA2994QDRQ1 is qualified at MSL1 260C

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-NPD-2401-051

[1]-2x

**Automotive Qualification Summary
(As per AEC-Q100 Rev. H and JEDEC Guidelines)**

**OPA994QDBVRQ1 Grade 1, Q100H, Q006
Approve Date 04-September-2024**

Product Attributes

Attributes	Qual Device:	QBS Process Reference:	QBS Product Reference:	QBS Package Reference:	QBS Package Reference:
	OPA994QDBVRQ1	OPA2863QDRQ1	LMH32404QWRHFRQ1	TPS3840PH30DBVRQ1	OPA992QDBVRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Signal Chain	Signal Chain	Signal Chain	Power Management	Signal Chain
Wafer Fab Supplier	FR-BIP-1	FR-BIP-1, FR-BIP-1	FR-BIP-1	RFAB	RFAB
Assembly Site	CDAT	MLA	CDAT	CDAT	CDAT
Package Group	SOT	SOIC	QFN	SOT	SOT
Package Designator	DBV	D	RHF	DBV	DBV
Pin Count	5	8	28	5	5

- QBS: Qual By Similarity, also known as Generic Data
- Qual Device OPA994QDBVRQ1 is qualified at MSL1 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device:	QBS Process Reference:	QBS Product Reference:	QBS Package Reference:	QBS Package Reference:
								OPA994QDBVRQ1	OPA2863QDRQ1	LMH32404QWRHFRQ1	TPS3840PH30DBVRQ1	OPA992QDBVRQ1
Test Group A - Accelerated Environment Stress Tests												
Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device:	QBS Process Reference:	QBS Product Reference:	QBS Package Reference:	QBS Package Reference:
								OPA994QDBVRQ1	OPA2863QDRQ1	LMH32404QWRHFRQ1	TPS3840PH30DBVRQ1	OPA992QDBVRQ1
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	1/0/0	-	-	3/0/0	1/0/0
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	3/915/0	3/1215/0	-	-
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	110C/85%RH	264 Hours	-	-	-	-	1/77/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	1/77/0	-	3/231/0	-	1/77/1 ^A
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	-	3/231/0	-
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	110C/85%RH	264 Hours	-	-	-	-	1/77/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	1/77/0	3/231/0	3/231/0	-	-
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	3/231/0	3/231/0	3/231/0	1/77/1 ^B
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	-	1/5/0	1/5/0	1/5/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	1/45/0	3/135/0	-	3/135/0	1/45/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	1/45/0	-	-
Test Group B - Accelerated Lifetime Simulation Tests												
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	1/77/0	-	3/231/0	3/231/0	-

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device:	QBS Process Reference:	QBS Product Reference:	QBS Package Reference:	QBS Package Reference:
								OPA994QDBVRQ1	OPA2863QDRQ1	LMH32404QWRHRQ1	TPS3840PH30DBVRQ1	OPA992QDBVRQ1
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	150C	300 Hours	-	3/231/0	-	-	1/77/0
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	3/2400/0	3/2400/0	-	-
Test Group C - Package Assembly Integrity Tests												
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0	3/90/0	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	3/90/0	3/90/0	1/30/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	1/15/0	1/15/0	1/15/0	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15/0	1/15/0	1/15/0	1/15/0	-
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	1/10/0	3/30/0	3/30/0	3/30/0	1/10/0
Test Group D - Die Fabrication Reliability Tests												
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements				
TDD	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements				
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements				
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements				
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements				
Test Group E - Electrical Verification Tests												
Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device:	QBS Process Reference:	QBS Product Reference:	QBS Package Reference:	QBS Package Reference:
								OPA994QDBVRQ1	OPA2863QDRQ1	LMH32404QWRHRQ1	TPS3840PH30DBVRQ1	OPA992QDBVRQ1
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	1/3/0	1/3/0	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	1/3/0	1/3/0	1/3/0	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	1/3/0	1/6/0	1/6/0	-	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	3/90/0	3/90/0	3/90/0	3/90/0	1/30/0
Additional Tests												

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

[A]-EIPD induced during handling. See 8D Report.
[B]-EIPD induced during handling. See 8D Report.

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-NPD-2302-107

TI Information
Selective Disclosure

**Automotive Qualification Summary
(As per AEC and JEDEC Guidelines)**

**Q006 {SOT} at {CDAT}
Approve Date 04-September-2024**

Attributes	Qual Device: <u>OPA994QDBVRQ1</u>	QBS Package Reference: <u>TPS3840PH30DBVRQ1</u>
Automotive Grade Level	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125
Product Function	Signal Chain	Power Management
Wafer Fab Supplier	FR-BIP-1	RFAB
Assembly Site	CDAT	CDAT
Package Group	SOT	SOT
Package Designator	DBV	DBV
Pin Count	5	5

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	QBS Reference: TPS3840PH30DBVRQ1
Test Group A - Accelerated Environment Stress Tests								
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	3/Pass
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	3/66/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	3/3/0 Utilized 2x data
HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	-	3/9/0 Utilized 2x data
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	-	3/9/0 Utilized 2x data
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	-	3/9/0 Utilized 2x data
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C/85%RH	192 Hours	3/210/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	3/3/0
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	3/9/0
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	3/9/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	3/9/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	3/66/0
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	3/3/0 Utilized 2x data
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	3/9/0 Utilized 2x data
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	3/9/0 Utilized 2x data
TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	3/9/0 Utilized 2x data
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	3/210/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3/0
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	3/9/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	3/9/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	3/3/0 Utilized 2x data
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	3/132/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	3/3/0
Test Group C - Package Assembly Integrity Tests								
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0

- QBS: Qual By Similarity, also known as Generic Data
- Qual Device OPA994QDBVRQ1 is qualified at MSL1 260C

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TI Qualification ID: R-NPD-2302-107

ZVEI ID's: SEM-DE-03, SEM-PW-09, SEM-PW-13, SEM-PA-07, SEM-PA-08, SEM-PA-11, SEM-PA-18, SEM-QG-01, SEM-TF-01, SEM-PA-13

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