

**Notification# 20240722000.0
LP876242-Q1 NVM Configuration change
Information Only**

Date: July 22, 2024
To: MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team
SC Business Services

20240722000.0
Attachment: 1

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
LP876242B0RQKRQ1	NULL

Technical details of this Product Change follow on the next page(s).

Notification Number:	20240722000.0	PCN Date:	July 22, 2024
Title:	LP876242-Q1 NVM Configuration change		
Customer Contact:	Change Management team	Dept:	Quality Services
Change Type:			
<input type="checkbox"/> Assembly Site	<input checked="" type="checkbox"/> Design	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Data Sheet	<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Part number change	<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Site	<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Process	<input type="checkbox"/>	Wafer Fab Process
N Details			
Description of Change:			
This notification is to communicate the NVM configuration change for the LP876242-Q1. Affected devices are listed in the Product Affected section of this document.			
The NVM configuration was updated to disable clock monitoring inside the PMIC by default.			
As a result, the Register NVM_CODE_2 was changed from 0x2 to 0x3.			
Reason for Change:			
Improved device functionality			
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
None.			
Product Affected:			
LP876242B0RQKRQ1			

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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