



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

PCN# 20240628000.2

**Add Cu as Alternative Wire Base Metal for Selected Device(s)
Change Notification / Sample Request**

Date: June 28, 2024

To: Mouser PCN

Dear Customer:

This is an announcement of a change to a device that is currently offered by Texas Instruments. The details of this change are on the following pages.

Texas Instruments requires acknowledgement of receipt of this notification within **30** days of the date of this notice. Lack of acknowledgement of this notice within 30 days constitutes acceptance of the change. If samples or additional data are required, requests must be received within **30 days** of this notification.

The changes discussed within this PCN will not take effect any earlier than the proposed first ship date on Page 3 of this notification, unless customer agreement has been reached on an earlier implementation of the change.

This notice does not change the end-of-life status of any product. Should product affected be on a previously issued product withdrawal/discontinuance notice, this notification does not extend the life of that product or change the life time buy offering/discontinuance plan.

For questions regarding this notice or to provide acknowledgement of this PCN, you may contact your local Field Sales Representative or the Change Management team.

For sample requests or sample related questions, contact your local Field Sales Representative.

Sincerely,

Change Management Team
SC Business Services

20240628000.2
Change Notification / Sample Request
Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
AMC1200TDWVRQ1	NULL

Technical details of this Product Change follow on the next page(s).

PCN Number:	20240628000.2			PCN Date:	June 28, 2024								
Title:	Add Cu as Alternative Wire Base Metal for Selected Device(s)												
Customer Contact:	Change Management team		Dept:	Quality Services									
Proposed 1st Ship Date:	December 25, 2024		Estimated Sample Availability:	July 28, 2024									
*Sample requests received after July 28, 2024 will not be supported.													
Change Type:													
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material								
<input checked="" type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process								
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Fab Site								
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Material								
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Process								
PCN Details													
Description of Change:													
Texas Instruments is pleased to announce the qualification of new assembly material set to add Cu as an additional bond wire option for devices listed in "Product affected" section below. Devices will remain in current assembly facility and piece part changes as follows:													
<table border="1"> <thead> <tr> <th>Material</th> <th>Current</th> <th>Proposed</th> </tr> </thead> <tbody> <tr> <td>Wire type</td> <td>0.96mil Au</td> <td>0.8mil Cu</td> </tr> </tbody> </table>						Material	Current	Proposed	Wire type	0.96mil Au	0.8mil Cu		
Material	Current	Proposed											
Wire type	0.96mil Au	0.8mil Cu											
Reason for Change:													
Continuity of supply.													
<ol style="list-style-type: none"> 1) To align with world technology trends and use wiring with enhanced mechanical and electrical properties 2) Maximize flexibility within our Assembly/Test production sites. 3) Cu is easier to obtain and stock 													
Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):													
None													
Impact on Environmental Ratings:													
Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.													
<table border="1"> <thead> <tr> <th>RoHS</th> <th>REACH</th> <th>Green Status</th> <th>IEC 62474</th> </tr> </thead> <tbody> <tr> <td><input checked="" type="checkbox"/> No Change</td> </tr> </tbody> </table>						RoHS	REACH	Green Status	IEC 62474	<input checked="" type="checkbox"/> No Change			
RoHS	REACH	Green Status	IEC 62474										
<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change										
Changes to product identification resulting from this PCN:													
None													
Product Affected:													
AMC1200TDWVRQ1													

Qualification Report
 Automotive Qualification Summary
 (As per AEC-Q100 Rev. H and JEDEC Guidelines)
 Approve Date 21-February-2024

Product Attributes

Attributes		Qual Device: <u>ISO6721QDWVRQ1</u>	QBS Process Reference: <u>UCC23513QDWYQ1</u>	QBS Package Reference: <u>ISO6763QDWRQ1</u>	QBS Package Reference: <u>AMC1311CQDWVRQ1</u>
Automotive Grade Level		Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)		-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function		Interface	Power Management	Interface	Signal Chain
Wafer Fab Supplier		RFAB, RFAB	RFAB, RFAB	RFAB, RFAB	AIZU, AIZU, MH8, MH8
Assembly Site		MLA	TAI	MLA	MLA
Package Group		SOIC	SOIC	SOIC	SOIC
Package Designator		DWV	DWY	DW	DWV
Pin Count		8	6	16	8

QBS: Qual By Similarity

Qual Device ISO6721QDWVRQ1 is qualified at MSL2 260C

Qual Device ISO6721FQDWVRQ1 is qualified at MSL2 260C

Qual Device ISO6720QDWVRQ1 is qualified at MSL2 260C

Qual Device ISO6720FQDWVRQ1 is qualified at MSL2 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>ISO6721QDWVRQ1</u>	QBS Process Reference: <u>UCC23513QDWYQ1</u>	QBS Package Reference: <u>ISO6763QDWRQ1</u>	QBS Package Reference: <u>AMC1311CQDWVRQ1</u>
Test Group A - Accelerated Environment Stress Tests											
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	1/154/0	-	3/828/0	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	-	-	-	-	3/828/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	3/231/0	3/231/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	1/77/0	-	3/231/0	-
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	-	-	-	3/231/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	-	3/231/0	3/231/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	-	1/5/0	1/5/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	-	3/135/0	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	3/135/0
Test Group B - Accelerated Lifetime Simulation Tests											

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6721QDWVRQ1	QBS Process Reference: UCC23513QDWYQ1	QBS Package Reference: ISO6763QDWQ1	QBS Package Reference: AMC1311CQDWVRQ1
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	-	3/231/0	-	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	3/2400/0	-	-
Test Group C - Package Assembly Integrity Tests											
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	3/90/0	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	-	1/15/0
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	-	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	-	-	-	3/30/0
Test Group D - Die Fabrication Reliability Tests											
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
BTI	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests											
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	1/3/0	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	750 Volts	1/3/0	1/3/0	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	1/6/0	-	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	3/90/0	3/90/0	1/30/0

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles
Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

Qualification Report

Automotive Qualification Summary (As per AEC and JEDEC Guidelines)

Q006 SOIC at MLA and TAI
Approve Date 19-OCTOBER -2023

Product Attributes

Attributes		Qual Device: <u>ISO6763QDWRQ1</u>	Qual Device: <u>UCC21540QDWKRQ1</u>	QBS Package Reference: <u>PUCC21550ADWKR</u>
Automotive Grade Level		Grade 1	Grade 1	Grade 1
Operating Temp Range (C)		-40 to 125	-40 to 125	-40 to 125
Wafer Fab Supplier		RFAB, RFAB	MH8, MH8, MH8	DMOS6, DMOS6, MH8
Assembly Site		MLA	TAI	TAI
Package Group		SOIC	SOIC	SOIC
Package Designator		DW	DWK	DWK
Pin Count		16	14	14

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <u>ISO6763QDWRQ1</u>	Qual Device: <u>UCC21540QDWKRQ1</u>	QBS Reference: <u>PUCC21550ADWKR</u>
Test Group A - Accelerated Environment Stress Tests										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	3/597/0	1/199/0	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	-	-	-	3/693/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	3/66/0	1/22/0	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	3/66/0	1/22/0	3/66/0
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231/0	1/77/0	3/231/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	3/3/0	1/1/0	-
HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	-	3/9/0	1/3/0	3/9/0
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	-	3/9/0	1/3/0	3/9/0
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	-	3/9/0	1/3/0	3/9/0
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C/85%RH	192 Hours	3/210/0	1/70/0	3/210/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	3/66/0	1/22/0	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	3/3/0	1/1/0	3/3/0
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	3/9/0	1/3/0	3/9/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6763QDWRQ1	Qual Device: UCC21540QDWKRQ1	QBS Reference: PUCC21550ADWKR
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	3/9/0	1/3/0	3/9/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	3/9/0	1/3/0	3/9/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-55C/150C	1000 Cycles	-	-	3/231/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231/0	1/77/0	-
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	3/66/0	1/22/0	3/66/0
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	3/3/0	1/1/0	-
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	3/9/0	1/3/0	3/9/0
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	3/9/0	1/3/0	3/9/0
TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	3/9/0	1/3/0	3/9/0
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-55C/150C	2000 Cycles	-	-	3/210/0
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	3/210/0	1/70/0	-
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66/0	1/22/0	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3/0	1/1/0	3/3/0
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	3/9/0	1/3/0	3/9/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	3/9/0	1/3/0	3/9/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	3/9/0	1/3/0	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	3/135/0	1/45/0	-
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	175C	500 Hours	-	-	3/231/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	3/3/0	1/1/0	3/3/0
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	3/132/0	1/44/0	-
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	175C	1000 Hours	-	-	3/228/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	3/3/0	1/1/0	3/3/0
Test Group C - Package Assembly Integrity Tests										
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0

QBS: Qual By Similarity

Qual Device ISO6763QDWRQ1 is qualified at MSL2 260C

Qual Device UCC21540QDWKRQ1 is qualified at MSL3 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

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Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

ZVEI ID: SEM-PA-08

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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