



**Notification# # 20240507001.0  
MSL Improvement for Select Devices  
Information Only**

**Date:** May 08, 2024  
**To:** MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team  
SC Business Services

**20240507001.0**

**Attachment: 1**

**Products Affected:**

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

<b>DEVICE</b>	<b>CUSTOMER PART NUMBER</b>
TPS54240QDRCRQ1	NULL
TPS54260QDRCRQ1	NULL
TPS54260QDRCTQ1	NULL
TPS57060QDRCRQ1	NULL
TPS57140QDRCRQ1	NULL
TPS57160QDRCRQ1	NULL
TPS62067AQDSGRQ1	NULL
TPS62067QDSGRQ1	NULL

Technical details of this Product Change follow on the next page(s).

<b>PCN Number:</b>	20240507001.0		<b>PCN Date:</b>	May 08, 2024						
<b>Title:</b>	MSL Improvement for Select Devices									
<b>Customer Contact:</b>	Change Management team		<b>Dept:</b>	Quality Services						
<b>Change Type:</b>										
<input type="checkbox"/> Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material						
<input type="checkbox"/> Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process						
<input type="checkbox"/> Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Fab Site						
<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Materials						
<input checked="" type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Process						
<b>PCN Details</b>										
<b>Description of Change:</b>										
This notice is to communicate an update in the MSL Level for the devices in the product affected selection as follows:										
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center; padding: 5px;">Current</th> <th style="text-align: center; padding: 5px;">New</th> </tr> </thead> <tbody> <tr> <td style="text-align: center; padding: 5px;">Moisture Sensitivity Level (MSL)</td> <td style="text-align: center; padding: 5px;">MSL3, MSL2</td> </tr> <tr> <td style="text-align: center; padding: 5px;"></td> <td style="text-align: center; padding: 5px;">MSL1</td> </tr> </tbody> </table>					Current	New	Moisture Sensitivity Level (MSL)	MSL3, MSL2		MSL1
Current	New									
Moisture Sensitivity Level (MSL)	MSL3, MSL2									
	MSL1									
<b>Reason for Change:</b>										
Standardization										
<b>Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):</b>										
None										
<b>Changes to product identification resulting from this PCN:</b>										
None										
<b>Product Affected:</b>										
TPS54240QDRCRQ1	TPS57040QDRCRQ1	TPS57160QDRCRQ1	TPS62067QDSGRQ1							
TPS54260QDRCRQ1	TPS57060QDRCRQ1	TPS57160SDRCRWB								
TPS54260QDRCTQ1	TPS57140QDRCRQ1	TPS62067AQDSGRQ1								

**Qualification Report**  
 Automotive New Product Qualification Summary  
 (As per AEC-Q100, AEC-Q006, and JEDEC Guidelines)  
 Approve Date 25-MAY -2023

**Qualification Results**  
 Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: <a href="#">TLC69601QRTWRQ1</a>	QBS Reference: <a href="#">LM2775QDSGRQ1</a>	QBS Reference: <a href="#">BQ51013BQWRHLRQ1</a>
<b>Test Group A - Accelerated Environment Stress Tests</b>										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	1 Step	3/All/0	-	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	1 Step	-	3/All/0	3/All/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	1 Step	3/66/0	3/66/0	-
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	1 Step	3/66/0	3/66/0	-
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231/0	-	-
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	1/1/0	3/3/0	-
HAST	A2.1.3	-	3	30	Wire Bond Shear, post bHAST, 1X	Post stress	Wires	1/3/0	3/9/0	-
HAST	A2.1.4	-	3	30	Bond Pull over Stitch, post bHAST, 1X	Post stress	Wires	1/3/0	3/9/0	-
HAST	A2.1.5	-	3	30	Bond Pull over Ball, post bHAST, 1X	Post stress	Wires	1/3/0	3/9/0	-
HAST	A2.2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	192 Hours	3/231/0	3/231/0	-
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	1/22/0	3/66/0	-
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	1/1/0	3/3/0	-
HAST	A2.2.3	-	3	30	Wire Bond Shear, post bHAST, 2X	Post stress	Wires	1/3/0	3/9/0	-
HAST	A2.2.4	-	3	30	Bond Pull over Stitch, post bHAST, 2X	Post stress	Wires	1/3/0	3/9/0	-
HAST	A2.2.5	-	3	30	Bond Pull over Ball, post bHAST, 2X	Post stress	Wires	1/3/0	3/9/0	-
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	3/66/0	3/66/0	-
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	3/3/0	3/3/0	-
TC	A4.1.3	-	3	30	Wire Bond Shear, post TC, 1X	Post stress	Wires	3/9/0	3/9/0	-
TC	A4.1.4	-	3	30	Bond Pull over Stitch, post TC, 1X	Post stress	Wires	3/9/0	3/9/0	-
TC	A4.1.5	-	3	30	Bond Pull over Ball, post TC, 1X	Post stress	Wires	3/9/0	3/9/0	-

TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	1000 Cycles	3/231/0	3/231/0	3/231/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66/0	3/66/0	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3/0	3/3/0	3/3/0
TC	A4.2.3	-	3	30	Wire Bond Shear, post TC, 2X	Post stress	Wires	3/9/0	3/9/0	3/9/0
TC	A4.2.4	-	3	30	Bond Pull over Stitch, post TC, 2X	Post stress	Wires	3/9/0	3/9/0	3/9/0
TC	A4.2.5	-	3	30	Bond Pull over Ball, post TC, 2X	Post stress	Wires	3/9/0	3/9/0	3/9/0
PTC	A5.2	JEDEC JESD22-A105	1	45	PTC	-40/125C	2000 Cycles	-	1/45/0	-
HTSL	A6.2	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	3/135/0		
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	175C	500 Hours	-	3/135/0	-
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	1/1/0	3/3/0	-
HTSL	A6.2	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	2000 Hours	3/135/0	-	-
HTSL	A6.2	JEDEC JESD22-A103	3	45	High Temperature Storage Life	175C	1000 Hours	-	3/135/0	-
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	1/1/0	3/3/0	-

#### Test Group B - Accelerated Lifetime Simulation Tests

#### Test Group C - Package Assembly Integrity Tests

WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	3/90/0	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	1/15/0	1/15/0
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	1/15/0	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	3/30/0	3/30/0	3/30/0

#### Test Group D - Die Fabrication Reliability Tests

EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements

QBS: Qual By Similarity

Qual Device TLC69601QRTWRQ1 is qualified at MSL1 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

**Ambient Operating Temperature by Automotive Grade Level:**

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

**E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):**

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

ZVEI ID reference: SEM-PS-02, SEM-PS-04

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative

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