



**12500 TI Boulevard, MS 8640, Dallas, Texas 75243**

**Notification# 20260106000.0  
Datasheet for DP83TC814x-Q1  
Information Only**

**Date:** January 08, 2026  
**To:** MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team  
SC Business Services

**20260106000.0**  
**Information Only Datasheet**  
**Attachments**

**Products Affected:**

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

<b>DEVICE</b>	<b>CUSTOMER PART NUMBER</b>
DP83TC814SRHARQ1	NULL
DP83TC814RRHARQ1	NULL

Technical details of this Product Change follow on the next page(s).

<b>PCN Number:</b>	20260106000.0	<b>PCN Date:</b>	January 08, 2026
<b>Title:</b>	Datasheet for DP83TC814x-Q1		
<b>Customer Contact:</b>	Change Management team	<b>Dept:</b>	Quality Services
<b>Change Type:</b>	Electrical Specification		

### PCN Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



**DP83TC814S-Q1, DP83TC814R-Q1**

SNLS663A – DECEMBER 2021 – REVISED DECEMBER 2025

<b>Changes from Revision * (December 2021) to Revision A (December 2025)</b>	<b>Page</b>
• Updated the CLKOUT frequency to be 50MHz in RMII Follower mode in the Pin Configuration and Functions section.....	4
• Added line to CLKOUT/GPIO2 description about which registers to program to disable switching in the <i>Pin Configuration and Functions</i> section.....	4
• Added line to INT pin description, "Reg 12-13 is recommended to be read only when INT_N is LOW", in the <i>Pin Configuration and Functions</i> section.....	4
• Added corrections for maximum clock rate in MDC description of the <i>Pin Functions</i> table .....	4
• Updated MDIO pin description to include link to Compliance Test Modes section in the <i>Pin Configuration and Functions</i> section.....	16
• Removed supply ramp delay offset for all supplies in the <i>Timing Requirements</i> section.....	16
• Updated lozh to clarify mapping of Rx_Ctrl and Rx_ER pins in the <i>Electrical Characteristics</i> table.....	16
• Added corrections to the TDR description and deleted Register 0x310, Bit 8 in the <i>Time Domain Reflectometry</i> section.....	33
• Updated and corrected order of register writes for packet generation in the BIST and Loopback Modes section.....	35
• Clarified register reads for checking incoming data on the MAC side.....	37
• Added corrections to the RGMII Transmit Encoding table for Normal Data Transmission and Transmit Error Propagation .....	45
• Added corrections to the RGMII Transmit Encoding table for Normal Data Transmission and Transmit Error Propagation.....	47
• Added correction to note referring to SNLA389 Application Note .....	53
• Updated PHY Address Bootstraps: Binary values corrected to match hex settings .....	58
• Clarified and updated Bit Descriptions throughout the <i>Registers</i> .....	58
• Updated RMII Follower typical application diagram for correct XI configuration in the <i>Typical Applications</i> section .....	146
• Updated Typical Application (General) Diagram in the <i>Typical Applications</i> .....	146
• Updated RGMII Typical Application diagram to include 25MHz input.....	146

The datasheet number will be changing.

Device Family	Change From:	Change To:
DP83TC814x-Q1	SNLS663	<b>SNLS663A</b>

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/DP83TC814S-Q1>

#### Reason for Change:

To accurately reflect device characteristics.

#### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact.			
<b>Changes to product identification resulting from this PCN:</b>			
None.			
<b>Product Affected:</b>			
DP83TC814RRHARQ1	DP83TC814RRHARQ1. A	DP83TC814RRHATQ1	DP83TC814RRHATQ1. A
DP83TC814SRHARQ1	DP83TC814SRHARQ1. A	DP83TC814SRHATQ1	DP83TC814SRHATQ1. A

ZVEI ID: SEM-DS-02

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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