



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

**Notification# 20251029000.0
Datasheet for CDCE6214-Q1
Information Only**

Date: October 30, 2025
To: MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team
SC Business Services

20251029000.0
Information Only Datasheet
Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
CDCE6214TWRGETQ1	NULL

Technical details of this Product Change follow on the next page(s).

PCN Number:	20251029000.0		PCN Date:	October 30, 2025																												
Title:	Datasheet for CDCE6214-Q1																															
Customer Contact:	Change Management team	Dept:	Quality Services																													
Change Type:	Electrical Specification																															
PCN Details																																
Description of Change:																																
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.  CDCE6214-Q1 SNAS786C – JULY 2020 – REVISED JULY 2025 <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">Changes from Revision B (October 2021) to Revision C (July 2025)</th> <th style="text-align: right;">Page</th> </tr> </thead> <tbody> <tr> <td>• Updated the numbering format for tables, figures, and cross-references throughout the document.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated PCIe Generation compliance in the <i>Features</i> section.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated PCIe Generation compliance in the <i>Applications</i> section.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Deleted <i>Description (cont.)</i> section.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated Package Information table.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated the "Type" footnote for the <i>Pin Functions</i> table to consolidate information.....</td> <td style="text-align: right;">4</td> </tr> <tr> <td>• Updated capitalizations of letters throughout the <i>Specifications</i> section.....</td> <td style="text-align: right;">7</td> </tr> <tr> <td>• Updated to clarify footnote ⁽³⁾ of <i>Timing Requirements, I²C-Compatible Serial Interface</i></td> <td style="text-align: right;">13</td> </tr> <tr> <td>• Updated SSC_EN capabilities in the <i>Control Pins</i> section.....</td> <td style="text-align: right;">32</td> </tr> <tr> <td>• Updated factory configurations in the <i>Detailed Design Procedure</i> section.....</td> <td style="text-align: right;">44</td> </tr> <tr> <td>• Added the <i>Power Supply Recommendations</i> and <i>Layout</i> sections to the <i>Application and Implementation</i> section.....</td> <td style="text-align: right;">45</td> </tr> <tr> <td>• Updated <i>Layout Guidelines</i> to correct link to Figure 8-5</td> <td style="text-align: right;">46</td> </tr> <tr> <td>• Added references to EVM User's Guide and Register Map User Guide.....</td> <td style="text-align: right;">48</td> </tr> </tbody> </table>					Changes from Revision B (October 2021) to Revision C (July 2025)	Page	• Updated the numbering format for tables, figures, and cross-references throughout the document.....	1	• Updated PCIe Generation compliance in the <i>Features</i> section.....	1	• Updated PCIe Generation compliance in the <i>Applications</i> section.....	1	• Deleted <i>Description (cont.)</i> section.....	1	• Updated Package Information table.....	1	• Updated the "Type" footnote for the <i>Pin Functions</i> table to consolidate information.....	4	• Updated capitalizations of letters throughout the <i>Specifications</i> section.....	7	• Updated to clarify footnote ⁽³⁾ of <i>Timing Requirements, I²C-Compatible Serial Interface</i>	13	• Updated SSC_EN capabilities in the <i>Control Pins</i> section.....	32	• Updated factory configurations in the <i>Detailed Design Procedure</i> section.....	44	• Added the <i>Power Supply Recommendations</i> and <i>Layout</i> sections to the <i>Application and Implementation</i> section.....	45	• Updated <i>Layout Guidelines</i> to correct link to Figure 8-5	46	• Added references to EVM User's Guide and Register Map User Guide.....	48
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The datasheet number will be changing. <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 40%;">Device Family</td> <td style="width: 30%;">Change From:</td> <td style="width: 30%;">Change To:</td> </tr> <tr> <td>CDCE6214-Q1</td> <td>SNAS786B</td> <td>SNAS786C</td> </tr> </table> These changes may be reviewed at the datasheet links provided. http://www.ti.com/product/CDCE6214-Q1					Device Family	Change From:	Change To:	CDCE6214-Q1	SNAS786B	SNAS786C																						
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To accurately reflect device characteristics.																																
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No anticipated impact.																																
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CDCE6214TWRGETQ1	CDCE6214TWRGERQ	1																														

ZVEI ID: SEM-DS-02

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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