



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

**Notification# 20250225005.0
Datasheet for DP83825I
Information Only**

Date: March 05, 2025
To: MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team
SC Business Services

20250225005.0
Information Only Datasheet
Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
DP83825IRMQR	DP83825IRMQR
DP83825IRMQT	DP83825IRMQT
DP83825IRMQR	99

Technical details of this Product Change follow on the next page(s).

PCN Number:	20250225005.0		PCN Date:	March 05, 2025																										
Title:	Datasheet for DP83825I																													
Customer Contact:	Change Management team	Dept:	Quality Services																											
Change Type:	Electrical Specification																													
PCN Details																														
Description of Change:																														
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.  DP83825I SNLS638B – DECEMBER 2018 – REVISED JANUARY 2025 <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">Changes from Revision A (August 2019) to Revision B (January 2025)</th> <th style="text-align: right;">Page</th> </tr> </thead> <tbody> <tr> <td>• Updated the numbering format for tables, figures, and cross-references throughout the document.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Changed the amount of time to hold RST_N in Pin Functions Table.....</td> <td style="text-align: right;">3</td> </tr> <tr> <td>• Changed Power Supply sequencing to say VDDIO before AVDD under Power-Up Timing (T2).....</td> <td style="text-align: right;">10</td> </tr> <tr> <td>• Added 0ms as minimum time for T2 under Power-up Timing.....</td> <td style="text-align: right;">10</td> </tr> <tr> <td>• RMII Master/Slave changed to RMII Transmit/Receive to better reflect the timing diagrams.....</td> <td style="text-align: right;">10</td> </tr> <tr> <td>• Added how long to hold down reset in case of unstable clock.....</td> <td style="text-align: right;">12</td> </tr> <tr> <td>• Changed Figure 6-2 and Figure 6-3</td> <td style="text-align: right;">24</td> </tr> <tr> <td>• Changed extended register access section for clarity.....</td> <td style="text-align: right;">28</td> </tr> <tr> <td>• Changed Table 6-5, Table 6-6 and Table 6-7 to move write to address 0x001F as the last step.....</td> <td style="text-align: right;">37</td> </tr> <tr> <td>• Flipped the bit values in register 0x17 (bits 2 and 3) and 0x4D1 (bits 0 and 3).....</td> <td style="text-align: right;">40</td> </tr> <tr> <td>• Added VOD Configuration section.....</td> <td style="text-align: right;">95</td> </tr> <tr> <td>• Added Capacitive Blocking section.....</td> <td style="text-align: right;">101</td> </tr> </tbody> </table>					Changes from Revision A (August 2019) to Revision B (January 2025)	Page	• Updated the numbering format for tables, figures, and cross-references throughout the document.....	1	• Changed the amount of time to hold RST_N in Pin Functions Table.....	3	• Changed Power Supply sequencing to say VDDIO before AVDD under Power-Up Timing (T2).....	10	• Added 0ms as minimum time for T2 under Power-up Timing.....	10	• RMII Master/Slave changed to RMII Transmit/Receive to better reflect the timing diagrams.....	10	• Added how long to hold down reset in case of unstable clock.....	12	• Changed Figure 6-2 and Figure 6-3	24	• Changed extended register access section for clarity.....	28	• Changed Table 6-5 , Table 6-6 and Table 6-7 to move write to address 0x001F as the last step.....	37	• Flipped the bit values in register 0x17 (bits 2 and 3) and 0x4D1 (bits 0 and 3).....	40	• Added VOD Configuration section.....	95	• Added Capacitive Blocking section.....	101
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These changes may be reviewed at the datasheet links provided. http://www.ti.com/product/DP83825I																														
Reason for Change:																														
To accurately reflect device characteristics.																														
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):																														
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device																														
Changes to product identification resulting from this PCN:																														
None.																														
Product Affected:																														
DP83825IRMQR	DP83825IRMQT																													

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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