



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

**Notification# 20240923004.0
Datasheet for LMK03328
Information Only**

Date: September 25, 2024

To: MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team
SC Business Services

20240923004.0
Information Only Datasheet
Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

| DEVICE | CUSTOMER PART NUMBER |
|--------------|----------------------|
| LMK03328RHST | NULL |

Technical details of this Product Change follow on the next page(s).

| | | | |
|--------------------------|--------------------------|------------------|--------------------|
| PCN Number: | 20240923004.0 | PCN Date: | September 25, 2024 |
| Title: | Datasheet for LMK03328 | | |
| Customer Contact: | Change Management team | Dept: | Quality Services |
| Change Type: | Electrical Specification | | |

PCN Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



LMK03328

SNAS668E – AUGUST 2015 – REVISED SEPTEMBER 2024

| Changes from Revision D (April 2018) to Revision E (July 2024) | Page |
|--|-------------|
| • Updated the numbering format for tables, figures, and cross-references throughout the document. Updated units to the latest TI standards throughout the document. Updated phrases to latest TI inclusive language standards throughout the document..... | 1 |
| • Added links for each application..... | 1 |
| • Changed the <i>Device Information</i> table to <i>Package Information</i> , moved detailed description to <i>Overview</i> section..... | 1 |
| • Added Gen 5 and Gen 6 to <i>PCIe Clock Output Jitter</i> table..... | 16 |
| • Changed the <i>Device Information</i> table to <i>Package Information</i> , moved <i>Detailed Description</i> from <i>Description</i> section to <i>Overview</i> section..... | 26 |
| • Changed the <i>Detailed Description</i> by moving the <i>Description</i> section to <i>Overview</i> section..... | 26 |
| • Changed PLL loop bandwidth programming in the <i>Loop Filter</i> section to match the <i>PLL1_CALCTRL1 Register; R120</i> table..... | 41 |
| • Clarified use case for First Order Modulator..... | 43 |
| • Updated the <i>Device Vitals Selection Matrix</i> for <i>STATUS[1:0]</i> table reference in <i>Status Outputs</i> | 49 |
| • Added information to the soft pin mode description in the <i>I²C Serial Interface</i> section..... | 52 |
| • Added row for <i>POR_CTRL</i> register, updated register count from 123 to 124. Updated instances of <i>Itail</i> to <i>I_{TAIL}</i> | 66 |
| • Changed the <i>PINMODE_SW Register; R8</i> <i>GPIO32_SW_MODE[2:0]</i> <i>GPIO[2]</i> bit fields from: Z to: VIM | 70 |
| • Added Target Address programming information to the <i>TARGETADR_GPIO1_SW[7:1]</i> bits in <i>TARGETADR Register; R10</i> | 71 |
| • Changed <i>RESETN_SW</i> bit description in <i>DEV_CTL Register; R12</i> | 71 |
| • Added the VIH and VIL voltages for the <i>LVL_SEL_PRI[1:0]</i> bits in <i>REFDETCTL Register; R25</i> | 79 |
| • Updated definition of <i>PLL1_LOOPBW</i> bit..... | 107 |
| • Updated definition of <i>PLL2_LOOPBW</i> bit..... | 108 |
| • Added <i>NVMSCRC Register; R135</i> table to the <i>Register Maps</i> section..... | 108 |
| • Added <i>POR_CTRL</i> register..... | 111 |
| • Clarified EEPROM map description, added <i>PLL1_POR_SLOW</i> and <i>PLL2_POR_SLOW</i> to EEPROM map..... | 112 |
| • Updated jitter discussion for clarity..... | 116 |
| • Replaced mentions of <i>WEBENCH Clock Architect Tool</i> with <i>PLLatinum Sim</i> and <i>Clock Tree Architect</i> in the <i>Detailed Design Procedure</i> sections..... | 121 |
| • Changed the <i>Device Configuration</i> section..... | 123 |
| • Updated recommendation for separate supply use case, added clarification for noise coupling into device..... | 128 |
| • Added the capacitor values in the <i>Powering Up From Split Supply Rails</i> section..... | 129 |
| • Changed <i>Ensure Thermal Reliability</i> title to "Assess Thermal Reliability" | 131 |
| • Added the <i>Documentation Support</i> and <i>Related Documentation</i> sections..... | 133 |
| • Added links to <i>Related Documentation</i> | 133 |

The datasheet number will be changing.

Device Family

Change From:

Change To:

LMK03328

SNAS668D

SNAS668E

These changes may be reviewed at the datasheet links provided.
<http://www.ti.com/product/LMK03328>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device

Changes to product identification resulting from this PCN:

None.

Product Affected:

| | | | |
|----------------|----------------|----------------|----------------|
| LMK03328RHSR | LMK03328RHST | LMK03328RHSRC6 | LMK03328RHSTC4 |
| LMK03328RHSRC1 | LMK03328RHSTC1 | LMK03328RHSRC4 | LMK03328RHSTC7 |
| LMK03328RHSRC3 | LMK03328RHSTC2 | LMK03328RHSRC9 | LMK03328RHSRC2 |
| LMK03328RHSRC8 | LMK03328RHSTN1 | LMK03328RHSTC3 | LMK03328RHSRN1 |
| LMK03328RHSRC7 | LMK03328RHSTC6 | LMK03328RHSTC8 | LMK03328RHSTC9 |

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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