



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

**Notification# 20240619003.0
Datasheet for LMKDB1xxx
Information Only**

Date: June 20, 2024
To: MOUSER PCN

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this notification are for your information only.

Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team
SC Business Services


20240619003.0
Information Only Datasheet
Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
LMKDB1120Z100NPPR	NULL
LMKDB1120Z100NPPT	NULL
LMKDB1120Z85NPPR	NULL
LMKDB1120Z85NPPT	NULL

Technical details of this Product Change follow on the next page(s).

PCN Number:	20240619003.0		PCN Date:	June 20, 2024																														
Title:	Datasheet for LMKDB1xxx																																	
Customer Contact:	Change Management team	Dept:	Quality Services																															
Change Type:	Electrical Specification																																	
PCN Details																																		
Description of Change:																																		
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.</p> <div style="display: flex; justify-content: space-between; align-items: center;"> <div style="text-align: center;">  </div> <div style="text-align: right;"> LMKDB1102, LMKDB1202, LMKDB1104, LMKDB1204, LMKDB1108, LMKDB1120 <small>SNAS855D – NOVEMBER 2023 – REVISED JUNE 2024</small> </div> </div> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">Changes from Revision C (May 2024) to Revision D (June 2024)</th> <th style="text-align: right;">Page</th> </tr> </thead> <tbody> <tr> <td>• Updated the number format for tables, figures, and cross-references throughout the document.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated R17 register 7:4 name to "AMP" to match GUI name for LMKDB1108:.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated R18 table to stack Reserved bits 5:4 in one row for LMKDB1108.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Exposed registers R5 to change REV_ID for LMKDB1108.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Exposed R91 and R92 to show output slew rate control for LMKDB1108.....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Corrected register names that control correct outputs of chip (R0, R1, R2, R3, R20, R21) for LMKDB1204....</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Updated ^vSADR1_tri to ^vSADR0_tri for pin 4 in Figure 5-2</td> <td style="text-align: right;">3</td> </tr> <tr> <td>• Updated pin 7 name and description from "VDD" to "VDDA" in Figure 5-2.....</td> <td style="text-align: right;">3</td> </tr> <tr> <td>• Updated pin 10 name and description from "VDDA" to "VDD" in Figure 5-2</td> <td style="text-align: right;">3</td> </tr> <tr> <td>• Updated functional block diagram Figure 8-2 to fix HW_SW_CTRL pin naming to SMB_EN. Changed the structure of functional block diagram.....</td> <td style="text-align: right;">26</td> </tr> <tr> <td>• Updated functional block diagram Figure 8-2 to fix HW_SW_CTRL pin naming to SMB_EN.....</td> <td style="text-align: right;">26</td> </tr> <tr> <td>• Changed the structure of functional block diagram.....</td> <td style="text-align: right;">26</td> </tr> <tr> <td>• Added pin mode description in the Section 8.3.4.3 section.....</td> <td style="text-align: right;">30</td> </tr> <tr> <td>• Added layout examples for LMDB1102, LMKDB1202, LMKDB1104, LMKD1204 and LMKDB1108.....</td> <td style="text-align: right;">78</td> </tr> </tbody> </table>					Changes from Revision C (May 2024) to Revision D (June 2024)	Page	• Updated the number format for tables, figures, and cross-references throughout the document.....	1	• Updated R17 register 7:4 name to "AMP" to match GUI name for LMKDB1108:.....	1	• Updated R18 table to stack Reserved bits 5:4 in one row for LMKDB1108.....	1	• Exposed registers R5 to change REV_ID for LMKDB1108.....	1	• Exposed R91 and R92 to show output slew rate control for LMKDB1108.....	1	• Corrected register names that control correct outputs of chip (R0, R1, R2, R3, R20, R21) for LMKDB1204....	1	• Updated ^vSADR1_tri to ^vSADR0_tri for pin 4 in Figure 5-2	3	• Updated pin 7 name and description from "VDD" to "VDDA" in Figure 5-2	3	• Updated pin 10 name and description from "VDDA" to "VDD" in Figure 5-2	3	• Updated functional block diagram Figure 8-2 to fix HW_SW_CTRL pin naming to SMB_EN. Changed the structure of functional block diagram.....	26	• Updated functional block diagram Figure 8-2 to fix HW_SW_CTRL pin naming to SMB_EN.....	26	• Changed the structure of functional block diagram.....	26	• Added pin mode description in the Section 8.3.4.3 section.....	30	• Added layout examples for LMDB1102, LMKDB1202, LMKDB1104, LMKD1204 and LMKDB1108.....	78
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<p>The datasheet number will be changing.</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">Device Family</th> <th style="text-align: left;">Change From:</th> <th style="text-align: left;">Change To:</th> </tr> </thead> <tbody> <tr> <td>LMKDB1xxx</td> <td>SNAS855C</td> <td>SNAS855D</td> </tr> </tbody> </table> <p>These changes may be reviewed at the datasheet links provided. http://www.ti.com/product/LMKDB1102</p>					Device Family	Change From:	Change To:	LMKDB1xxx	SNAS855C	SNAS855D																								
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LMKDB1xxx	SNAS855C	SNAS855D																																
Reason for Change:																																		
To accurately reflect device characteristics.																																		
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):																																		
No anticipated impact. There are no changes to the actual device.																																		
Changes to product identification resulting from this PCN:																																		
None.																																		
Product Affected:																																		
PLMKDB1120NPPT	LMKDB1120Z85NPPT	LMKDB1120Z85NPPT	LMKDB1204REXT																															
PLMKDB1108RKPT	LMKDB1108Z85RKPR	LMKDB1108Z100RKPR	LMKDB1120Z100NPPT																															
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