



# 180409264 Alternate Sourcing BLED112-N

**PCN Issue Date:** 4/9/2018

**Effective Date:** 7/13/2018

**PCN Type:** Assembly

## Description of Change

Silicon Labs is pleased to announce the successful qualification of Ryder as an additional assembly, test and ship site for BLED112. Ryder is an existing module assembly supplier for Silicon Labs, certified and registered to ISO9001, ISO14001 and ISO/TS16949.

The new module assembly address for Ryder is:

Ryder Electronics (Xin Feng) Ltd.  
Lüyuan Ave (E), Industrial Park  
Xin Feng County  
Ganzhou City  
Jiang Xi Province 341600  
China

As a result from this change, the packing material will change from pouch to antistatic bag along with the size of the inner and outer shipping boxes.

## Reason for Change

The additional assembly/test/ship location will provide additional capacity for supply assurance

## Impact on Form, Fit, Function, Quality, Reliability

No change in fit, function, quality or reliability. The shipping media has changed from pouch to antistatic bag along with the size of the inner and outer shipping boxes. Please see additional details below.

## Product Identification

Existing Part #  
BLED112  
BLED112-X

Where X denotes custom part number.

**Last Date of Unchanged Product:** 7/13/2018

## Qualification Samples

Available upon request.

## Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to [PCNEarlyAcceptance@silabs.com](mailto:PCNEarlyAcceptance@silabs.com)

### **User Registration**

Register today to create your account on Silabs.com. Your personalized profile allows you to receive technical document updates, new product announcements, “how-to” and design documents, product change notices (PCN) and other valuable content available only to registered users. <http://www.silabs.com/profile>

### **Qualification Data**

Please see attached qualification report.

# BLED112 Qualification Report



## W7104F1 - Module Product Qualification Report Record Rev. A

BLED112 Ryder Alternate PCB source							
Test Name	Test Condition	Qualification	Lot ID or Start	Fail/Pass or End	Notes	Summary	Status
Test Group A – Accelerated Environment Stress Tests							
Temp Cycle	JA104	1 lot, N=>10	Q042505	0/15	1	2 lots	Pass
	Cond N: -40°C to 85°C 1500 cycles		Q042539	0/15			
Test Group C – Package Assembly Integrity Tests							
Mechanical Shock	JESD22-B110, Cond. B (1500g)	1 lot, N=>10	Q042512	0/10		1 lot 0/10	Pass
Mechanical Vibration	JESD22-B103, Cond. 1 (20-2000Hz)	1 lot, N=>10	Q042513	0/10		1 lot 0/10	Pass
Cleanliness Assessment	IPC-TM-650 2.3.28B	1 lot, N=>10	Q042514	0/10		1 lot 0/10	Pass

Notes:

- Parts are Pre-conditioned at MSL3/260°C

This report applies to the following part numbers:

BLED112

### Packing Material Changes

#### Before – Pouch Type



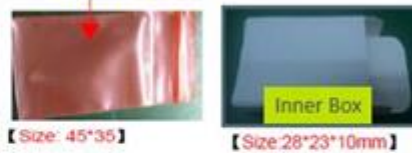
- Quantity/pouch = 500 units



- Inner Box Size: 352 x 330 x 75 mm
- Outer Box Size : 375 x 365 x 165 mm
- Number of pouch per inner box : 2
- Number of inner box per outer box: 2

#### After – Antistatic bag

Antistatic PE bag



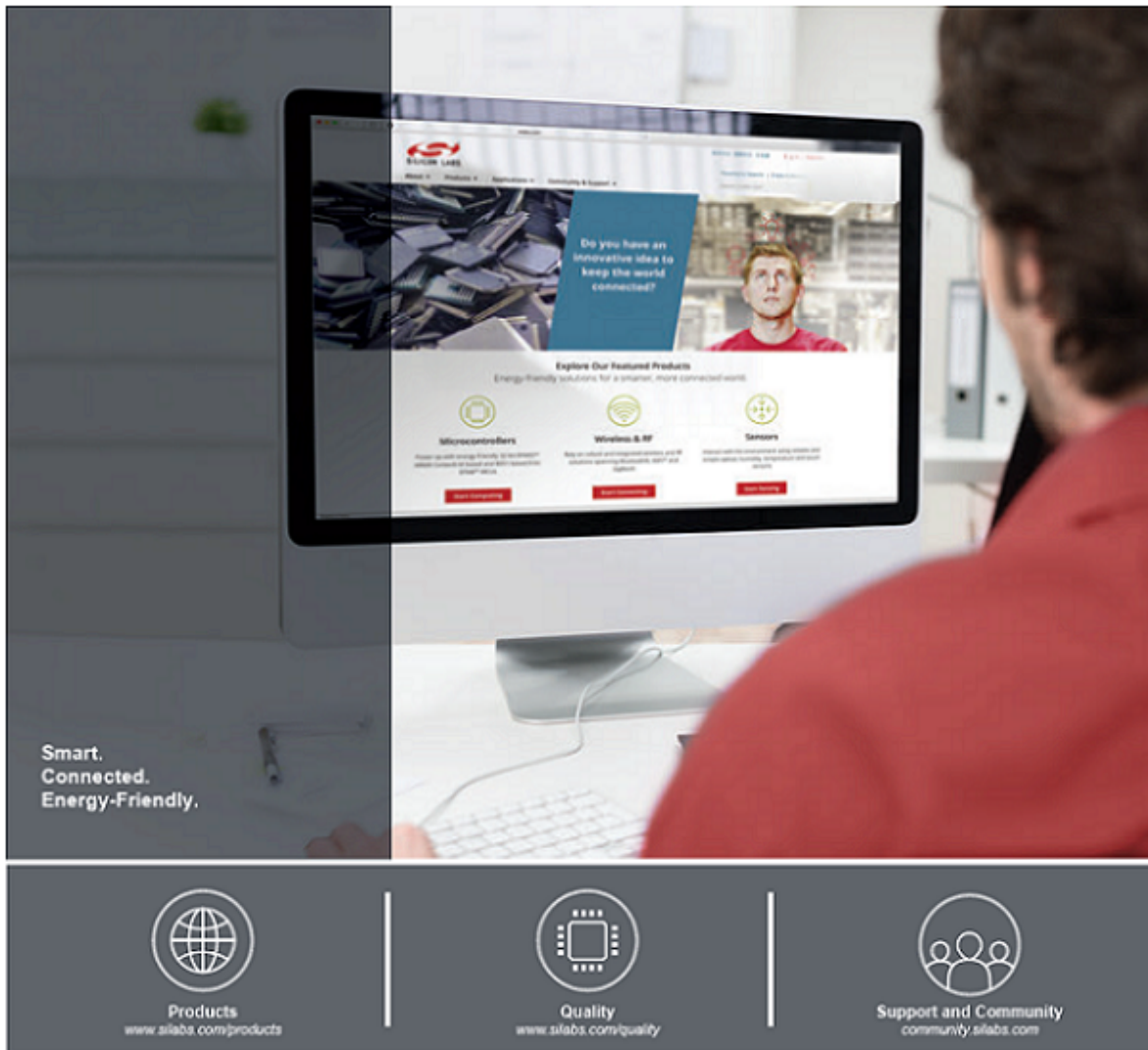
【Size: 45\*35】

【Size 28\*23\*10mm】

- Quantity/outer box = 500 units



- Inner Box Size: 28 X 23 X 10 mm
- Outer Box Size: 295 x 225 x 175 mm
- Number of bag per inner box: 1
- Number of inner box per outer box: 500



#### Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications. Application examples described herein are for illustrative purposes only. Silicon Labs reserves the right to make changes without further notice and limitation to product information, specifications, and descriptions herein, and does not give warranties as to the accuracy or completeness of the included information. Silicon Labs shall have no liability for the consequences of use of the information supplied herein. This document does not imply or express copyright licenses granted hereunder to design or fabricate any integrated circuits. The products are not designed or authorized to be used within any Life Support System without the specific written consent of Silicon Labs. A "Life Support System" is any product or system intended to support or sustain life and/or health, which, if it fails, can be reasonably expected to result in significant personal injury or death. Silicon Labs products are not designed or authorized for military applications. Silicon Labs products shall under no circumstances be used in weapons of mass destruction including (but not limited to) nuclear, biological or chemical weapons, or missiles capable of delivering such weapons.

#### Trademark Information

Silicon Laboratories Inc.®, Silicon Laboratories®, Silicon Labs®, SiLabs® and the Silicon Labs logo®, Bluegiga®, Bluegiga Logo®, Clockbuilder®, CMEMS®, DSPLL®, EFM®, EFM32®, EFR, Ember®, Energy Micro, Energy Micro logo and combinations thereof, "the world's most energy friendly microcontrollers", Ember®, EZLink®, EZRadio®, EZRadioPRO®, Gecko®, ISOModem®, Micrium, Precision32®, ProSLIC®, Simplicity Studio®, SiPHY®, Telegesis, the Telegesis Logo®, USBXpress®, Zentri and others are trademarks or registered trademarks of Silicon Labs. ARM, CORTEX, Cortex-M3 and THUMB are trademarks or registered trademarks of ARM Holdings. Keil is a registered trademark of ARM Limited. All other products or brand names mentioned herein are trademarks of their respective holders.



**Silicon Laboratories Inc.**  
**400 West Cesar Chavez**  
**Austin, TX 78701**

<http://www.silabs.com>