


PRODUCT / PROCESS CHANGE INFORMATION

1. PCI basic data

| | |
|----------------------|------------------------------------------------------------------------------------------------------------------------|
| 1.1 Company |  STMicroelectronics International N.V |
| 1.2 PCI No. | MDG/23/14333 |
| 1.3 Title of PCI | STM32H56x/57x and STM32H503 change of the memory configuration bits |
| 1.4 Product Category | STM32H503x, STM32H562x, STM32H563x, STM32H573x |
| 1.5 Issue date | 2023-12-06 |

2. PCI Team

| | |
|---------------------------|----------------------------|
| 2.1 Contact supplier | |
| 2.1.1 Name | ROBERTSON HEATHER |
| 2.1.2 Phone | +1 8475853058 |
| 2.1.3 Email | heather.robertson@st.com |
| 2.2 Change responsibility | |
| 2.2.1 Product Manager | Ricardo Antonio DE SA EARP |
| 2.1.2 Marketing Manager | Veronique BARLATIER |
| 2.1.3 Quality Manager | Pascal NARCHE |

3. Change

| | | |
|--------------------------|--------------------------------------------------------------------------------------------------------------------------------|----------------------------|
| 3.1 Category | 3.2 Type of change | 3.3 Manufacturing Location |
| General Product & Design | Firmware modification : firmware is programed in the memory without effect of electrical performance at the customer (bug fix) | ST Crolles 300 (France) |

4. Description of change

| | | |
|---------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | Old | New |
| 4.1 Description | memory configuration bits wrong settings | Change in the memory configuration bits settings: - Addressable memory size is configured in some engineering registers not available in user mode. - Settings of those configuration bits were updated at production site to clearly differentiate 1M from 2M devices to conform to the device specification. - We expect very low impact at user side as information about those registers are not communicated in user documentations. - However in case the software application extends the CRC on those registers it will trigger a change. |
| 4.2 Anticipated Impact on form,fit, function, quality, reliability or processability? | No change on product May impact customer in case CRC is done (it might impact customer production flow) | |

5. Reason / motivation for change

| | |
|----------------------|---------------------------------------------------------------------------------------------------|
| 5.1 Motivation | Those bits are non user mode bits - but in case of CRC application software may trigger a change. |
| 5.2 Customer Benefit | SERVICE IMPROVEMENT |

6. Marking of parts / traceability of change

| | |
|-----------------|-------------------------------------------------------------|
| 6.1 Description | Traceability of the change is ensured by ST internal tools. |
|-----------------|-------------------------------------------------------------|

7. Timing / schedule

| | |
|-----------------------------------|------------|
| 7.1 Date of qualification results | 2024-01-03 |
| 7.2 Intended start of delivery | 2024-01-10 |

| | |
|-------------------------------------|----------------|
| 7.3 Qualification sample available? | Not Applicable |
|-------------------------------------|----------------|

| 8. Qualification / Validation | | | |
|----------------------------------------------------|-------------|------------|--|
| 8.1 Description | | | |
| 8.2 Qualification report and qualification results | In progress | Issue Date | |

| 9. Attachments (additional documentations) |
|--------------------------------------------|
| 14333 Public product.pdf |

| 10. Affected parts | | |
|-------------------------|-------------------------|--------------------------|
| 10. 1 Current | | 10.2 New (if applicable) |
| 10.1.1 Customer Part No | 10.1.2 Supplier Part No | 10.1.2 Supplier Part No |
| | STM32H503CBT6 | |
| | STM32H503CBU6 | |
| | STM32H503EBY6TR | |
| | STM32H503KBU6 | |
| | STM32H503RBT6 | |
| | STM32H562RGT6 | |
| | STM32H562ZGT6 | |
| | STM32H563IHK6 | |
| | STM32H563RGV6 | |
| | STM32H563RIT6 | |
| | STM32H563VIT6 | |
| | STM32H563ZGT6 | |
| | STM32H563ZIT6 | |
| | STM32H573AII3Q | |
| | STM32H573AII6 | |
| | STM32H573IHK3Q | |
| | STM32H573IHK6 | |
| | STM32H573IIT3Q | |
| | STM32H573IIT6 | |
| | STM32H573MIY3QTR | |
| | STM32H573RIT6 | |
| | STM32H573RIV6 | |
| | STM32H573VIT3Q | |
| | STM32H573VIT6 | |
| | STM32H573ZIT3Q | |
| | STM32H573ZIT6 | |

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Public Products List

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PCI Title : STM32H56x/57x and STM32H503 change of the memory configuration bits

PCI Reference : MDG/23/14333

Subject : Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change.

| | | |
|------------------|------------------|------------------|
| STM32H573VIT3Q | STM32H563AI16 | STM32H573I1K3Q |
| STM32H573VIT6 | STM32H563ZIT6 | STM32H573AI13Q |
| STM32H563ZIT3Q | STM32H573ZIT6 | STM32H563VIT3Q |
| STM32H562ZGT6 | STM32H562I1K6 | STM32H563ZGT6 |
| STM32H562RGV6 | STM32H563I1K3Q | STM32H573MIY3QTR |
| STM32H573I1K6 | STM32H503RBT6 | STM32H563AI13Q |
| STM32H562IGK6 | STM32H562VGT6TR | STM32H562VGT6 |
| STM32H563RIV6 | STM32H562RIV6 | STM32H563AGI6 |
| STM32H573I1K3QTR | STM32H563RIT6 | STM32H562VIT6 |
| STM32H573ZIT3Q | STM32H562RGT6TR | STM32H573I1K6TR |
| STM32H573AI16 | STM32H563IIT3Q | STM32H573VIT6TR |
| STM32H503CBT6 | STM32H563VIT6 | STM32H573RIV6 |
| STM32H573RIT6 | STM32H562IGT6 | STM32H562ZIT6 |
| STM32H563I1K6 | STM32H563VGT6 | STM32H573IIT3Q |
| STM32H503RBT7 | STM32H503CBU6 | STM32H562RGT6 |
| STM32H562RIT6 | STM32H503KBU6 | STM32H563RGV6 |
| STM32H573IIT6 | STM32H563IIT6 | STM32H563ZIT3QTR |
| STM32H563IGT6 | STM32H503EBY6TR | STM32H562AI16 |
| STM32H563IGK6 | STM32H563MIY3QTR | STM32H562IIT6 |
| STM32H563RGT6 | | |

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