


PRODUCT / PROCESS CHANGE NOTIFICATION

1. PCN basic data

1.1 Company		STMicroelectronics International N.V
1.2 PCN No.	AMS/20/12524	
1.3 Title of PCN	VIPER31: Final Test Limits Change	
1.4 Product Category	See Products List (Power Conversion)	
1.5 Issue date	2020-12-21	

2. PCN Team

2.1 Contact supplier	
2.1.1 Name	ROBERTSON HEATHER
2.1.2 Phone	+1 8475853058
2.1.3 Email	heather.robertson@st.com
2.2 Change responsibility	
2.2.1 Product Manager	Domenico ARRIGO
2.1.2 Marketing Manager	Fulvio PULICELLI
2.1.3 Quality Manager	Alessandro PLATINI

3. Change

3.1 Category	3.2 Type of change	3.3 Manufacturing Location
General Product & Design	Modification of datasheet :parameters/electrical specification (min./max./typ. values) and/or AC/DC specification	FE/Catania, Italy (Controller die) FE/AMK, Singapore (Mosfet die) BE/Assy & FT: UTAC (Thai)

4. Description of change

	Old	New
4.1 Description	Limits are +/- 25%---- DATASHEET Rev. 2	Limits increased +/- 37.5% ---- DATASHEET Rev. 3
4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?	No IMPACT	

5. Reason / motivation for change

5.1 Motivation	The change of Final Test Limits improves the yield improvement at Final test.
5.2 Customer Benefit	YIELD IMPROVEMENT

6. Marking of parts / traceability of change

6.1 Description	DATASHEET Rev 3.
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7. Timing / schedule

7.1 Date of qualification results	2020-10-31
7.2 Intended start of delivery	2021-03-17
7.3 Qualification sample available?	Upon Request

8. Qualification / Validation

8.1 Description	12524 PCN_VIPer31_TSS_limits_change_application_report_.pdf		
8.2 Qualification report and qualification results	Available (see attachment)	Issue Date	2020-12-21

9. Attachments (additional documentations)		
12524 Public product.pdf 12524 PCN_ViPer31_TSS_limits_change_application_report_.pdf		

10. Affected parts		
10. 1 Current		10.2 New (if applicable)
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	VIPER318HDTR	
	VIPER318LDTR	
	VIPER319XDTR	

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life.augmented

VIPer31

evaluation on datasheet limits change

CP: VIPer318HDTR, VIPER318LDTR, VIPER319XDTR

I&PC Appl. Lab

Oct-2020

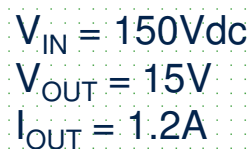
Purpose of evaluation

Parameter	VIPer31			
	old range		new range	
	Min	Max	Min	Max
t_{SS} [ms]	6	10	5	11
t_{OVP_DEB} [us]	188	312	156	344
t_{UVP_DEB} [ms]	22.5	37.5	18.75	41.25
$t_{UVP_RESTART}$ [ms]	22.5	37.5	18.75	41.25
$t_{RESTART}$ [s]	0.75	1.25	0.625	1.375
$t_{OVP_RESTART}$ [s]	0.375	0.625	0.312	0.688

In order to improve the yield, the range of the above listed parameters needs to be enlarged in VIPer31 (from «old range» to «new range»).

Purpose of this evaluation is to find out if this change may cause issues on the VIPer31 and/or on the system, both in terms of output voltage regulation/stability and of reliability (thermal dissipation)

15 JULY 2005

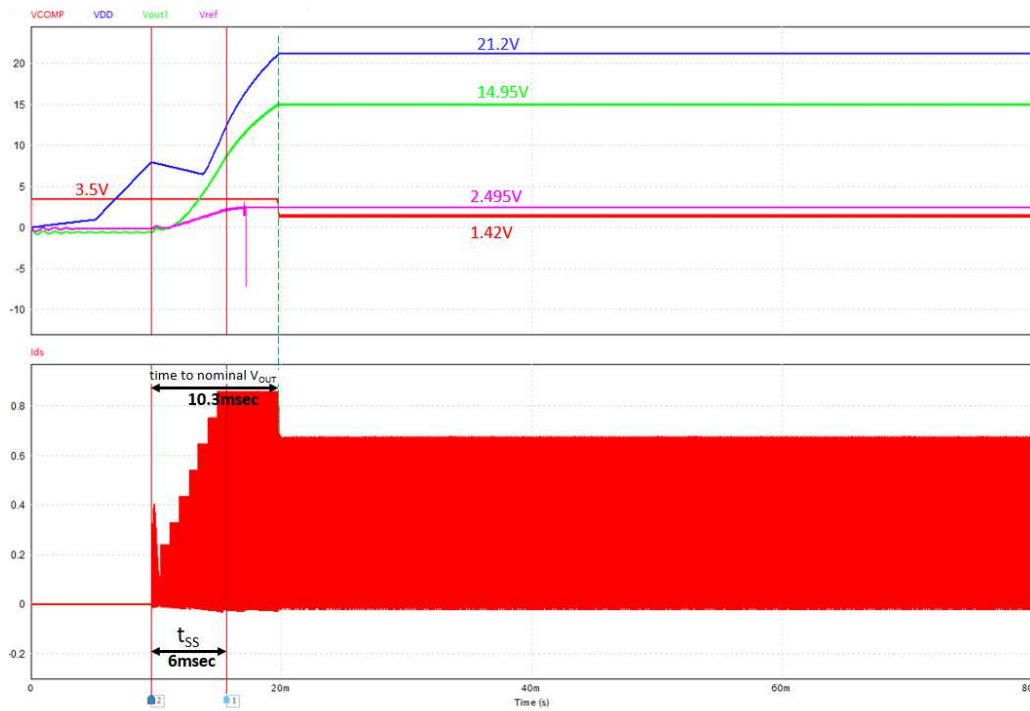


In the following slides, the converter startup with different t_{SS} values is simulated, in order to check if increasing the tolerance of this parameter from the old range to new range can affect the output voltage ramp-up or the system stability

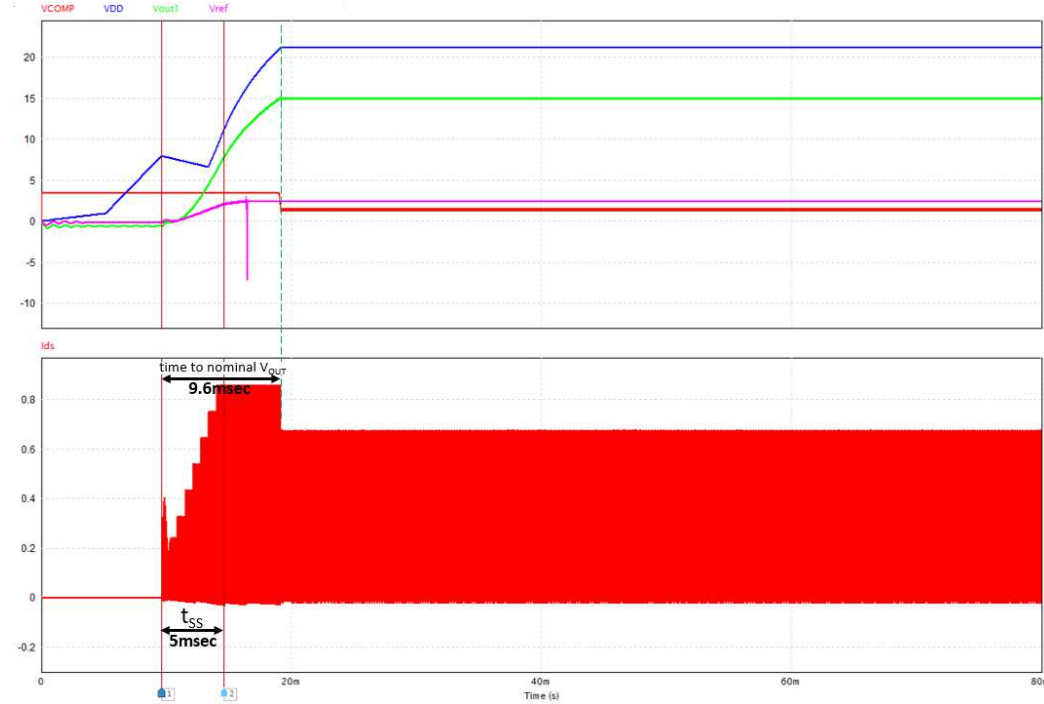
t _{ss}			
old range		new range	
min	max	min	max
6ms	10ms	5ms	11ms

Evaluation of t_{SS}

$t_{SS} = 6\text{msec}$



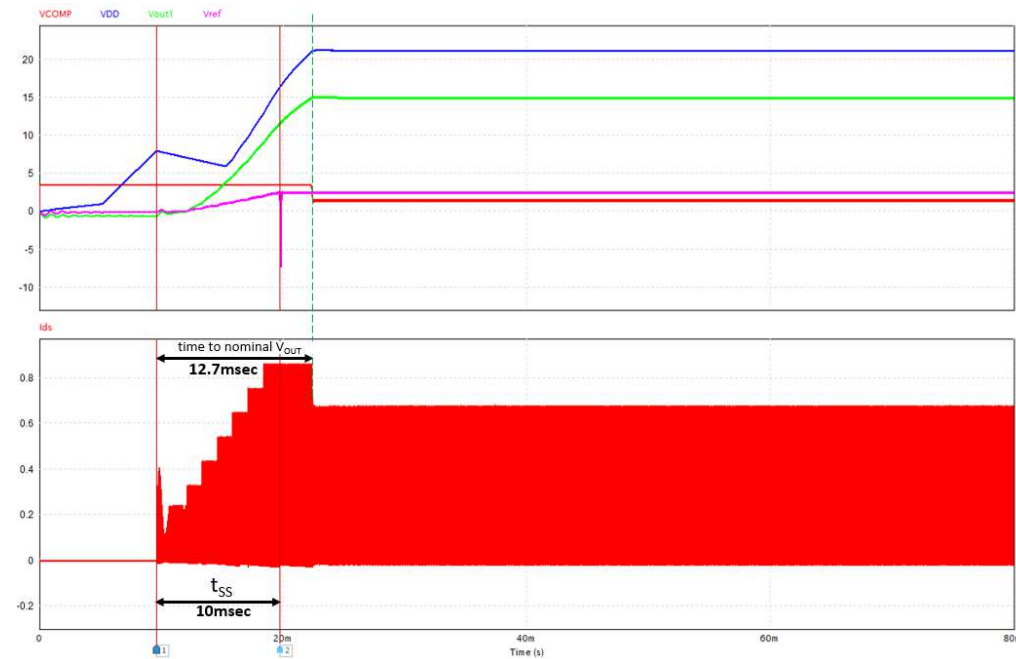
$t_{SS} = 5\text{msec}$



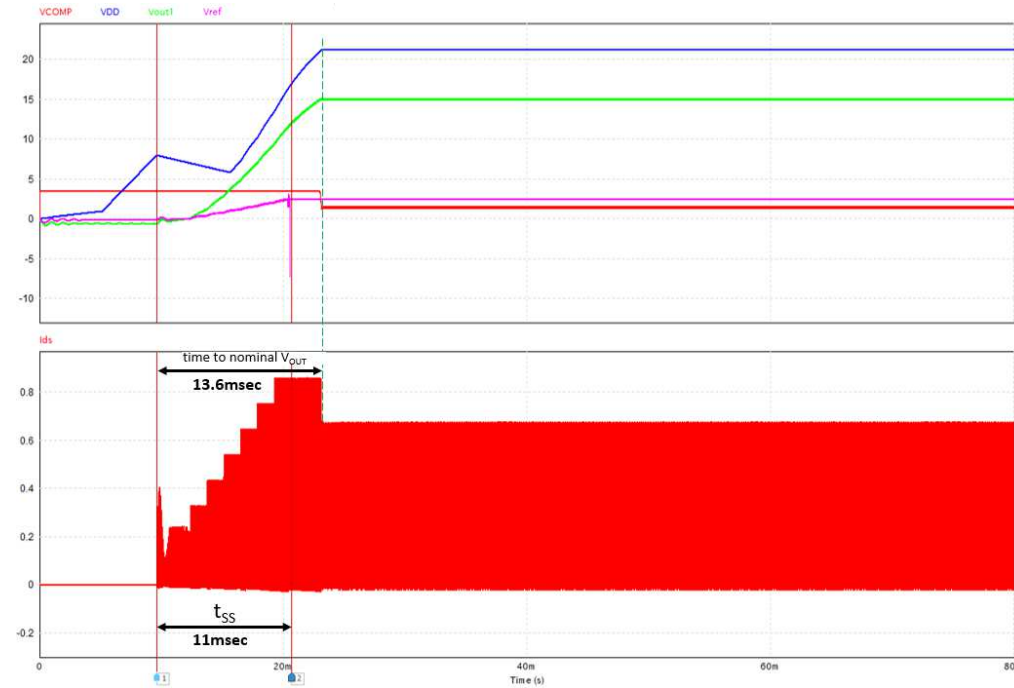
Result: if t_{SSmin} of «new range» is assumed in place of t_{SSmin} of «old range», the change we can expect in startup behavior is negligible

Evaluation of t_{SS}

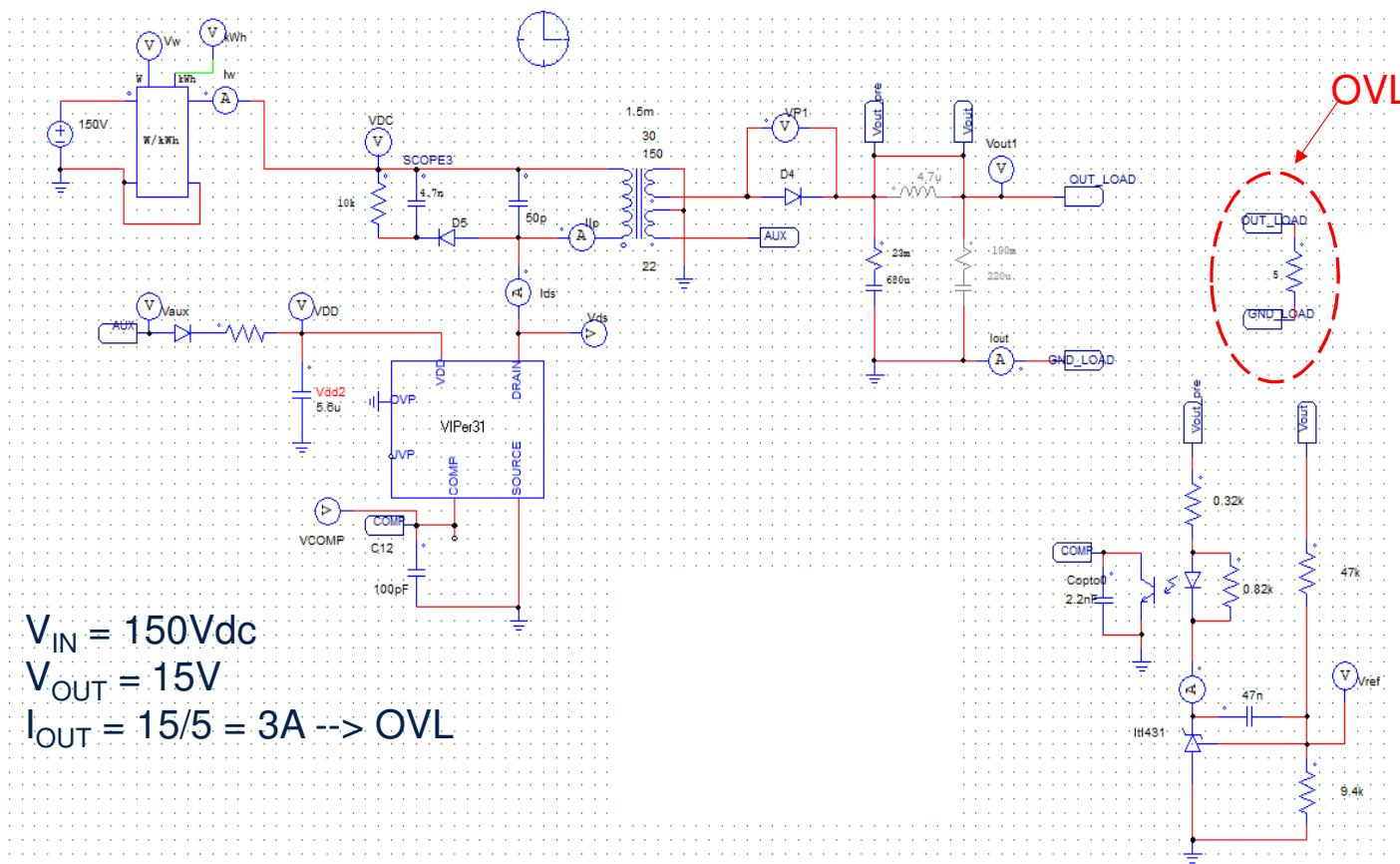
$t_{SS} = 10\text{msec}$



$t_{SS} = 11\text{msec}$



Result: if t_{SSmax} of «new range» is assumed in place of t_{SSmax} of «old range», the change we can expect in startup behavior is negligible



$V_{IN} = 150V_{dc}$
 $V_{OUT} = 15V$
 $I_{OUT} = 15/5 = 3A \rightarrow OVL$

Evaluation of $t_{RESTART}$

$t_{RESTART}$			
old range		new range	
min	max	min	max
0.75s	1.25s	0.625s	1.375s

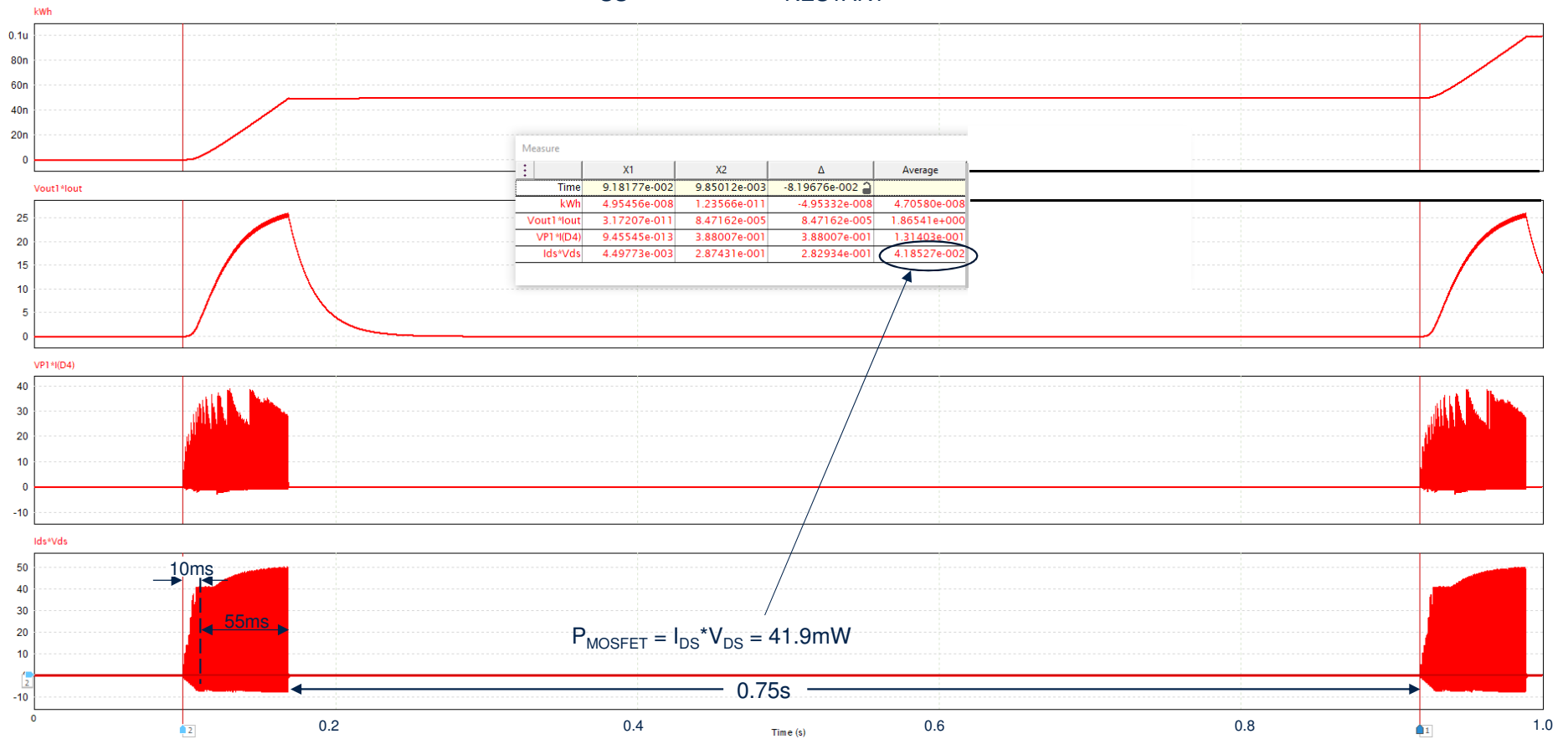
Output load has been set to 3A, which is beyond the maximum manageable by VIPer31, thus triggering overload protection.



In the following slides, the power dissipation across VIPer31 with t_{SS_max} , t_{OVL_max} and $t_{RESTART_min}$ (worst-case combination) is simulated, to estimate the max junction temperature increase which can be expected when «new range» (11ms, 55ms, 625ms) is assumed in place of «old range» (10ms, 55ms, 750ms)

Old range, worst case
 $t_{SS} = 10\text{ms}$; $t_{\text{RESTART}} = 0.75\text{s}$

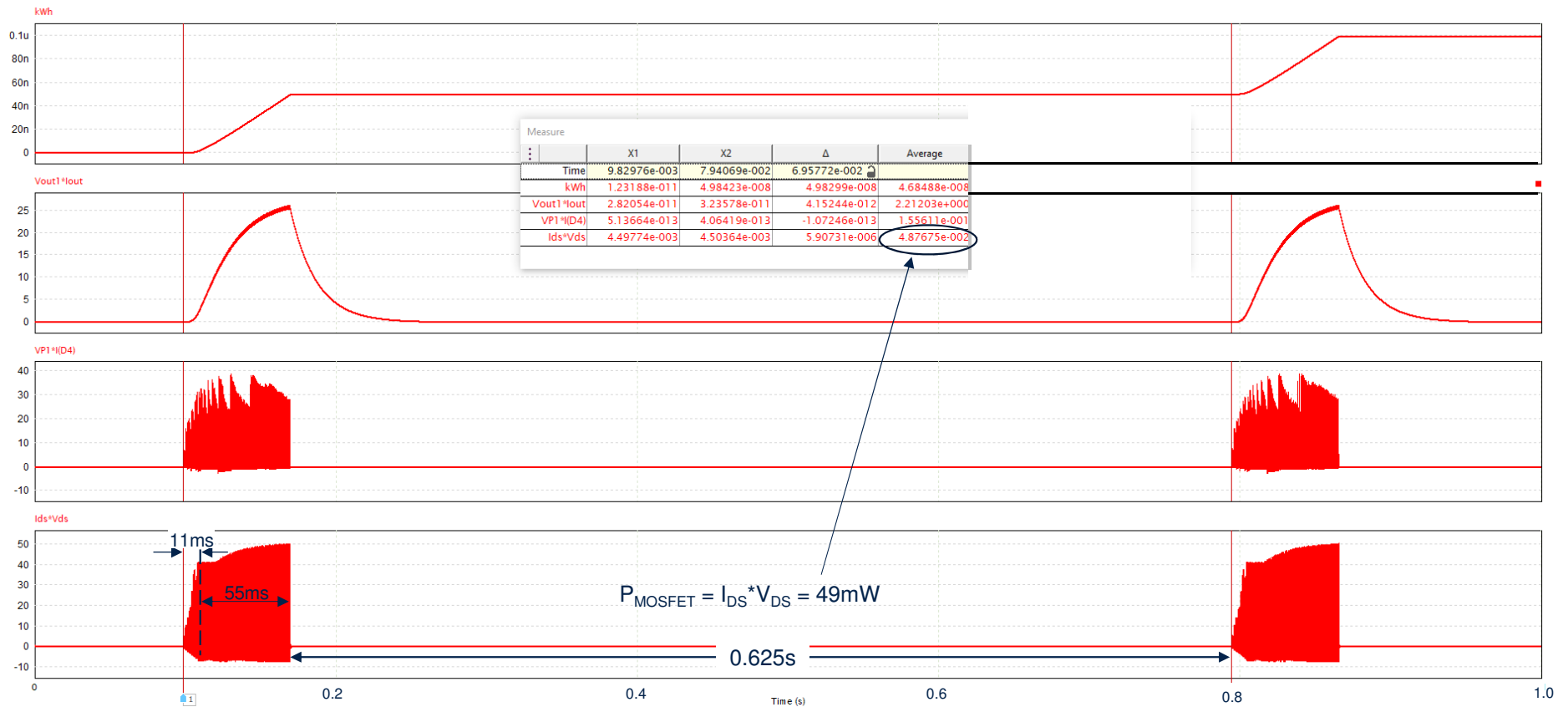
Evaluation of t_{RESTART}



Let us assume $R_{\text{th}} = 80^{\circ}\text{C/W}$ \longrightarrow $\Delta T_j = R_{\text{th}} \cdot P_{\text{MOSFET}} = 80 \cdot 0.0419 = 3.35^{\circ}\text{C}$

New range, worst case
 $t_{SS} = 11\text{ms}$; $t_{\text{RESTART}} = 0.625\text{s}$

Evaluation of t_{RESTART}



$$\Delta T_j = R_{\text{th}} \cdot P_{\text{MOSFET}} = 80 \cdot 0.0488 = 3.90^{\circ}\text{C}$$

Result: if «new range» is assumed in place of «old range», the max ΔT_j increase we can expect is negligible

Evaluation of $t_{\text{OVP_DEB}}$ & $t_{\text{OVP_RESTART}}$

old range				new range			
$t_{\text{OVP_DEB}}$		$t_{\text{OVP_RESTART}}$		$t_{\text{OVP_DEB}}$		$t_{\text{OVP_RESTART}}$	
min	max	min	max	min	max	min	max
188us	312us	0.375s	0.625s	156us	344us	0.312s	0.688s

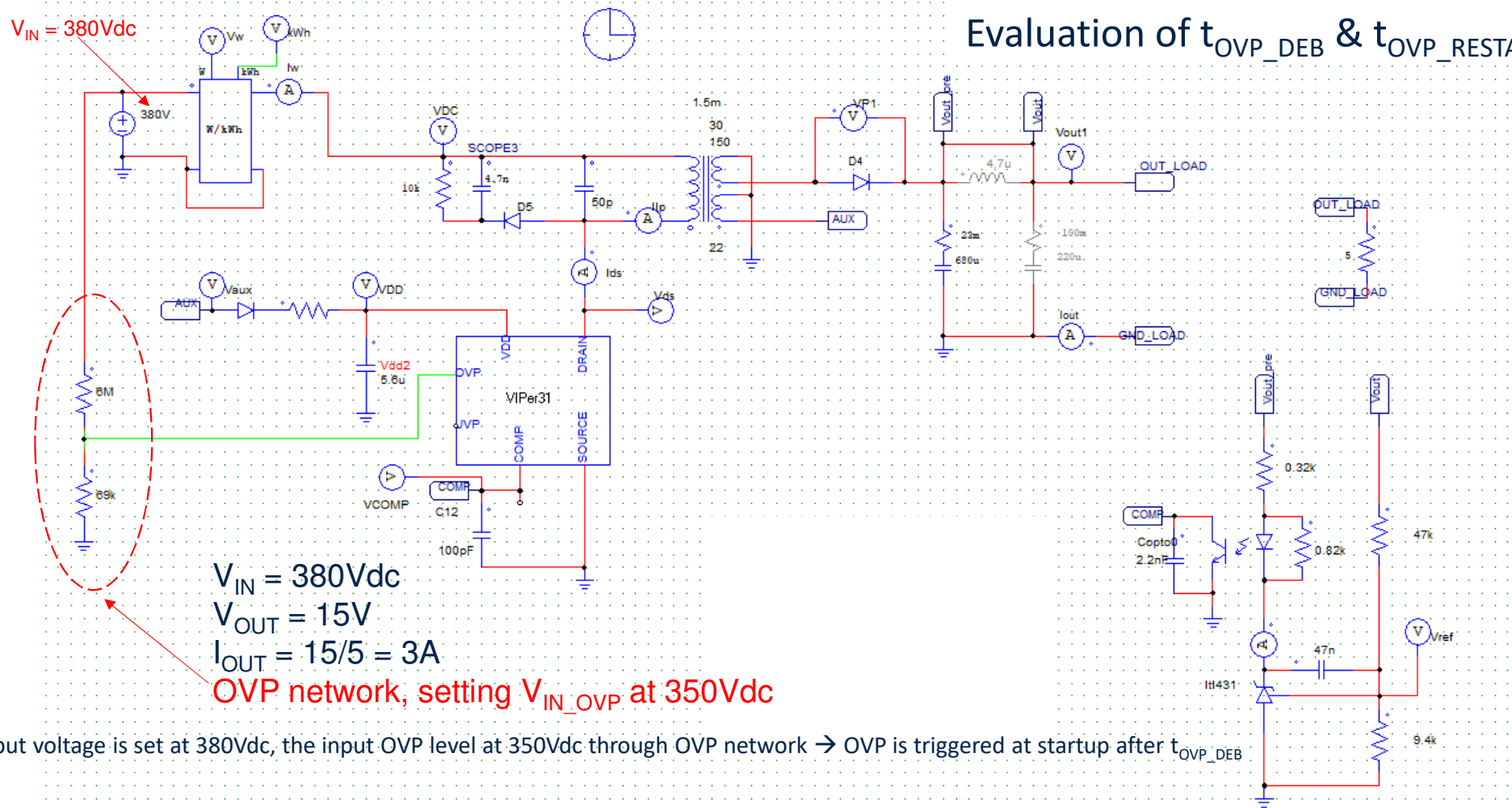


Worst cases

From thermal point of view, worst case is:

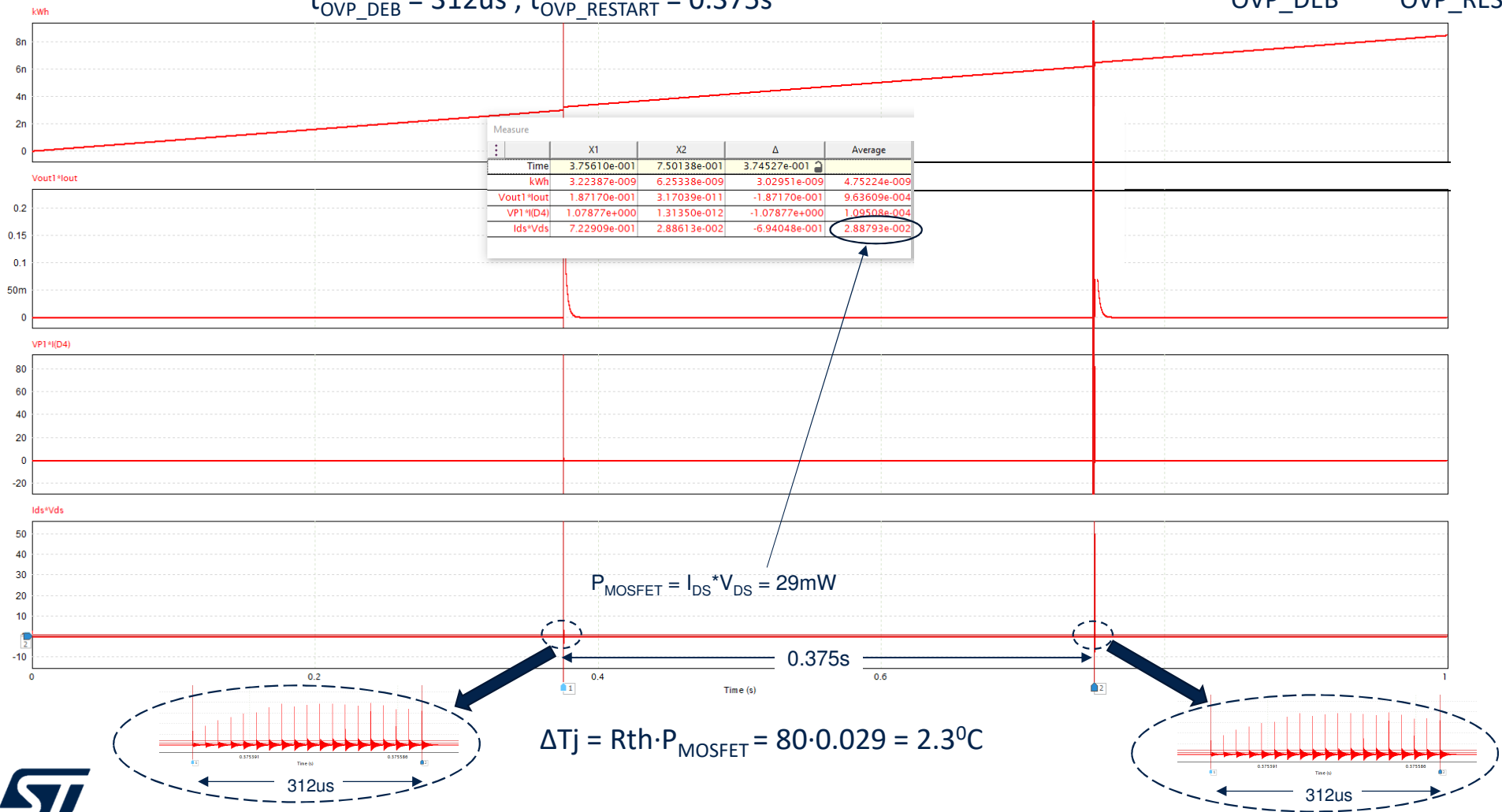
$$\begin{aligned} t_{\text{OVP_DEB}} &= \text{max} \\ t_{\text{OVP_RESTART}} &= \text{min} \end{aligned}$$

Evaluation of t_{OVP_DEB} & $t_{OVP_RESTART}$



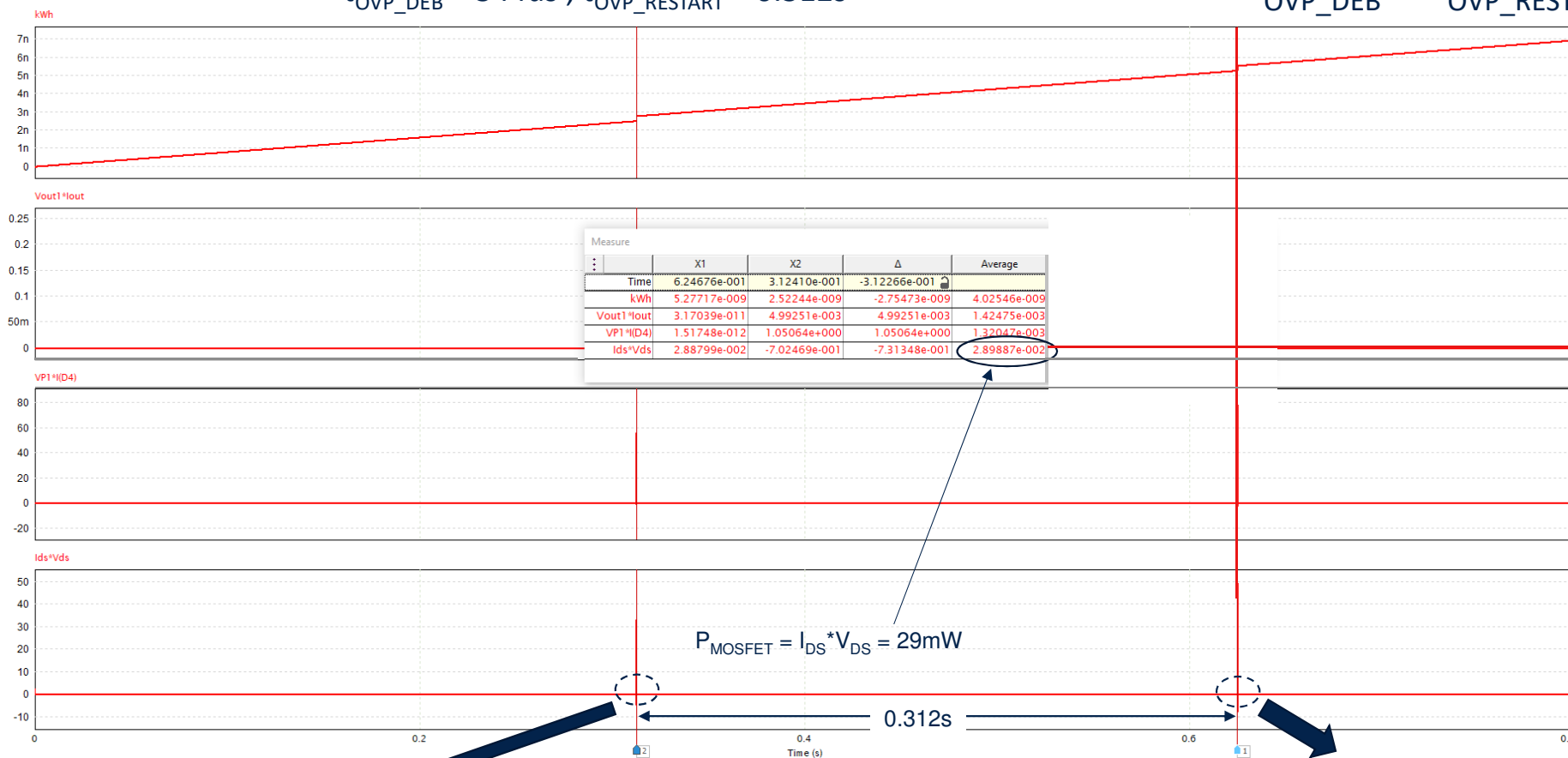
Old range, worst case
 $t_{\text{OVP_DEB}} = 312\mu\text{s}$; $t_{\text{OVP_RESTART}} = 0.375\text{s}$

Evaluation of $t_{\text{OVP_DEB}}$ & $t_{\text{OVP_RESTART}}$



New range, worst case
 $t_{OVP_DEB} = 344\mu s$; $t_{OVP_RESTART} = 0.312s$

Evaluation of t_{OVP_DEB} & $t_{OVP_RESTART}$



$\Delta T_j = R_{th} \cdot P_{MOSFET} = 80 \cdot 0.029 = 2.3^{\circ}C$
Result: if «new range» is assumed in place of «old range», the max ΔT_j increase we can expect is negligible

Evaluation of $t_{\text{UVP_DEB}}$ & $t_{\text{UVP_RESTART}}$

old range			
$t_{\text{UVP_DEB}}$		$t_{\text{UVP_RESTART}}$	
min	max	min	max
22.5ms	37.5ms	22.5ms	37.5ms

new range			
$t_{\text{UVP_DEB}}$		$t_{\text{UVP_RESTART}}$	
min	max	min	max
18.75ms	41.25ms	18.75ms	41.25ms

$t_{\text{UVP_DEB}}$ is the time interval the UVP pin voltage must stay below the internal threshold $V_{\text{UVP_th}}$ for UVP triggering. Once UVP is entered, VIPer31 switching is inhibited → no functional/reliability issue can come from limits range enlargement.

$t_{\text{UVP_REST}}$ is the time interval the UVP pin voltage must stay above $V_{\text{UVP_th}}$ for exiting UVP and resume switching. During $t_{\text{UVP_REST}}$, VIPer31 is not switching → no functional/reliability issue can come from limits range enlargement.

Conclusions

		VIPer31				
Parameter	Unit of measure	Old range		New range		Application impact of the change
		Min	Max	Min	Max	
t_{SS}	ms	6	10	5	11	Negligible
t_{OVP_DEB}	us	188	312	156	344	Negligible
t_{UVP_DEB}	ms	22.5	37.5	18.75	41.25	Negligible
$t_{UVP_RESTART}$	ms	22.5	37.5	18.75	41.25	Negligible
$t_{RESTART}$	s	0.75	1.25	0.625	1.375	Negligible
$t_{OVP_RESTART}$	s	0.375	0.625	0.312	0.688	Negligible



Public Products List

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PCN Title : VIPER31: Final Test Limits Change

PCN Reference : AMS/20/12524

Subject : Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change.

VIPER319XDTR	VIPER318LDTR	VIPER318HDTR
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