



# PRODUCT/PROCESS CHANGE NOTIFICATION

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PCN IPG-IPC/14/8676

Dated 04 Sep 2014

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**TD310ID: New glue and MSL3 (Moisture Sensitive Level) introduction**

**Table 1. Change Implementation Schedule**

Forecasted implementation date for change	28-Aug-2014
Forecasted availability date of samples for customer	28-Aug-2014
Forecasted date for <b>STMicroelectronics</b> change Qualification Plan results availability	28-Aug-2014
Estimated date of changed product first shipment	04-Dec-2014

**Table 2. Change Identification**

Product Identification (Product Family/Commercial Product)	see attached list
Type of change	Package assembly material change, Packing
Reason for change	to harmonize Bill of Material as used on SO16 package
Description of the change	Following our plan to harmonize Bill of Material, we are now introducing for this specific product, a new glue already in use on same package by other different products. Moreover, we are also introducing the process and the packing relative to the JEDEC MSL3
Change Product Identification	Dedicated FG's
Manufacturing Location(s)	

## DOCUMENT APPROVAL

Name	Function
Chelli, Fabio	Marketing Manager
Arrigo, Domenico Massimo	Product Manager
Barbieri, Danilo	Product Manager
Moretti, Paolo	Q.A. Manager



**RELIABILITY EVALUATION**  
**MSL\_ (MOISTURE SENSITIVITY LEVEL)**  
**ASSESSMENT**  
**IPC-JEDEC J-STD-020D**  
**CHANGE FROM MSL\_1 TO MSL\_3**  
**\* D310SA2-TD310ID-2LF/**  
**SOIC16L SHD ST-SHENZHEN (CHINA)**

**DOCUMENT INFORMATION**

Version	Date	Pages	Prepared by	Approved by	Comment
1.0	25-JUN-2014	27	F.VENTURA I&PC QA&R / B/E	A.PLATINI I&PC QA&R MING.	

Note: This report is a summary of the reliability trials performed in good faith by STMicroelectronics in order to evaluate the potential reliability risks during the product life using a set of defined test methods.  
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General Information	
Product Line	KKQ7*D310SA2
P/N	<b>TD310ID-2LF</b>
Product Group	IPG
Product division	Industrial & Power Group
Package	SOIC 16L
Silicon Process	CMOS
technology	DM-CMOS
Maturity level step	29

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Document reference	Short description
<b>AEC-Q100</b>	Stress test qualification for automotive grade integrated circuits
<b>JESD47</b>	Stress-Test-Driven Qualification of Integrated Circuits
<b>ADCS:8161393</b>	General specification for product development

## **1 GLOSSARY**

<b>DUT</b>	Device Under Test
<b>SS</b>	Sample Size



## **2 RELIABILITY EVALUATION OVERVIEW**

### **2.1 Objectives**

MSL\_ (MOISTURE SENSITIVITY LEVEL) ASSESSMENT IPC-JEDEC J-STD-020D

**CHANGE FROM MSL\_1 TO MSL\_3**

### **2.2 Conclusion**

Qualification Plan requirements (WORKABILITY/ TESTING ) have been fulfilled without exception. It is stressed that reliability tests have shown that the devices behave correctly against environmental tests (no failure). Moreover, the stability of electrical parameters during the accelerated tests demonstrates the ruggedness of the products and safe operation, which is consequently expected during their lifetime.



## **2.3 Construction note**

<b>*D310_ P/N: TD310ID-2LF</b>	
<b>Wafer/Die fab. information</b>	<i>AMKF-AMJ9 5"</i>
Wafer fab manufacturing location	<i>ANG MO KIO S'PORE</i>
Technology	CMOS
Process family	DM-CMOS
Die finishing back side	RAW SILICON
Die size	4430 x 1980 mm
Bond pad metallization layers	Al/Si
Passivation type	P-VAPOX (SiO2) NITRIDE (SiN)
<b>Wafer Testing (EWS) information</b>	<i>AMJ9 5"</i>
Electrical testing manufacturing location	STS
<b>Assembly information</b>	
Assembly site	ST-SHENZHEN (CHINA)
Package description	SOIC16L SHDLF .15
Molding compound	ECOPAK 2 COMPLIANCE
Frame material	SHDLF 16L Ni/Thin/Pd/Ag/Au OPT.B
Die attach process	EPOXY
Die attach material	GLUE ABLEBOND 8601S-25
Die pad size	94 X 200MILS
Wire bonding process	THERMOSONIC
Wires bonding materials/diameters	1mils Cu
Lead finishing process	Pre- plated
Package code	Q7
<b>Final testing information</b>	
Testing location	ST-SHENZHEN (CHINA)



### **3 TESTS RESULTS SUMMARY**

#### **3.1 Test vehicle \*U324**

Lot #	Diffusion Lot	Assy Lot	Trace Code	Process/ Package	Product Line	Comments
1	VW3108L6	GK4170H6RR	GK4170H6	SOIC 16L SHDL	KKQ7*D310SA2	

Detailed results in below chapter will refer to P/N and Lot #.

#### **3.2 Test plan and results summary**

**P/N : TD310ID-2LF**

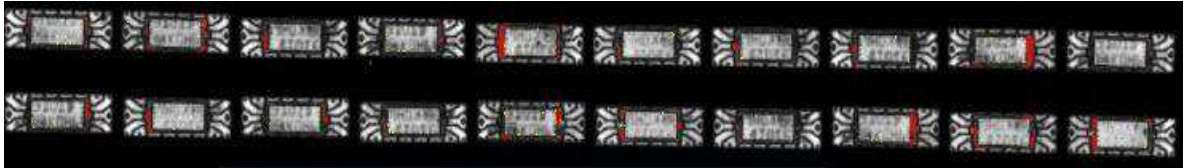
Test	PC	Std ref.	Conditions	Steps	Note
PC	Y	JESD22 A020-D	MSL_3 (192H 30°C/60%H.R)	0/200	NO DELAMINATION TOP/BOTTOM
TC	Y	JESD22 A-104	Ta = -65°C to 150°C	100Cy	0/80
					NO DELAMINATION AFTER TC.





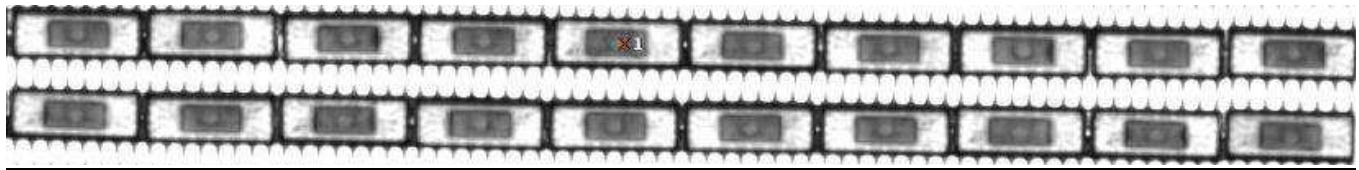
### 3.4.1 SAM AFTER MSL3(NO DELAMINATION FOUND)

#### C-SCAN



4

#### T-scan



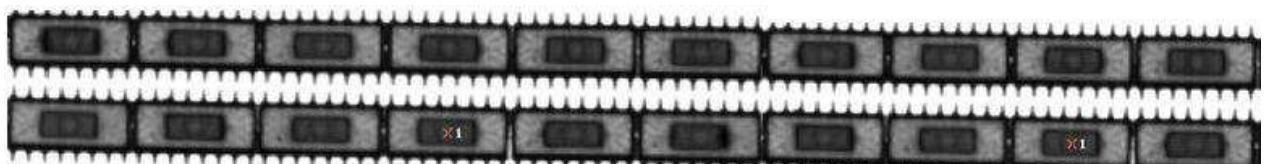
5

### **SAM after MSL3+100TC(No delamination found)**

#### C-SCAN



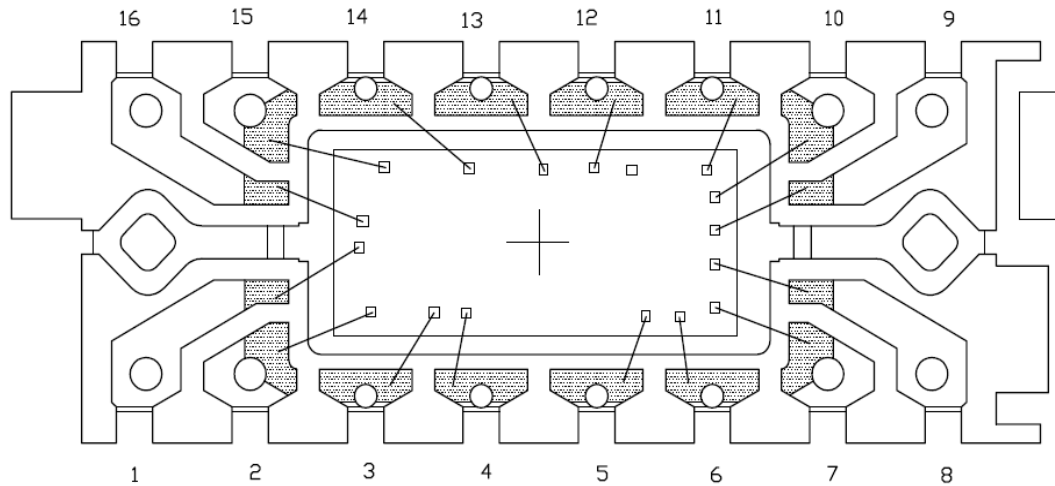
#### T-scan



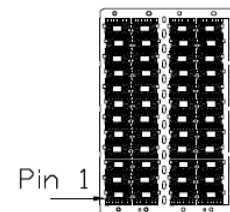
### 3.4.1 ANNEX 2 : MOUNT & BOND DIAGRAM:

## MBD FOR Line:D310 (S016L SHENZHEN)

FRAME PAD :  $\frac{94 \times 200 \text{ mls}}{2,387 \times 5080 \text{ mm}}$



Scale:  $\frac{1 \text{ mm}}{\text{---}}$



### 3.5.0 ANNEX 3 : POA PACKAGE OUTLINE ASSEMBLY



## PACKAGE OUTLINE ASSEMBLY

**TITLE: PLASTIC SMALL OUTLINE PACKAGE 16L NARROW**

**PACKAGE CODE: Q7**

**JEDEC/EIAJ REFERENCE NUMBER: JEDEC MS-012-AC**

DIMENSIONS							
DATABOOK (mm)				DRAWING (mm)			NOTES
REF.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
A			1.75	1.43	1.55	1.68	
A1	0.10		0.25	0.12	0.15	0.18	
A2	1.25			1.48	1.52	1.56	
b	0.31		0.51	0.375	0.40	0.425	
c	0.17		0.25			0.238	
D	9.80	9.90	10.00	9.82	9.85	9.88	(1) (3)
E	5.80	6.00	6.20	5.90	6.00	6.10	
E1	3.80	3.90	4.00	3.87	3.90	3.93	(2) (3)
e		1.27			1.27		
h	0.25		0.50	0.425		0.50	
L	0.40		1.27	0.585	0.635	0.685	
k	0		8	2	4	8	DEGREES
ccc			0.10			0.04	

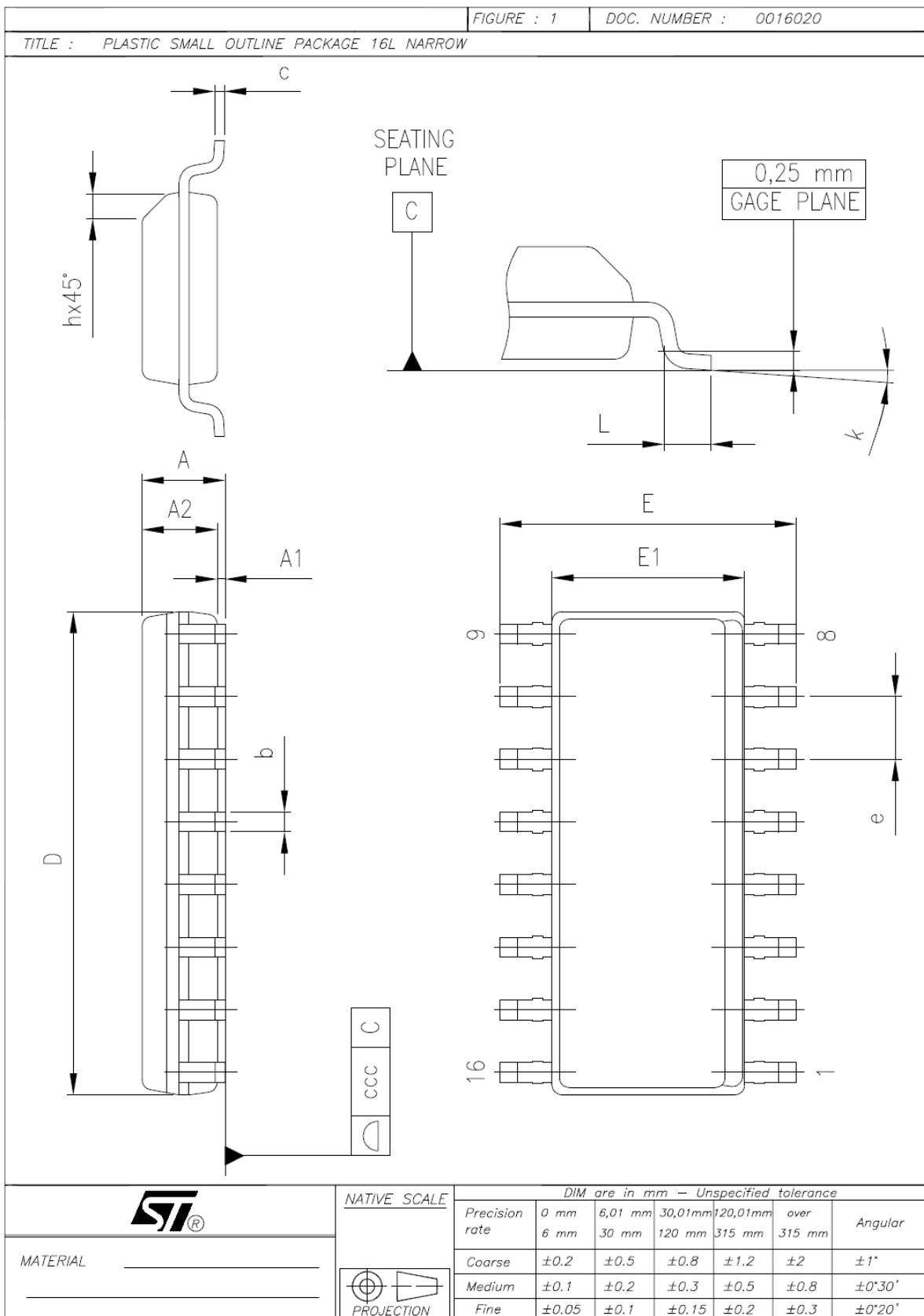
### NOTES:

- (1) – Dimension “D” does not include mold flash, protrusions or gate burrs.  
Mold flash, protrusions or gate burrs shall not exceed 0.15mm in total (both side).
- (2) – Dimension “E1” does not include interlead flash or protrusions.  
Interlead flash or protrusions shall not exceed 0.25mm per side.
- (3) – Dimensions referred to the bottom side of the package

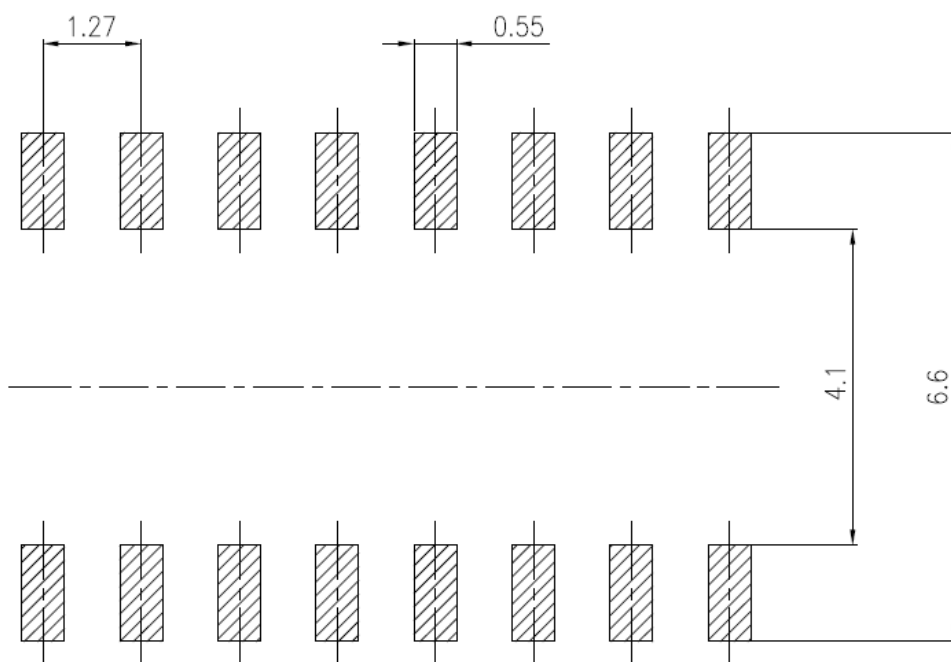




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Report ID **RR000114CT6004**



## RECOMMENDED FOOTPRINT



	NATIVE SCALE   PROJECTION	<i>DIM are in mm – Unspecified tolerance</i>						
		Precision rate	0 mm 6 mm	6,01 mm 30 mm	30,01mm 120 mm	120,01mm 315 mm	over 315 mm	Angular
		Coarse	±0.2	±0.5	±0.8	±1.2	±2	±1°
		Medium	±0.1	±0.2	±0.3	±0.5	±0.8	±0°30'
		Fine	±0.05	±0.1	±0.15	±0.2	±0.3	±0°20'
		MATERIAL _____ _____						



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**Quality & Reliability B-END**

Report ID **RR000114CT6004**

<b>PC</b> Preconditioning	The device is submitted to a typical temperature profile used for surface mounting devices, after a controlled moisture absorption.	As stand-alone test: to investigate the moisture sensitivity level. As preconditioning before other reliability tests: to verify that the surface mounting stress does not impact on the subsequent reliability performance. The typical failure modes are "pop corn" effect and delamination.
<b>AC</b> Auto Clave (Pressure Pot)	The device is stored in saturated steam, at fixed and controlled conditions of pressure and temperature.	To investigate corrosion phenomena affecting die or package materials, related to chemical contamination and package hermeticity.
<b>TC</b> Temperature Cycling	The device is submitted to cycled temperature excursions, between a hot and a cold chamber in air atmosphere.	To investigate failure modes related to the thermo-mechanical stress induced by the different thermal expansion of the materials interacting in the die-package system. Typical failure modes are linked to metal displacement, dielectric cracking, molding compound delamination, wire-bonds failure, die-attach layer degradation.
<b>HTSL</b> High Temperature Storage Life	The device is stored in unbiased condition at the max. temperature allowed by the package materials, sometimes higher than the max. operative temperature.	To investigate the failure mechanisms activated by high temperature, typically wire-bonds solder joint ageing, data retention faults, metal stress-voiding.
<b>THSL</b> Thermal Humidity Storage Life	The THS is performed for the purpose of evaluating the reliability of non-hermetic packaged solid state devices in humidity environments. Test employs temperature and humidity under non –condensed conditions to accelerate the penetration of moisture trough the external protective material and the metallic conductor which pass through it.	This test is used to identify failure mechanism internal to the package and is desctructive.
<b>Die Oriented</b>		
<b>THB</b> Temperature Humidity Bias	The device is biased in static configuration minimizing its internal power dissipation, and stored at controlled conditions of ambient temperature and relative humidity.	To evaluate the package moisture resistance with electrical field applied, both electrolytic and galvanic corrosion are put in evidence.
<b>HTRB</b> High Temperature Reverse Bias	The device is biased in dynamic configuration maximizing its internal reverse power dissipation, and stored at controlled conditions of ambient temperature and relative humidity.	This test is performed to evaluate die problems related with chip stability, layout structure, surface contamination and oxide faults.



## Public Products List

PCN Title : TD310ID: New glue and MSL3 (Moisture Sensitive Level) introduction

PCN Reference : IPG-IPC/14/8676

PCN Created on : 05-SEP-2014

Subject : Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change:

### ST COMMERCIAL PRODUCT

TD310ID

TD310IDT

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