


**PRODUCT / PROCESS CHANGE INFORMATION**

**1. PCI basic data**

1.1 Company		STMicroelectronics International N.V
1.2 PCI No.	MDG/17/10572	
1.3 Title of PCI	New Tape Header finishing procedure for EEPROM in WLCSP	
1.4 Product Category	EEPROM in WLCSP	
1.5 Issue date	2017-11-21	

**2. PCI Team**

<b>2.1 Contact supplier</b>	
2.1.1 Name	ROBERTSON HEATHER
2.1.2 Phone	+1 8475853058
2.1.3 Email	heather.robertson@st.com
<b>2.2 Change responsibility</b>	
2.2.1 Product Manager	Benoit RODRIGUES
2.1.2 Marketing Manager	Hubert LEDUC
2.1.3 Quality Manager	Rita PAVANO

**3. Change**

3.1 Category	3.2 Type of change	3.3 Manufacturing Location
Methods	Process flow chart: Revision change in Process/Recipe like addition, deletion of process step (process technology, sawing, die attach, plasma, marking, packing, labelling, transportation, etc..)	STATSChipPAC subcontractor

**4. Description of change**

	Old	New
4.1 Description	- In the current situation, the header is linked to the protective belt. Then placing (at finishing step) or removing (at customer unreeling step) the protective belt could induce tension to the tape.	With the new procedure, the header is linked to itself and pasted to the protective belt (with the same ESD tape). Then, removing the protective belt will have less tension on the tape.
4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?	No impact	

**5. Reason / motivation for change**

5.1 Motivation	Reduction of potential tight winding of the tape.
5.2 Customer Benefit	QUALITY IMPROVEMENT

**6. Marking of parts / traceability of change**

6.1 Description	N/A
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**7. Timing / schedule**

7.1 Date of qualification results	2017-11-21
7.2 Intended start of delivery	2017-12-11
7.3 Qualification sample available?	Not Applicable

**8. Qualification / Validation**

8.1 Description			
8.2 Qualification report and qualification results	In progress	Issue Date	

9. Attachments (additional documentations)
10572 Public product.pdf 10572 PCI End of Tape WLCSP WITHOUT EPE- ALL CUSTOMERS -5-.pdf

10. Affected parts		
10. 1 Current		10.2 New (if applicable)
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	M24C08-FCT6TP/T	
	M24C32-FCU6TP/TF	
	M24C64-FCU6TP/TF	
	M24C64M-FCU6T/TF	
	M24C64T-FCU6T/TF	

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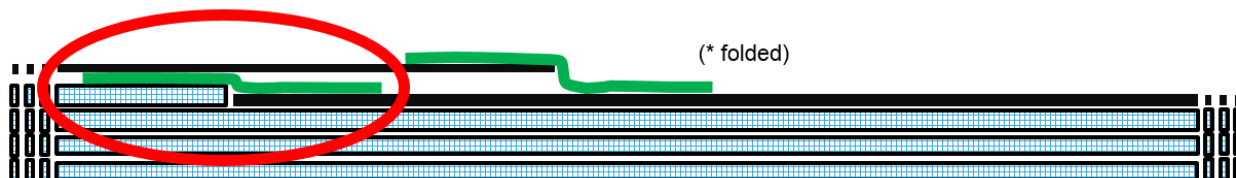
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## New Tape Header finishing procedure for EEPROM in WLCSP

### What is the change?

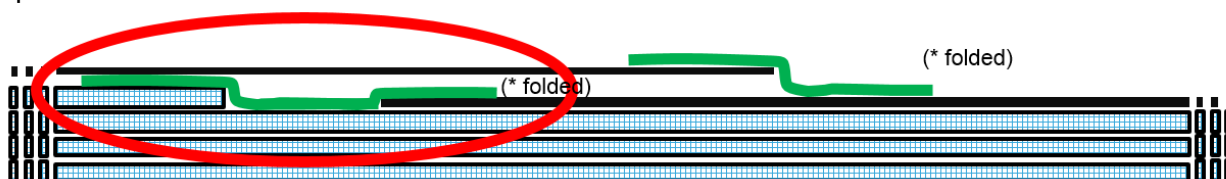
ST is modifying the Tape Header finishing procedure of its WLCSP assembled at STATSChipPAC (Singapore):




- In the **current situation**, the **header is linked to the protective belt**. Then placing (at finishing step) or removing (at customer unreeling step) the protective belt could induce tension to the tape.



 Protective Belt  
 Carrier Tape  
 ESD Tape (\* folded)

- With the **new procedure**, the **header is linked to itself and pasted to the protective belt** (with the same ESD tape). Then, removing the protective belt will have less tension on the tape.



 Protective Belt  
 Carrier Tape  
 ESD Tape (\* folded)

**Why?**

With the **new procedure**, placing or removing the protective belt will avoid potential tension to the tape at STATChipPAC finishing or at unreeling step at customer.

**When?**

New procedure for tape header finishing for all EEPROM WLCSP products will start from beginning of December 2017.

**How will the change be qualified?**

The **qualification** of this **new procedure** has been performed using the standard ST Microelectronics Corporate Procedures for Quality & Reliability and the Best Known Method (BKM) defined for WLCSP finishing.

**What is the impact of the change?**

- **Form, Fit, Function:** No change

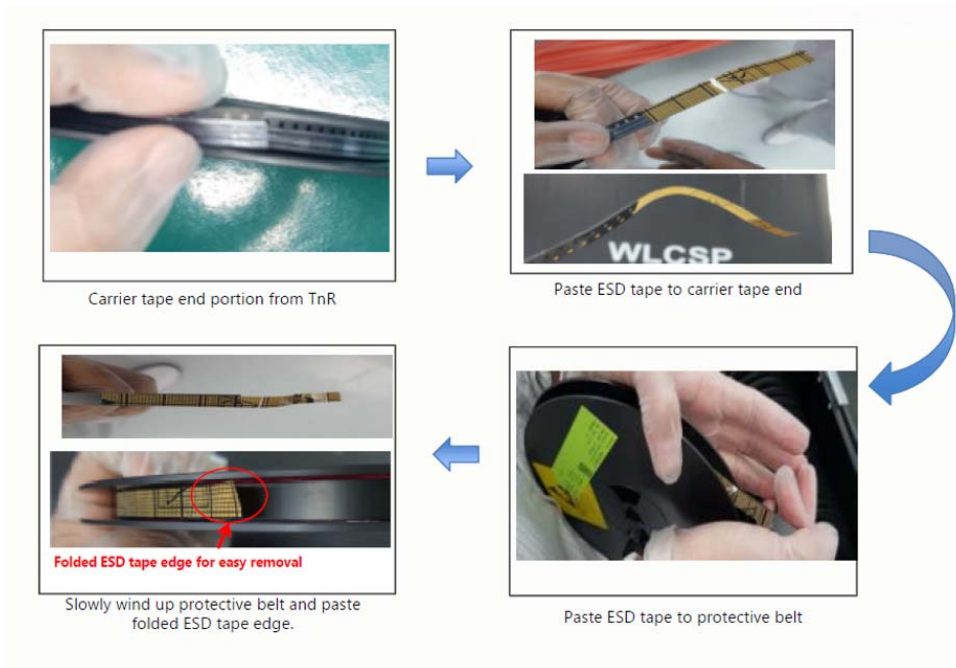
## **Appendix A: Product Change Information**

<b>Product family / Commercial products:</b>	EEPROM WLCSP
<b>Customer(s):</b>	All
<b>Type of change:</b>	New procedure for End of Tape (header) finishing
<b>Reason for the change:</b>	Quality improvement
<b>Description of the change:</b>	New “End of Tape” procedure for EEPROM WLCSP finishing at STATSChipPAC (Singapore)
<b>Forecast date of the change: (Notification to customer)</b>	Week 47 / 2017
<b>Forecast date of <u>Qualification samples</u> availability for customer(s):</b>	N/A
<b>Product Line(s) and/or Part Number(s):</b>	See APPENDIX B
<b>Estimated date of first shipment:</b>	Week 50 / 2017

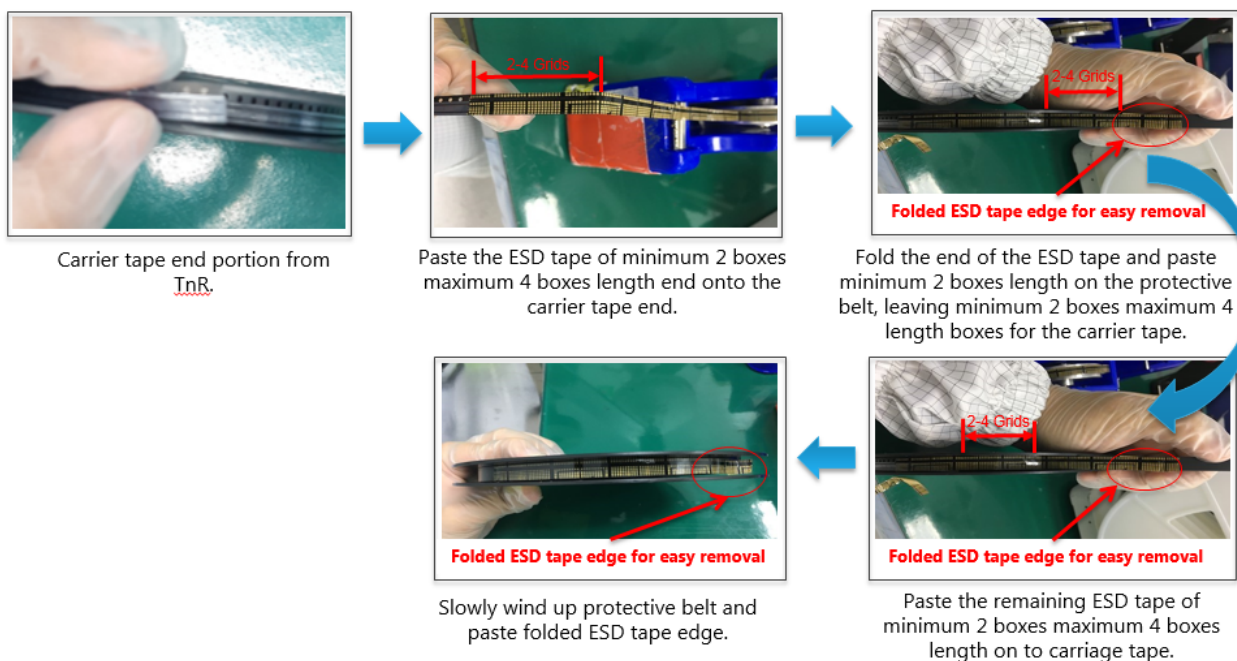
**Appendix B: Concerned Commercial Part Numbers:**

M24C64T-FCU6T/TF
M24C64-FCU6TP/TF
M24C64M-FCU6T/TF
M24C32-FCU6TP/TF
M24C32-FCU6TP/T
M24C08-FCT6TP/T

## New Tape Header finishing procedure for EEPROM in WLCSP



## CURRENT PROCEDURE FOR REELING



## NEW PROCEDURE FOR REELING



[illegible]

Source Documents & Reference Documents		
Source document Title	Rev.:	Date: