



Title of Change:	NCV7428 Datasheet Update – Revision 3					
Effective date:	27 August 2015					
Contact information:	Contact your local ON Semiconductor Sales Office or Roman Buzas <roman.buzas@onsemi.com>					
Type of notification:	ON Semiconductor will consider this change accepted.					
Change category:	☐ Wafer Fab Change ☐ Assembly Change ☐ Test Change ☒ Other _____					
Change Sub-Category(s):			☒ Datasheet/Product Doc change ☐ Shipping/Packaging/Marking ☐ Other: _____			
☐ Manufacturing Site Change/Addition ☐ Material Change ☐ Manufacturing Process Change ☐ Product specific change						
Sites Affected:						
☐ All site(s) ☒ not applicable ☐ ON Semiconductor site(s) : ☐ External Foundry/Subcon site(s)						
Description and Purpose:						
Electrical DC Characteristics updated: • Specification for VOUT regulator output voltage during exposure to RF electro-magnetic disturbances with no bus capacitance added.						
Table 6. DC CHARACTERISTICS						
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{OUT} REGULATOR						
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{OUT_5_EMC}	VOUT regulator output voltage under EMC (Note 6)	DPI EMC test applied to LIN pin. No bus capacitor. SOIC8 package; (Note 5)	4.85	5	5.15	V
V _{OUT_33_EMC}	VOUT regulator output voltage under EMC (Note 6)	DPI EMC test applied to LIN pin. No bus capacitor. SOIC8 package; (Note 5)	3.201	3.3	3.399	V
4. In case LIN bus capacitor of at least 82 pF is not used VOUT_5_EMC and VOUT_33_EMC needs to be taken into account. 5. Tested according to: LIN Conformance Test Specification Package for LIN 2.1, October 10th, 2008. Verified by external test house. 6. Values based on design and characterization. Not tested in production.						
List of Affected Standard Parts:						
NCV7428D15R2G NCV7428D13R2G NCV7428D1L5R2G NCV7428D1L3R2G NCV7428MW5R2G NCV7428MW3R2G NCV7428MWL5R2G NCV7428MWL3R2G						