



## Product Change Notification - SYST-27YUSU409

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**Date:**

28 Jan 2020

**Product Category:**

Microprocessors

**Affected CPNs:****Notification subject:**

Data Sheet - SAMA5D2 MPU SiP with up to 1Gbit DDR2 or 2Gbit LPDDR2

**Notification text:**

SYST-27YUSU409

Microchip has released a new Product Documents for the SAMA5D2 MPU SiP with up to 1Gbit DDR2 or 2Gbit LPDDR2 of devices. If you are using one of these devices please read the document located at [SAMA5D2 MPU SiP with up to 1Gbit DDR2 or 2Gbit LPDDR2](#).

**Notification Status:** Final

**Description of Change:**

- 1) Updated memory references in Reference Documents
- 2) Updated burst, write, read latencies in LPDDR2-SDRAM Features
- 3) Added 8.1 Recommended Thermal Operating Conditions in Electrical Characteristics

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 28 Jan 2020

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

**Attachment(s):**

[SAMA5D2 MPU SiP with up to 1Gbit DDR2 or 2Gbit LPDDR2](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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